

A

A

B

B

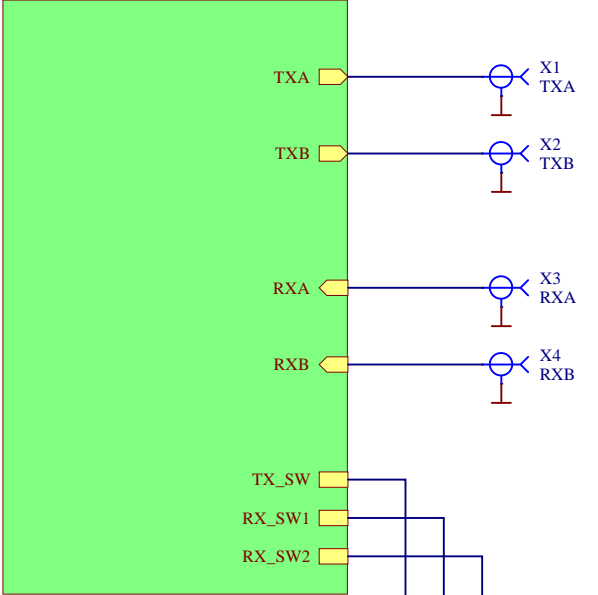
C

C

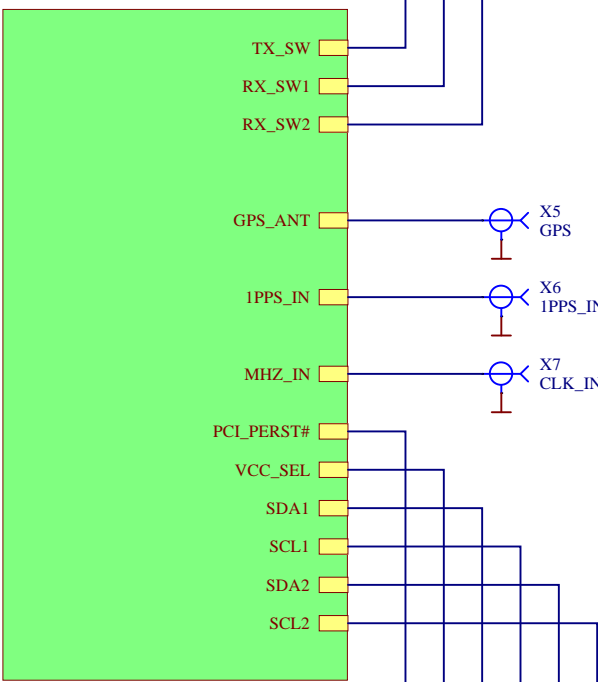
D

D

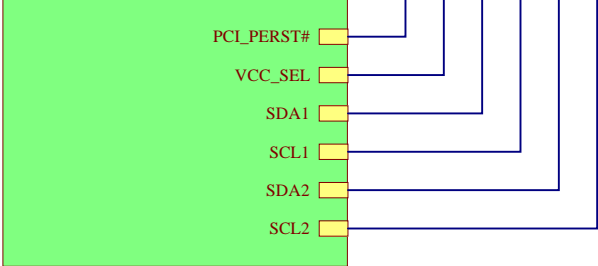
LMS7_FE.SchDoc



FPGA.SchDoc



PwrAndBias.SchDoc



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|----------------------------------|--------------|--------------------------|--|
| Title | | | |
| XTRX. SDR mini PCIe Transceiver. | | | |
| Size | Number | Revision | |
| A4 | Total.SchDoc | 5 | |
| Date: | | Var 3_3V_I | |
| File: | | 06.08.2019 Fairwaves Inc | |
| Sheet | | 2 of 5 | |
| File: | | C:\Git\...\Total.SchDoc | |
| Drawn By: | | Andrey Sviyazov | |

