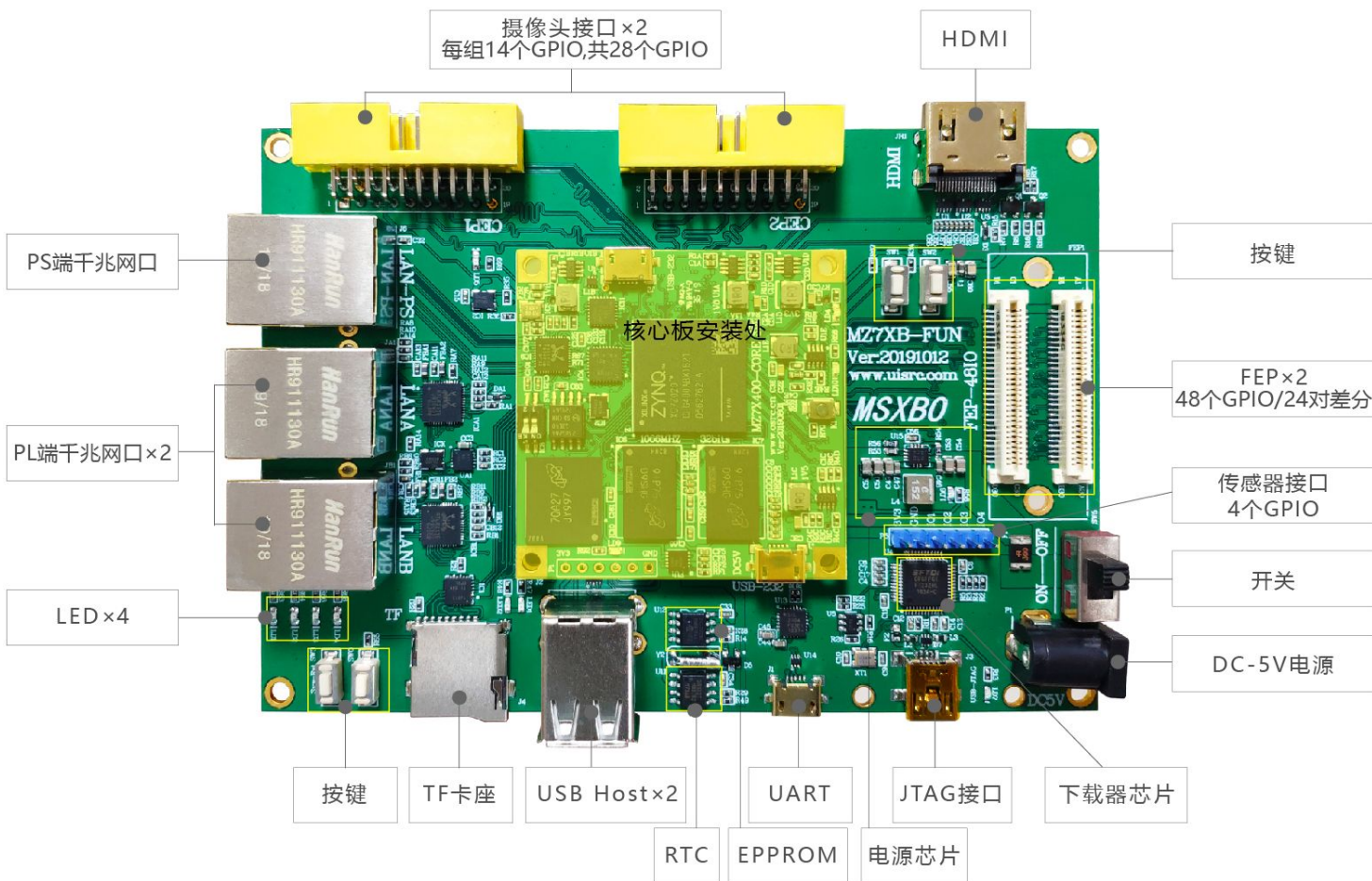


常州一二三电子科技有限公司/溧阳米联电子科技有限公司

(原南京米联电子科技有限公司)

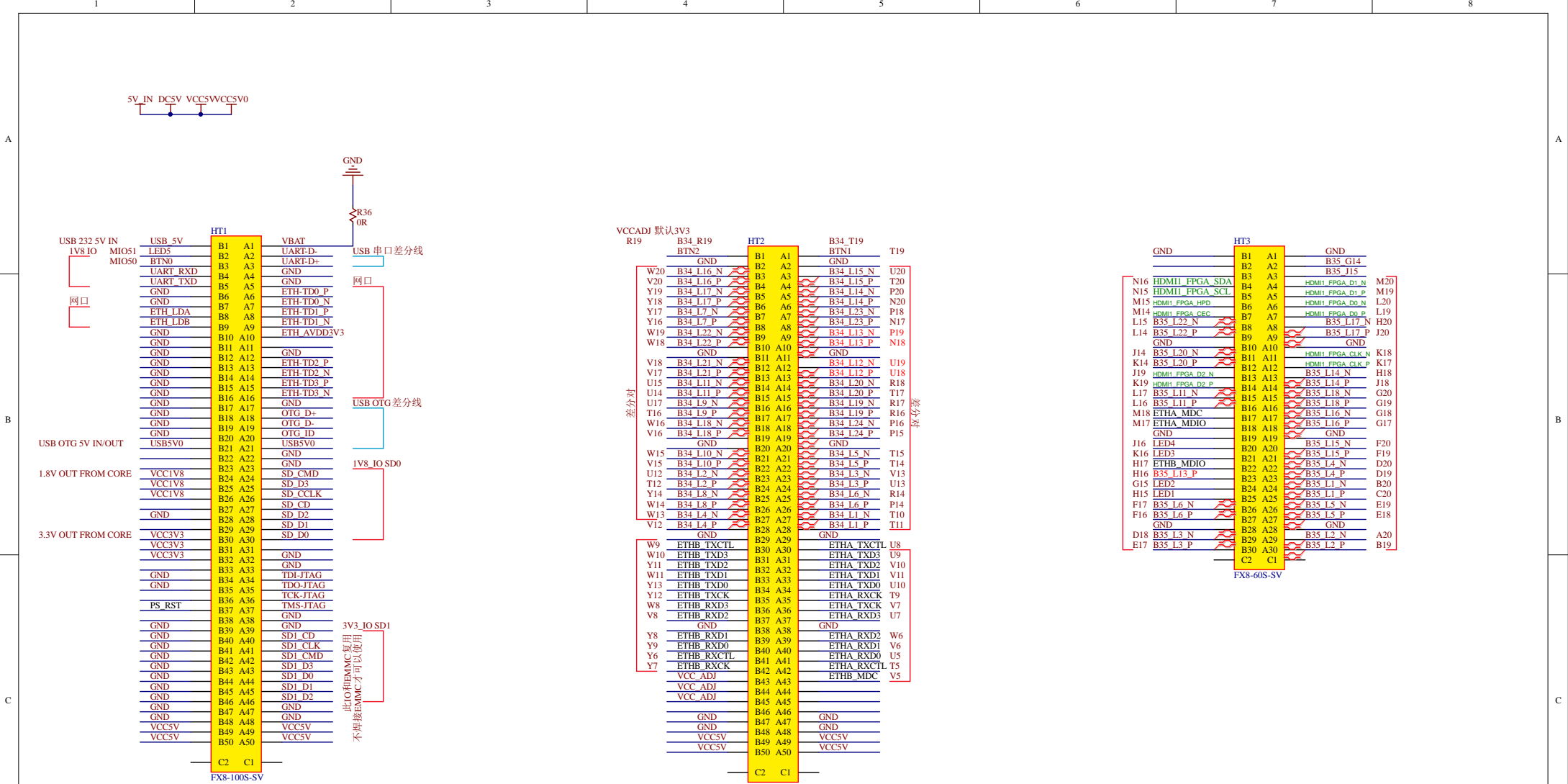
Num	PAGE
01	01_TOP.schdoc
02	02_connector.schdoc
03	03_UART_232.schdoc
04	04_USB_HOST.schdoc
05	05_USB_JTAG.schdoc
06	06_TF_CARD.schdoc
07	07_PS_ETH_RJ45.schdoc
08	08_PL_ETH_1.schdoc
09	09_PL_ETH_2.schdoc
10	10_HDMI_OUT.schdoc
11	11_BTN_LED_GPIO.schdoc
12	12_PLCLK_100M.schdoc
13	13_CEP.schdoc
14	14_FEP.schdoc
15	15_PMU.schdoc
16	16_I2C_EEPROM_RTC.schdoc
17	
18	
19	



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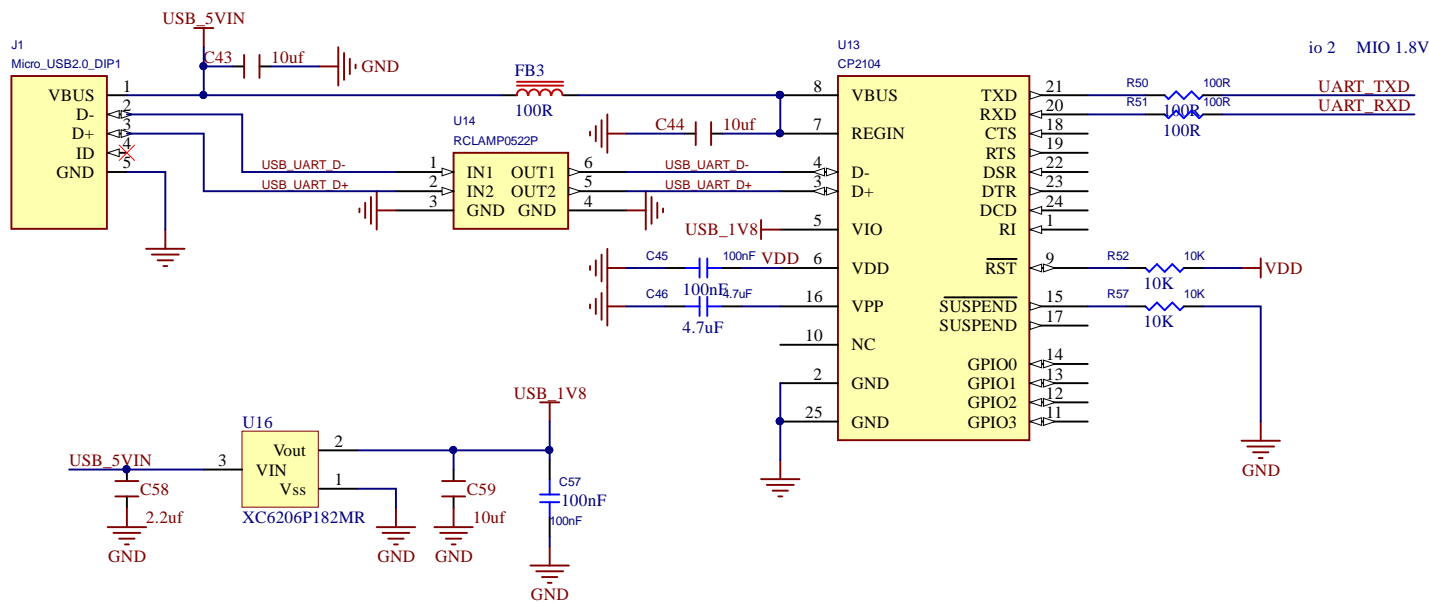
MSXBO

TechWeb:	www.uisrc.com		
ProjectName:	MZ7XB_Fun.PrjPCB		
Page Title:	01_TOP.SchDoc	Rev:	20191012
Date:	2019/12/2 - 15:38:24	Sheet 1 of 16	



VCCSV 通过底板连接器给核心板供电

差分对命名: 比如B13_L19_P,B13_L19_N代表了 BANK13上的L19对差分对
FPGA的PIN号: 为字母+数字 比如 W20 就是FPGA的IO 号



当底板和核心板共享串口芯片

- 1)、USB_5VIN给核心板串口芯片使用
- 2)、UART-D-/UART-D+为USB串口芯片USB的差分数据线

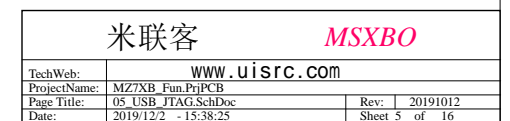
当底板单独使用串口芯片(推荐)

- 1)、UART_RXD/UART_TXD为ZYNQ的串口信号，接到底板的串口芯片
- 2)、去掉核心板上连接到核心板串口芯片的RX TX电阻，分别为R66,R69

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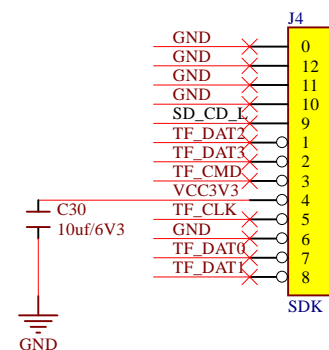
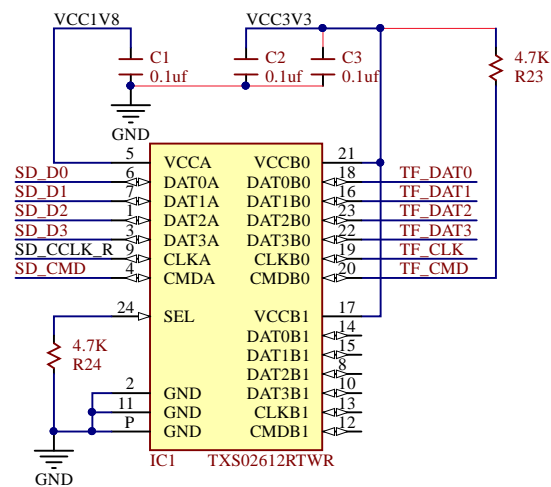
MSXBO

TechWeb:	www.uisrc.com		
ProjectName:	MZ7XB_Fun.PrjPCB		
Page Title:	03_UART_232.SchDoc	Rev:	20191012
Date:	2019/12/2 - 15:38:25	Sheet 3 of 16	



SD_CD 150 R20 SD_CD_L

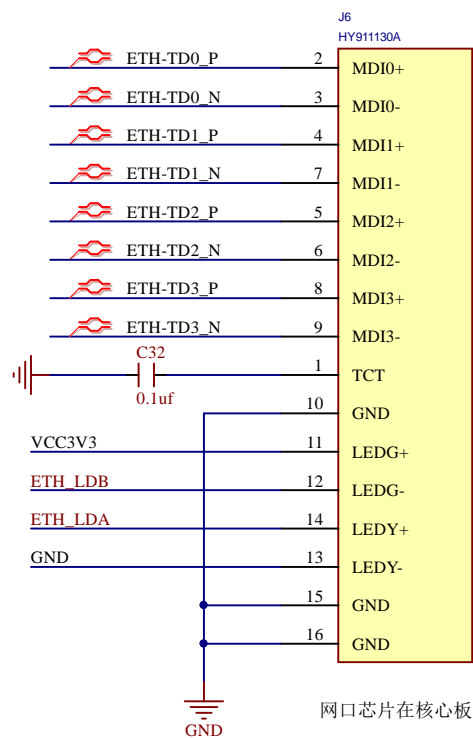
SD_CCLK R21 49.9 SD_CCLK_R



米联客

MSXBO

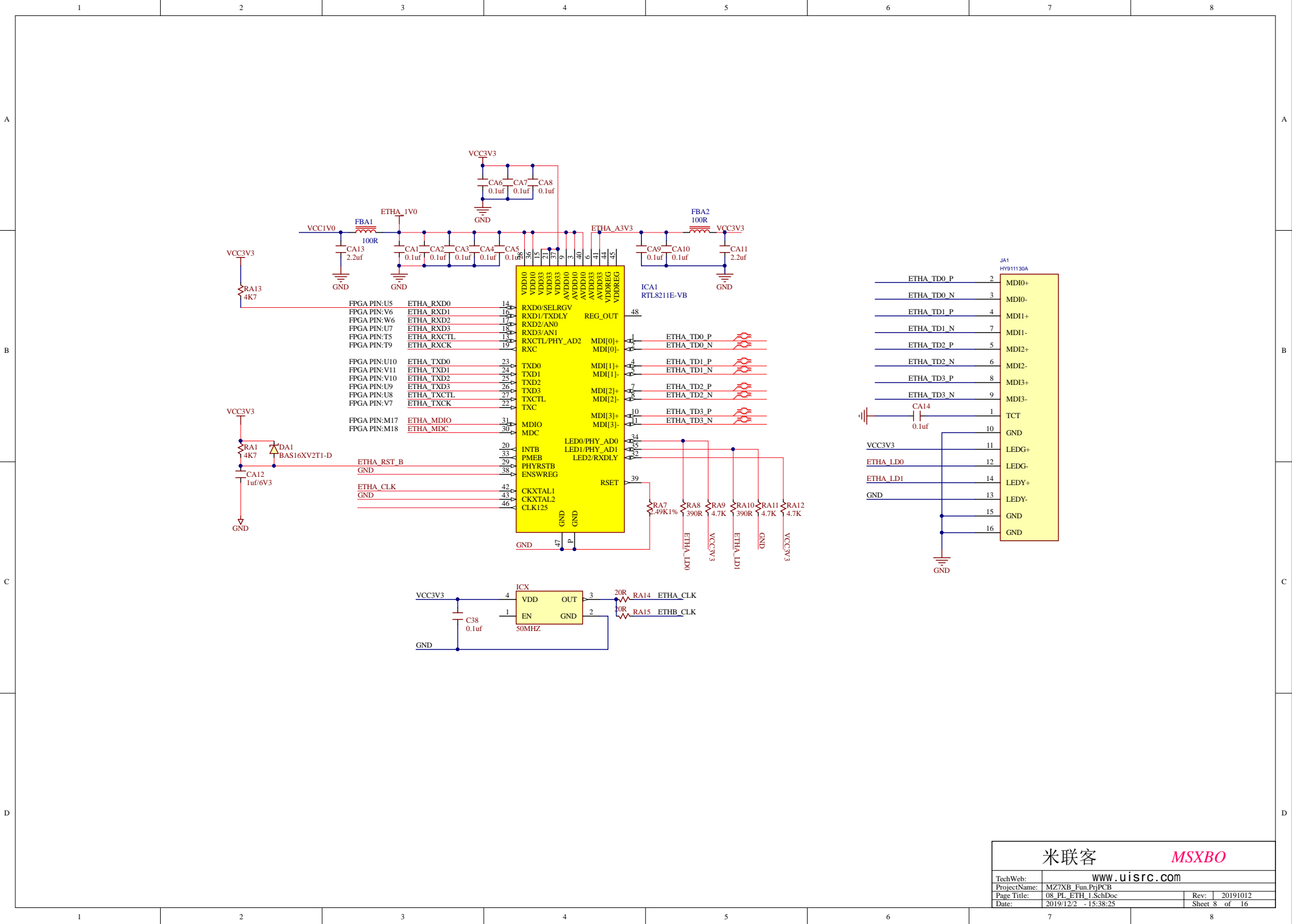
TechWeb:	www.uisrc.com		
ProjectName:	MZ7XB_Fun.PrjPCB		
Page Title:	06_TF_CARD.SchDoc	Rev:	20191012
Date:	2019/12/2 - 15:38:25	Sheet 6 of 16	

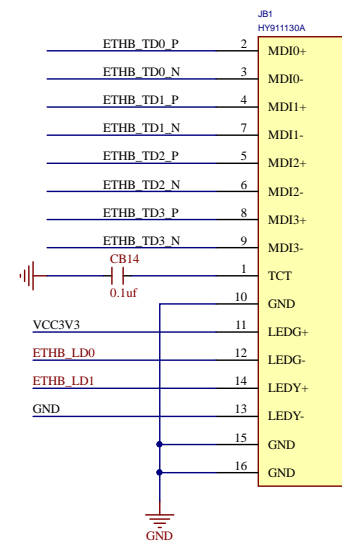
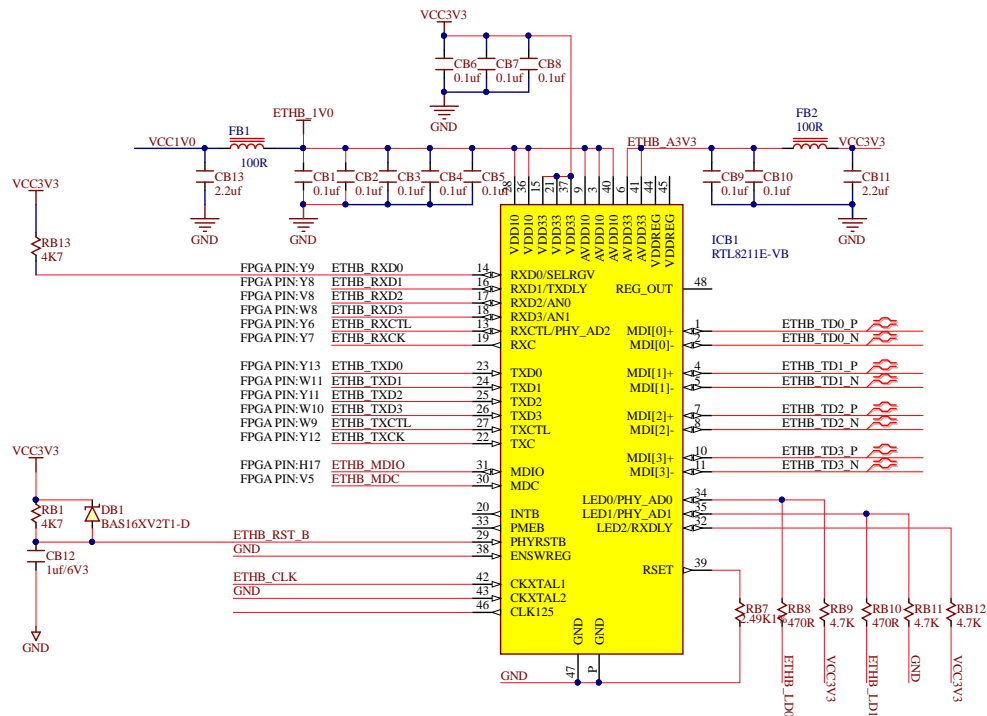


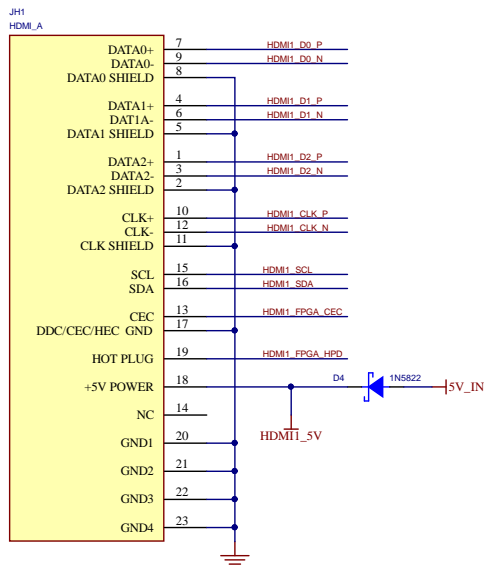
米联客

MSXBO

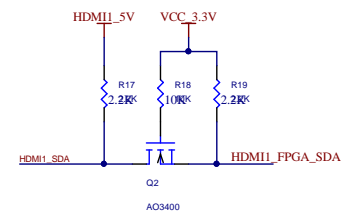
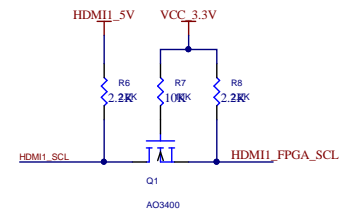
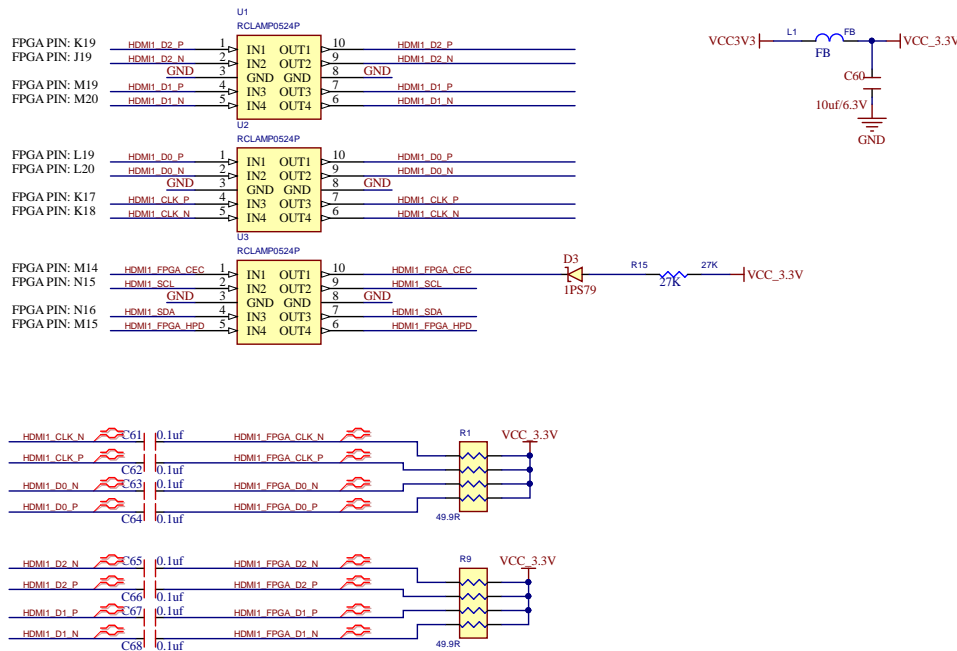
TechWeb:	www.uisrc.com		
ProjectName:	MZ7XB_Fun.PrjPCB		
Page Title:	07_PS_ETH_RJ45.SchDoc	Rev:	20191012
Date:	2019/12/2 - 15:38:25	Sheet 7	of 16

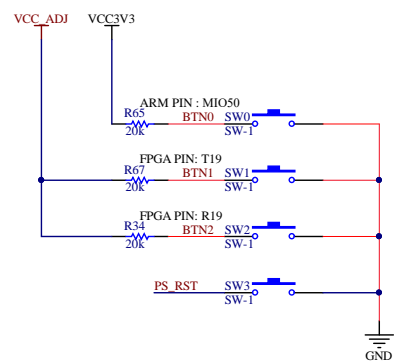




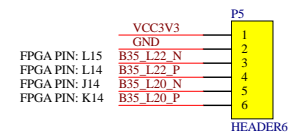
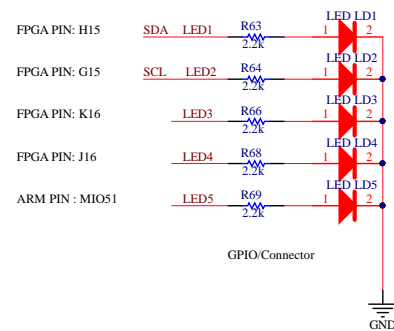


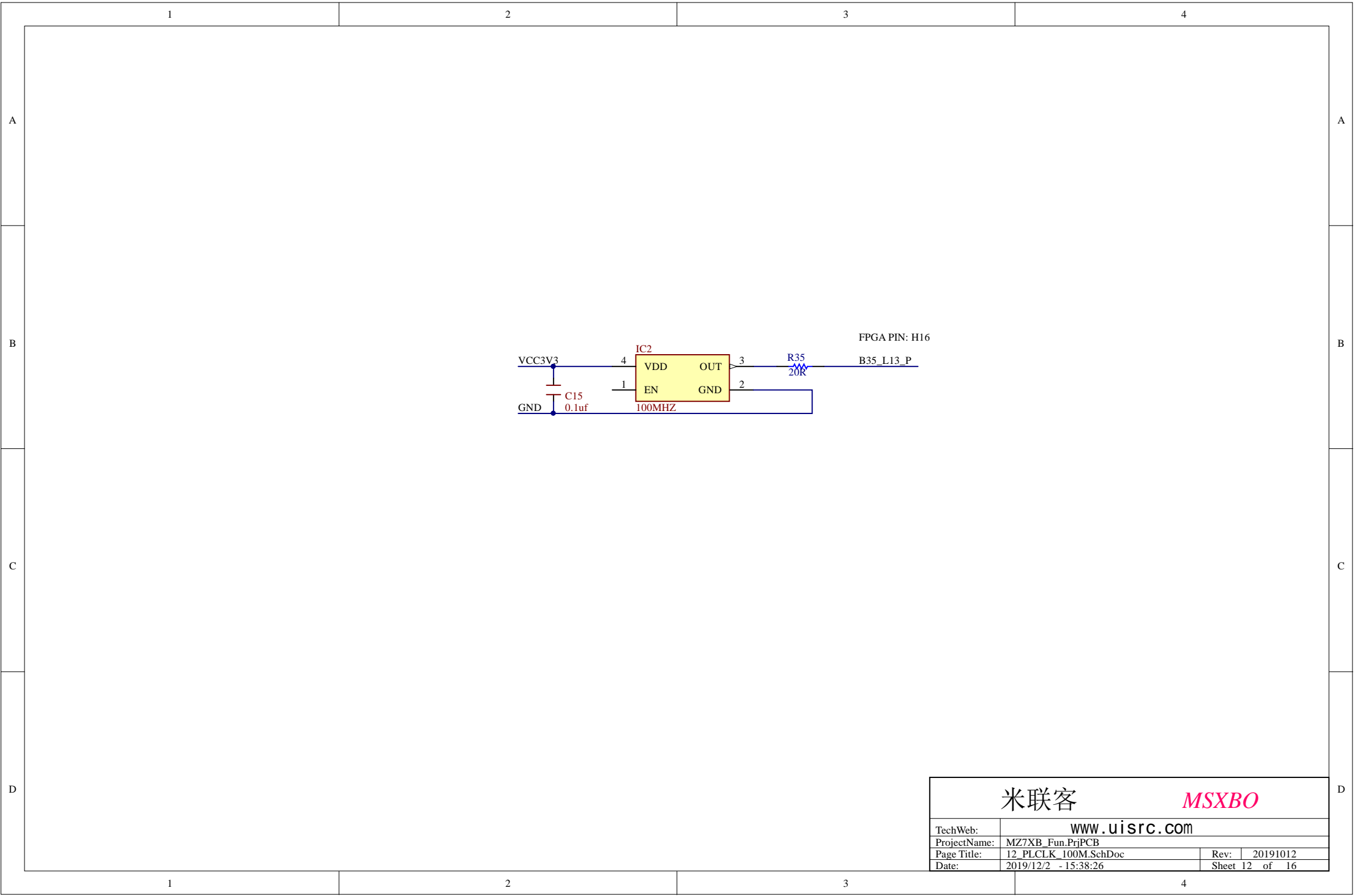
利用二极管的单向导电设计HDMI供电可以往外输出或者输入





SDA与LED1共用IO， SCL与LED2共用IO





A

B

C

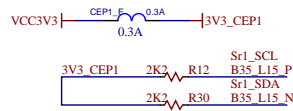
D

A

B

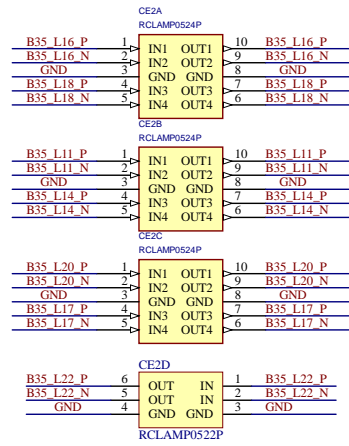
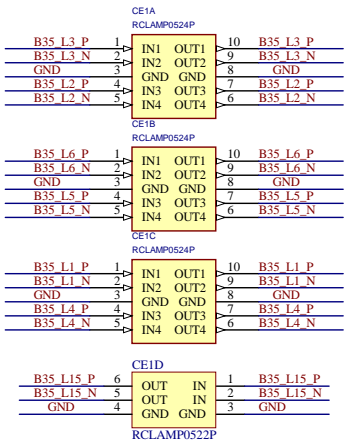
C

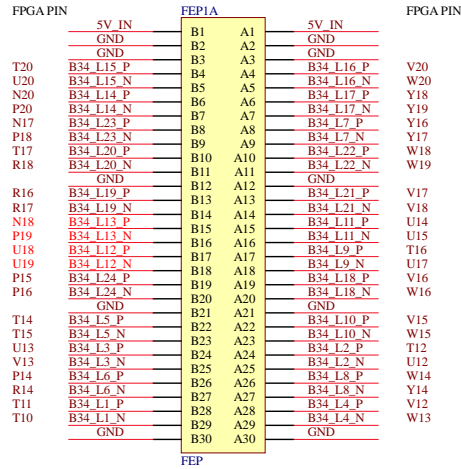
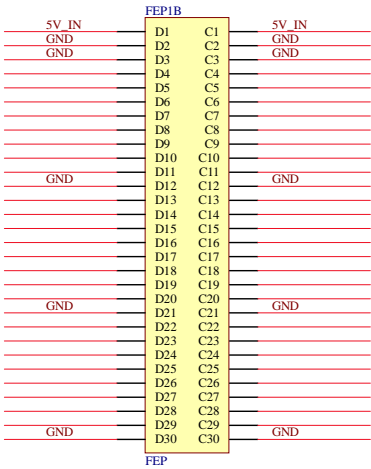
D



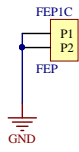
FPGA PIN		3V3_CEP1	CEP1		GND		FPGA PIN
F20	Sr1_SCL	B35_L15_N	20	19	B35_L15_P	Sr1_SDA	F19
D20	Sr1_SYNC	B35_L4_N	18	17	B35_L1_N	Sr1_HREF	B20
D19	Sr1_PCLK	B35_L4_P	16	15	B35_L1_P	Sr1_XCLK	C20
E19	Sr1_D7	B35_L5_N	14	13	B35_L6_N	Sr1_D6	F17
E18	Sr1_D5	B35_L5_P	12	11	B35_L6_P	Sr1_D4	F16
A20	Sr1_D3	B35_L2_N	10	9	B35_L3_N	Sr1_D2	D18
B19	Sr1_D1	B35_L2_P	8	7	B35_L3_P	Sr1_D0	E17
		GND	6	5			
			4	3			
			2	1			

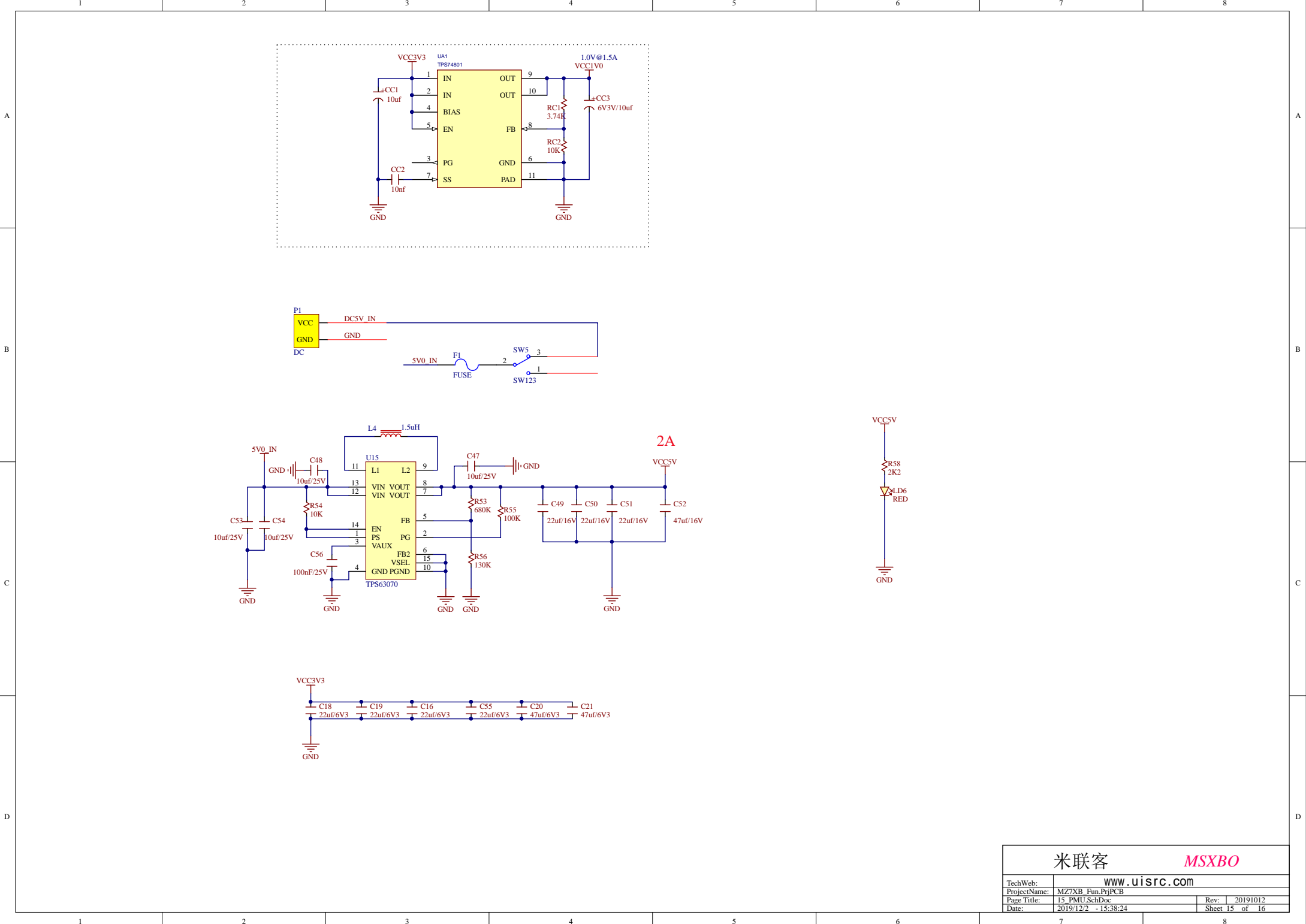
FPGA PIN		3V3_CEP2	CEP2		GND		FPGA PIN
L14	Sr2_SCL	B35_L22_P	20	19	B35_L22_N	Sr2_SDA	L15
H20	Sr2_SYNC	B35_L17_N	18	17	B35_L20_N	Sr2_HREF	J14
J20	Sr2_PCLK	B35_L17_P	16	15	B35_L20_P	Sr2_XCLK	K14
H18	Sr2_D7	B35_L14_N	14	13	B35_L11_N	Sr2_D6	L17
J18	Sr2_D5	B35_L14_P	12	11	B35_L11_P	Sr2_D4	L16
G20	Sr2_D3	B35_L18_N	10	9	B35_L16_N	Sr2_D2	G18
G19	Sr2_D1	B35_L18_P	8	7	B35_L16_P	Sr2_D0	G17
		GND	6	5			
			4	3			
			2	1			

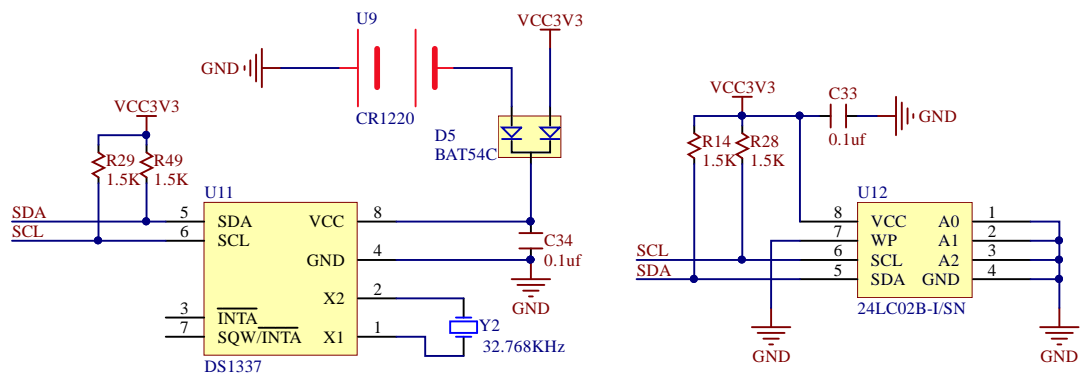




BANK34为ADJ电压BANK 改核心板电阻可以调整电压，默认ADJ=3.3V
差分对命名：比如B34_L1_P,B34_L1_N代表了 BANK34上的L1对差分对
FPGA的PIN号：为字母+数字 比如 W13 就是FPGA的IO 号







SDA与LED1共用IO，SCL与LED2共用IO

米联客

MSXBO

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ProjectName:	MZ7XB_Fun.PrjPCB		
Page Title:	16_I2C_EEPROM_RTC.SchDoc	Rev:	20191012
Date:	2019/12/2 - 15:38:26	Sheet 16 of 16	