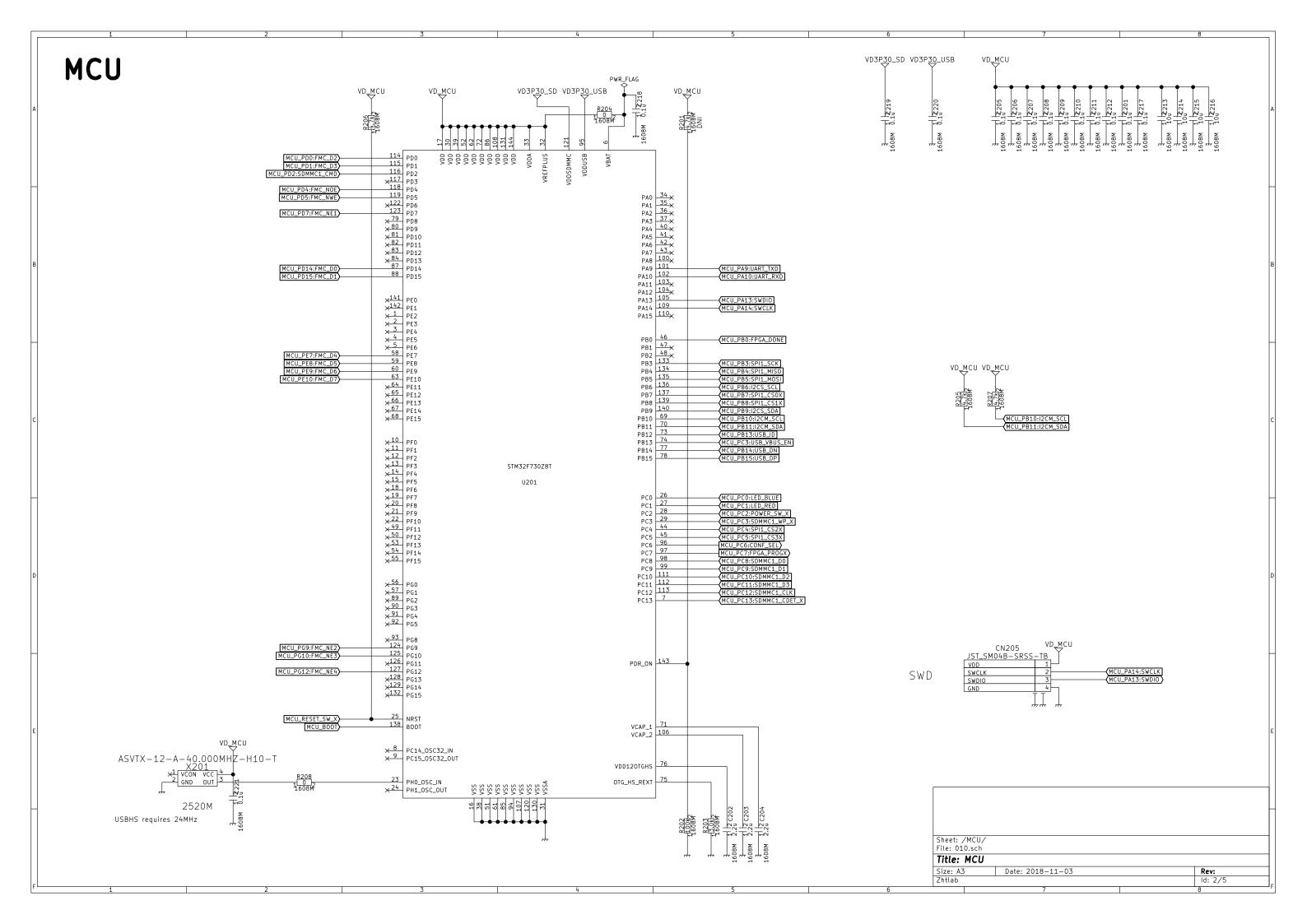
FPGACONF

MCU
010.sch

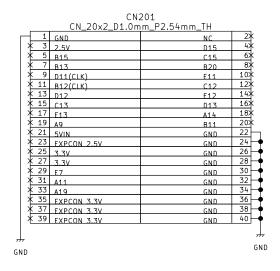
ECP5 CN
20.sch

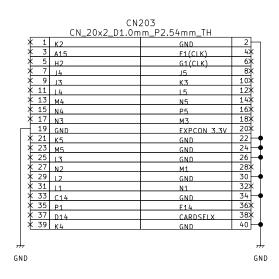
FX3 CN
021.sch

SPI FLASH, CS CONTROL



ECP5 CN





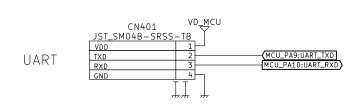


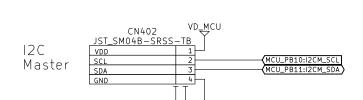


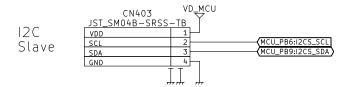
HOLE_01×1_D3.4mm_P0.00mm_NTH

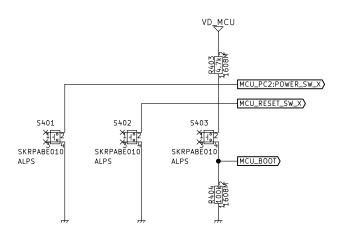
Zhtlab 6



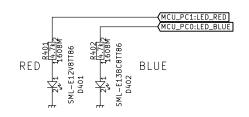


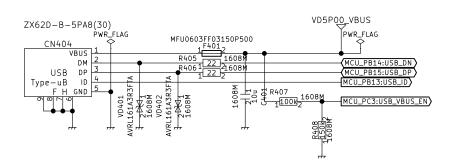


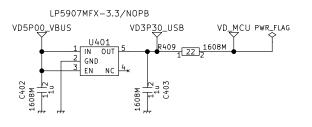




POWER SW RESET SW BOOT SW

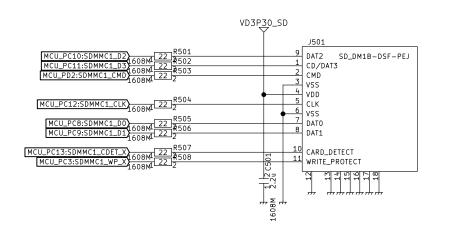


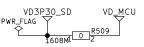




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Zhtlab			ld: 4/5	

SD CN, FPGA CONFIG CN







PARALLEL CONFIG



SPI CONFIG

Sheet: /SPI FLASH, CS CONTROL/							
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Title:							
Size: A3		Date:			Rev:		
Zhtlab	Zhtlab				ld: 5/5		
		7			<u> </u>		