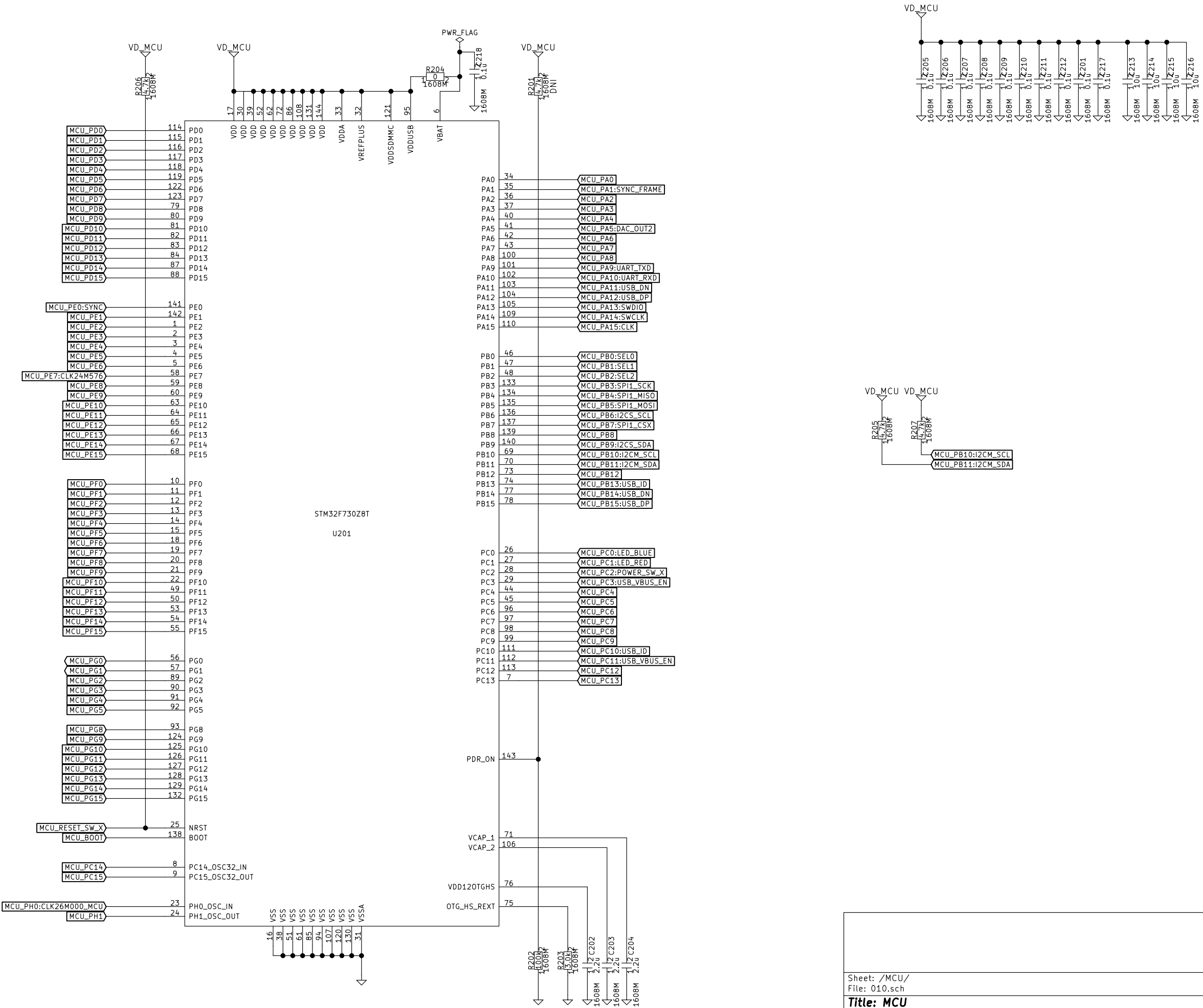


1	2	3	4	5	6	7	8	
A	USBSOFIC—01							
B	<div>MCU</div> <div>010.sch</div> <div>CLK, PLL, CN</div> <div>011.sch</div> <div>POWER</div> <div>012.sch</div>							
C	<div>Todo:</div> <div>xx</div>							
D								
E								
F	1	2	3	4	5	6	7	8

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Zhtlab	Id: 1/4	

MCU

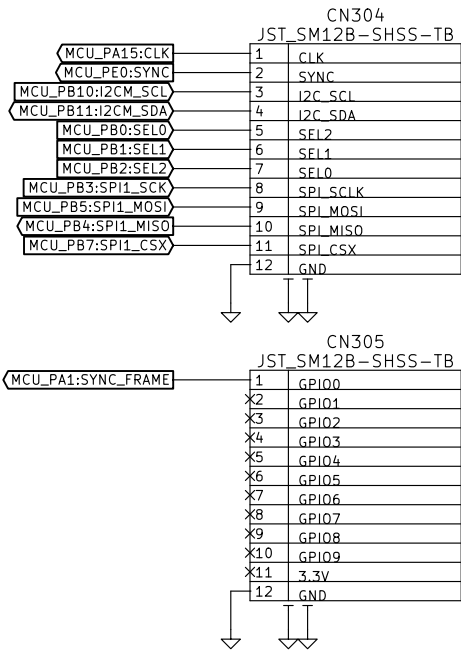
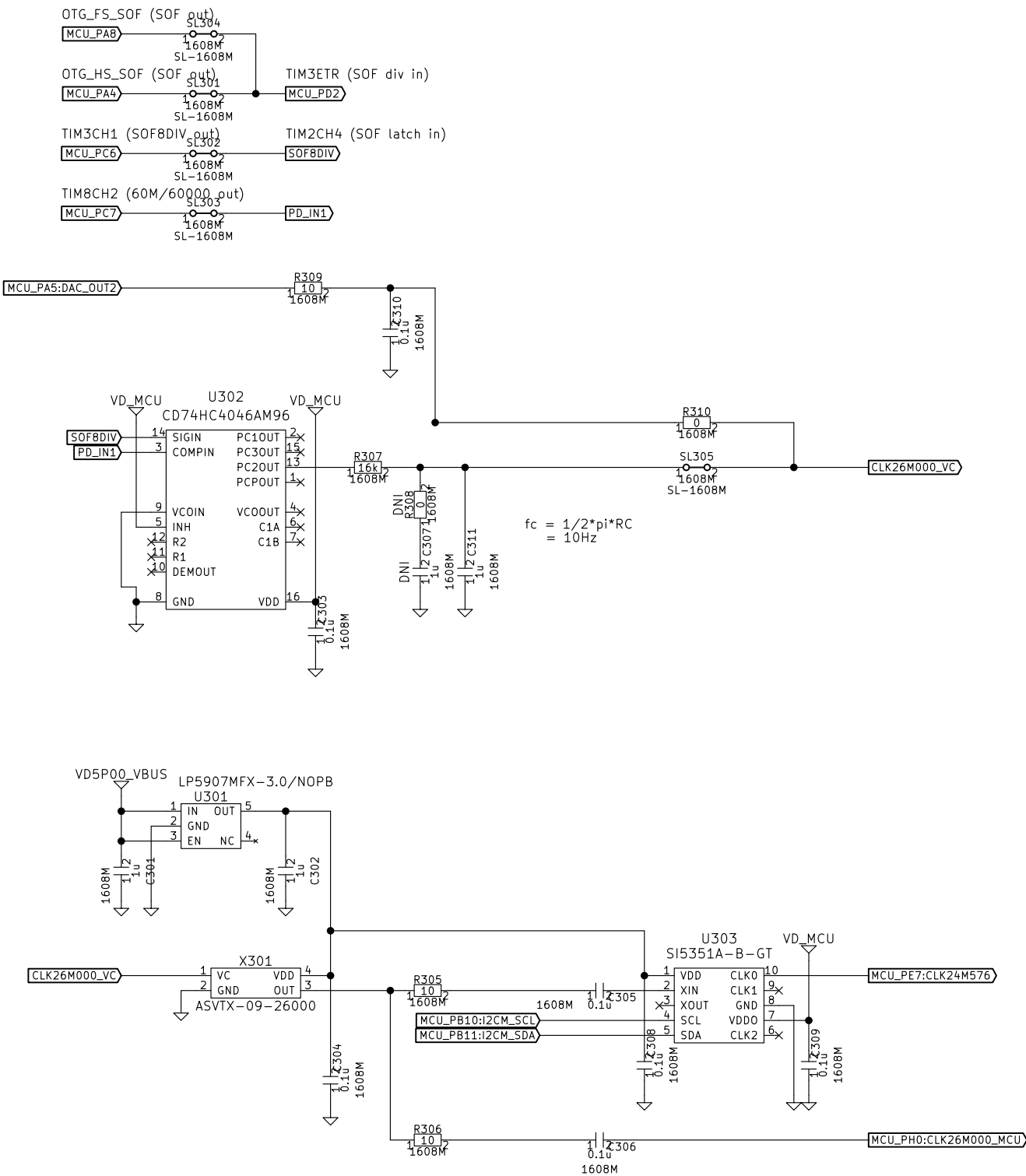
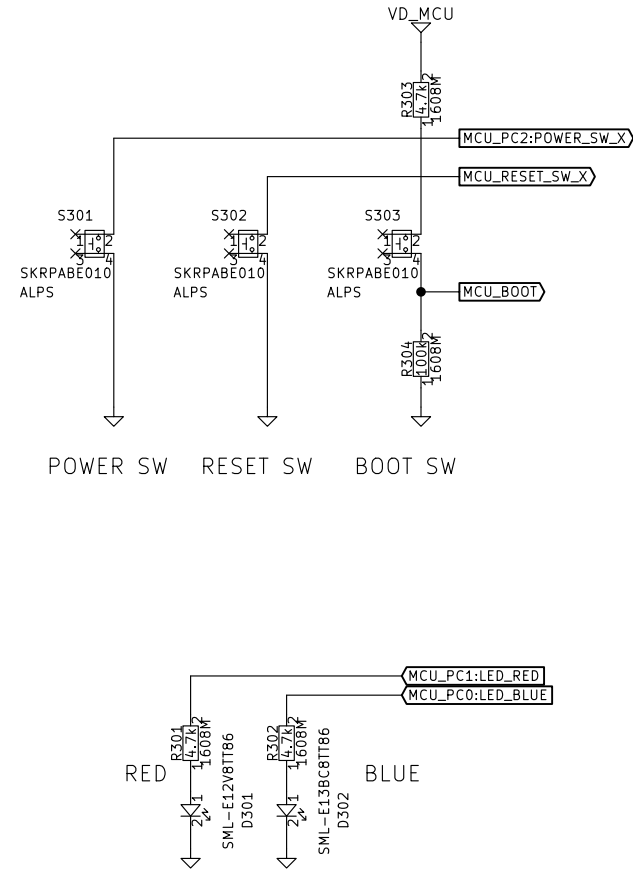


CLK, PLL, CN

UART

I2C Master

I2C Slave



[illegible]