

FPGACONF

MCU
010.sch

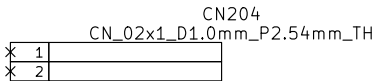
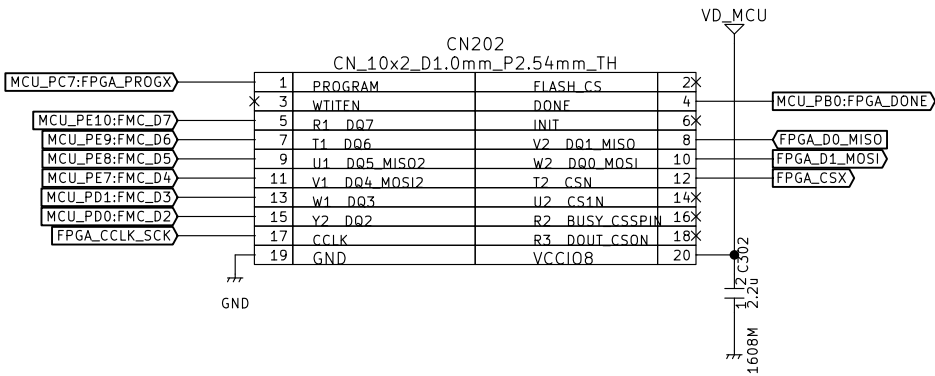
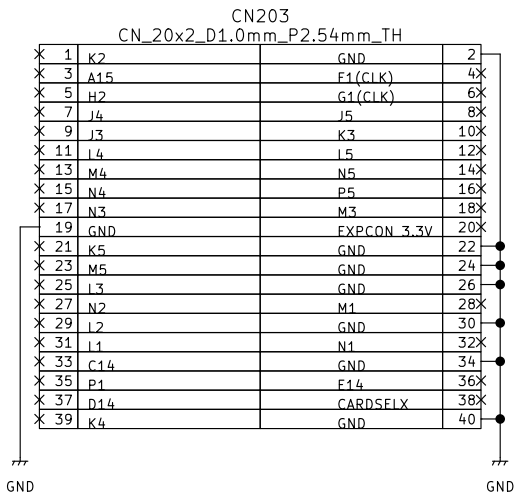
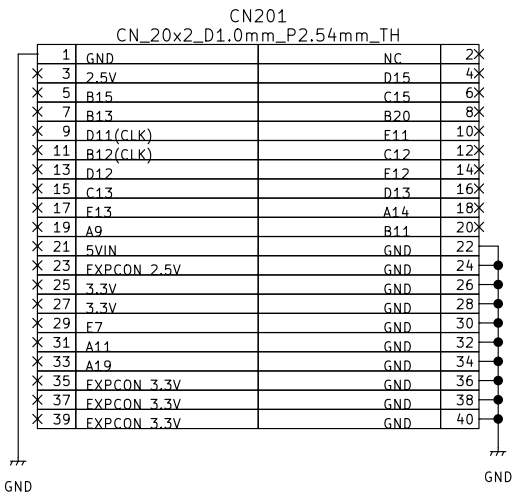
ECP5_CN
20.sch

EX3_CN
021.sch

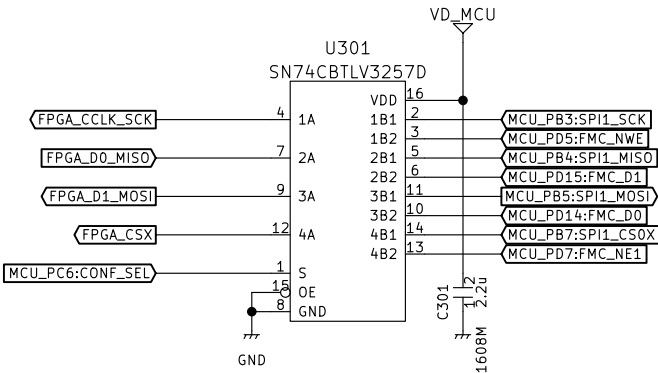
SPI_FLASH_CS_CONTROL
022.sch

Sheet: / File: FPGACONF.sch		
Title:		
Size: A3	Date:	Rev:
Zhtlab	Id: 1/5	

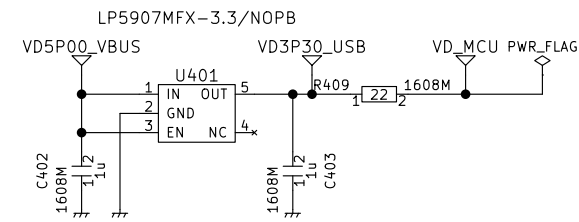
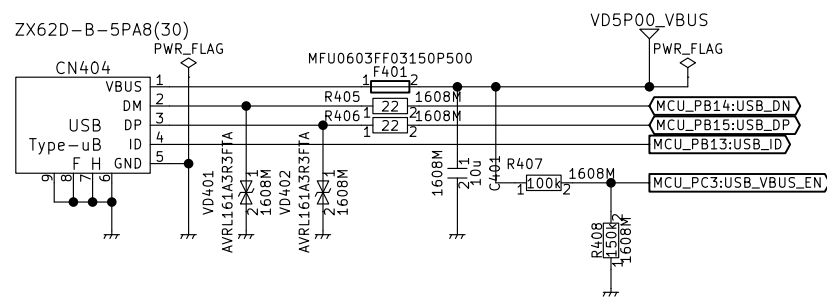
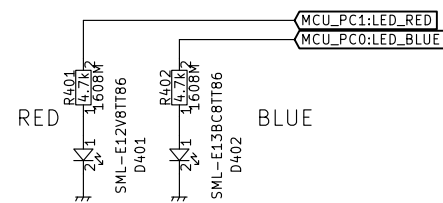
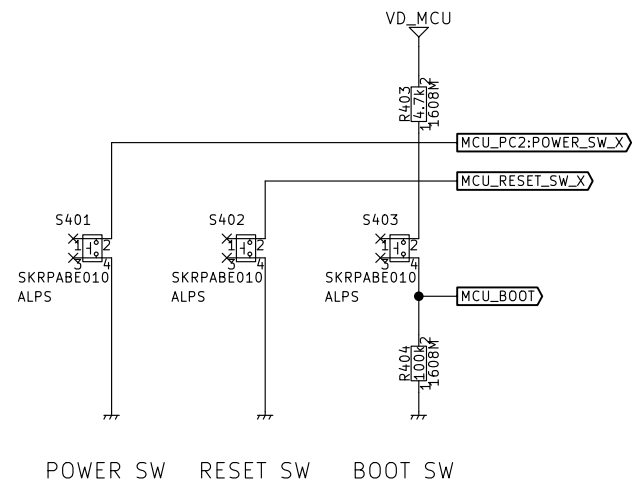
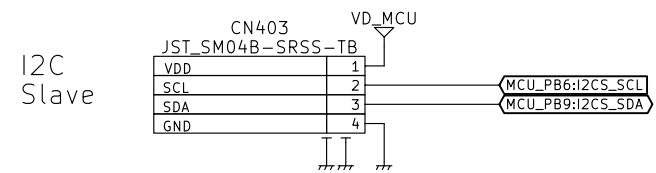
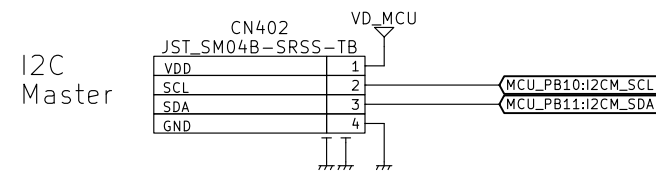
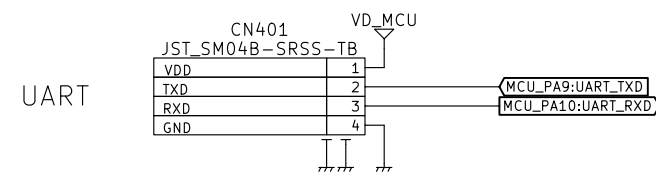
ECP5 CN



HOLE_01x1_D3.4mm_P0.00mm_NTH



I/O CN



SD CN, FPGA CONFIG CN

