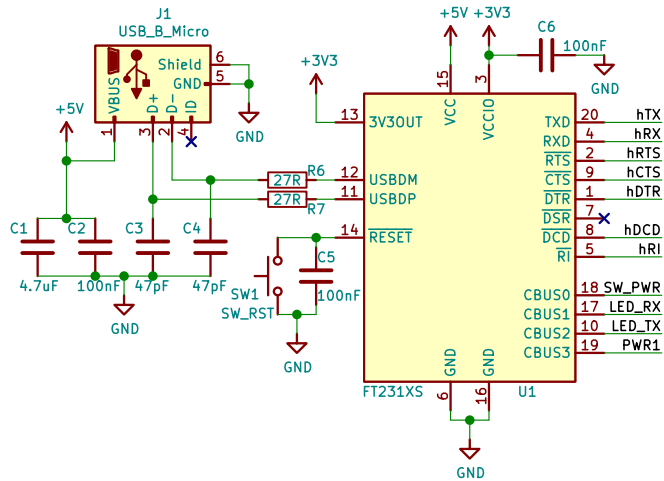
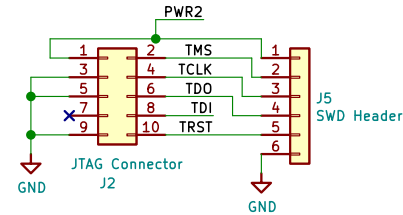


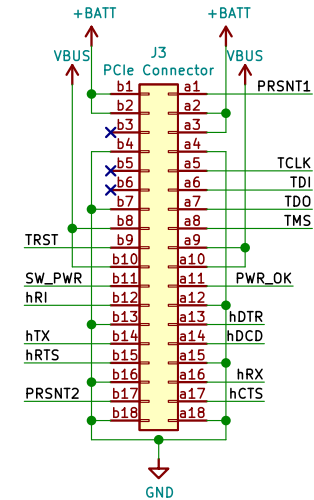
## USB to UART



## JTAG/SWD Interface

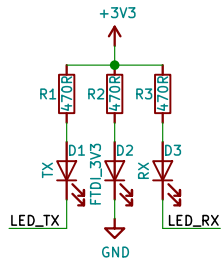


## PCIe Interface

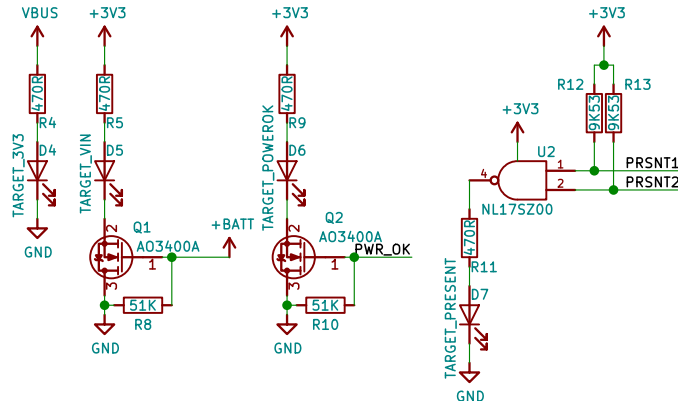


## LEDs

### FTDI



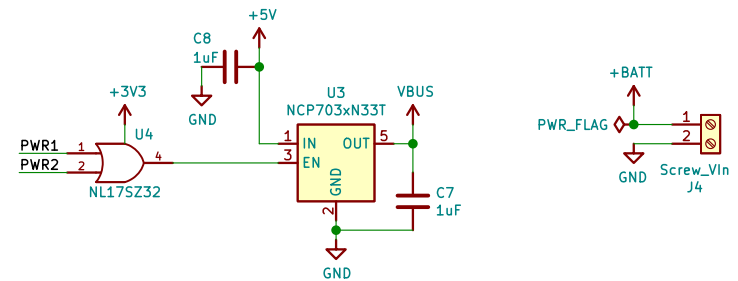
### Target



## Target Power Supply

### 3V3

### V\_IN



1. VBUS is 3V3 from a 300mA LDO.
2. FTDI 3V3 OUT supports up to 50mA output. Each 3V3 LED is 5mA.
3. 10 pin compact JTAG header follows Black Magic Probe pinning. SWD pin header is for other devices.
4. If target needs VBUS, power with USB and enable via JTAG/SWD/FTDI.

Puccinelli Laboratories, Inc.

Sheet: /

File: P1\_FT231X.sch

**Title: P1 – Module Programmer/Debugger**

Size: A4 Date: 2020-05-16

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Id: 1/1