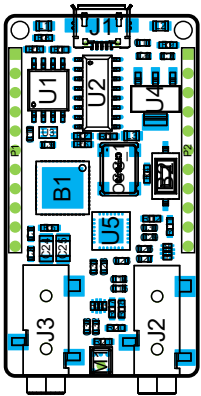


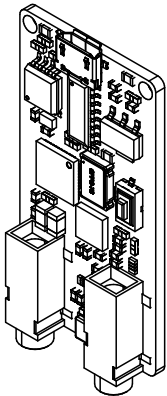
# RocketFPGA

Title	Page number
FPGA & Flash Memory	1
USB & MCU	2
Power & IO	3
Audio Codec	4

View from Top side (Scale 1:1)



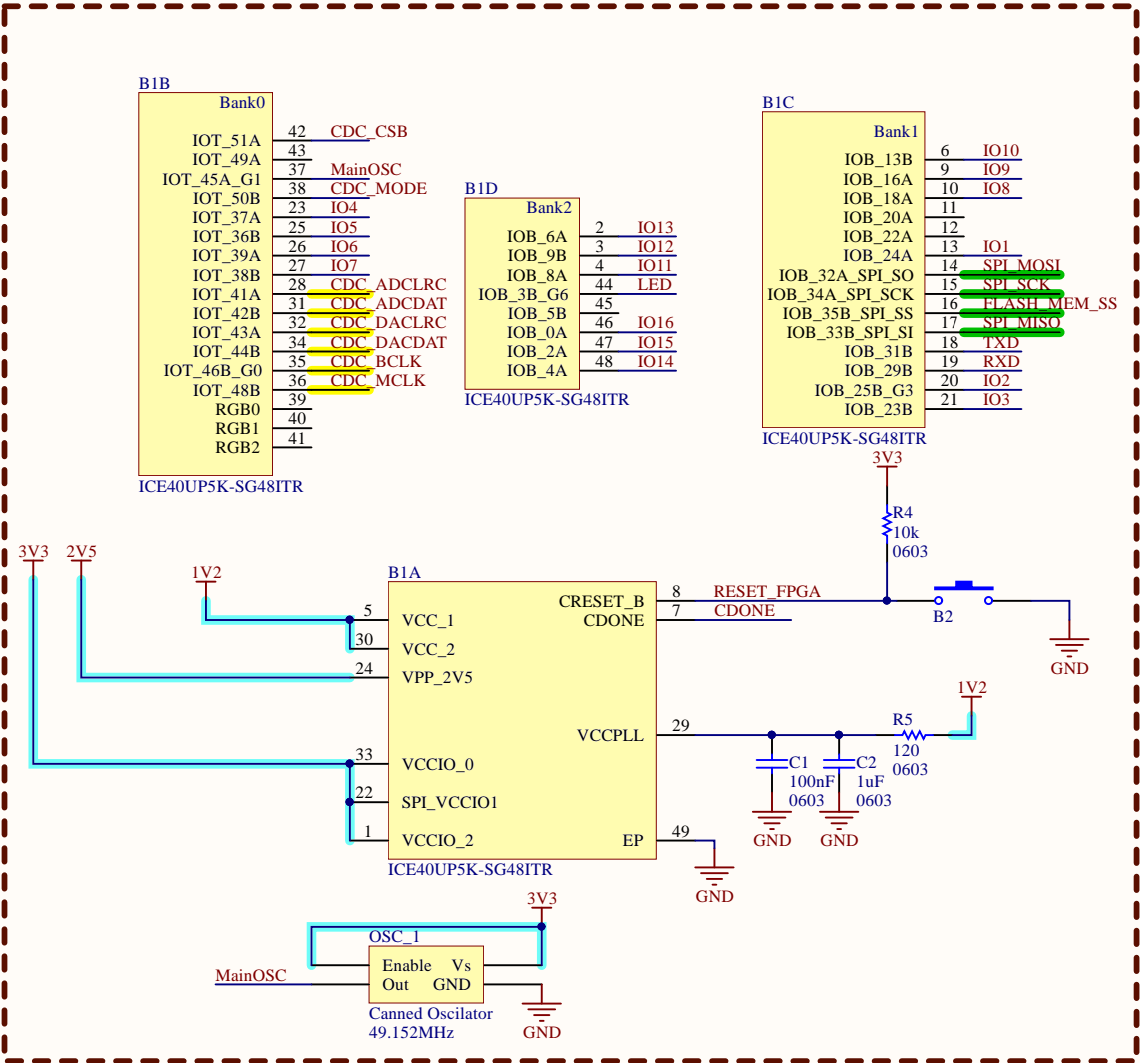
View from Top side (Scale 1:1)



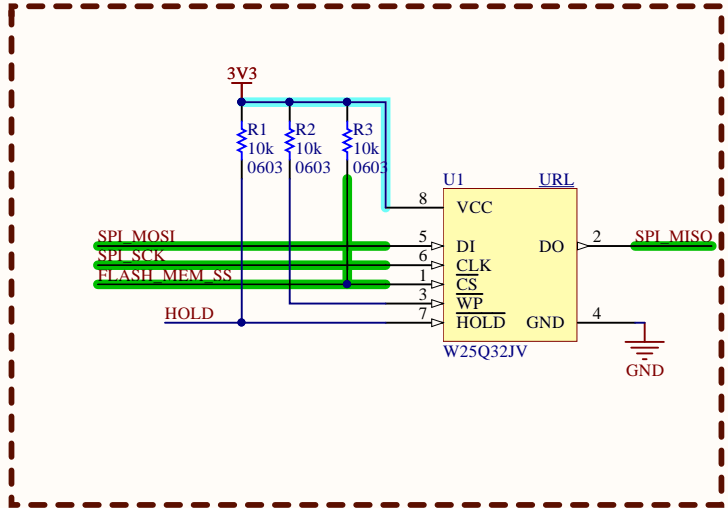
To be updated as  
project develops

# FPGA & Flash Memory

## ICE40UP5K



## FLASH MEMORY



Sheet title: **FPGA & Flash Memory**

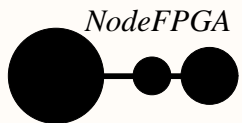
Project title: **RocketFPGA.PrjPcb**

Designer: **Luis Sánchez Velasco**

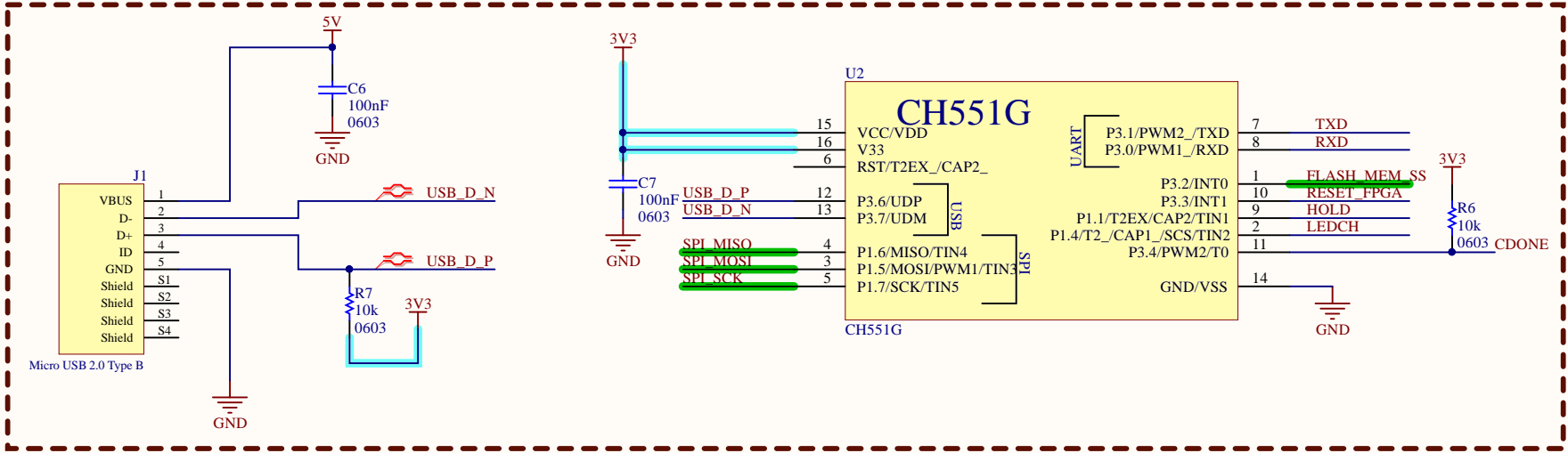
Date: **08/06/2019**

Revision: **0**

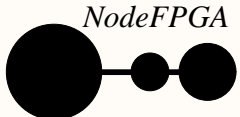
Sheet 1 of 4



# USB & Microcontroller

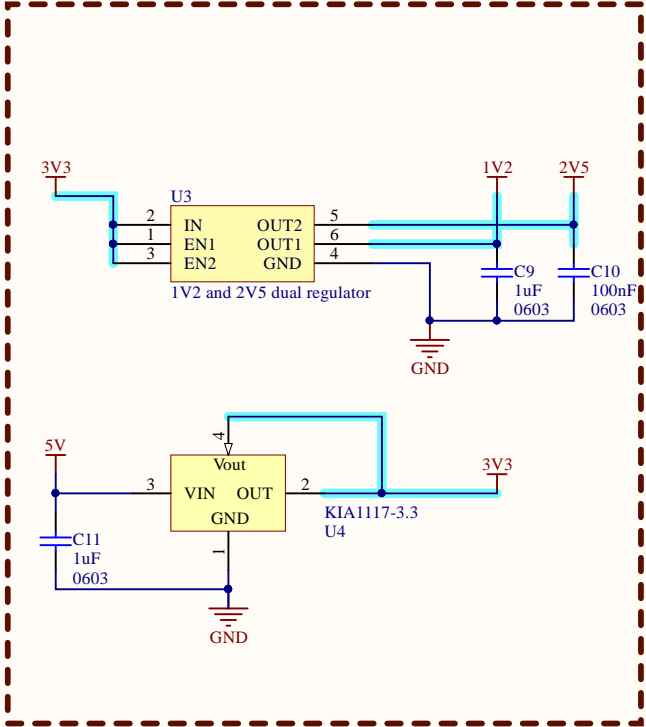


Sheet title: <b>USB &amp; Microcontroller</b>		
Project title: <b>RocketFPGA.PrjPcb</b>		
Designer: <b>Luis Sánchez Velasco</b>		
Date: <b>08/06/2019</b>	Revision: <b>0</b>	Sheet 2 of 4

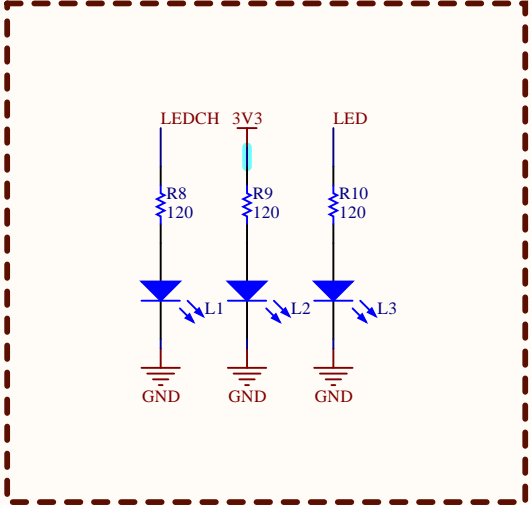


# Power & IO

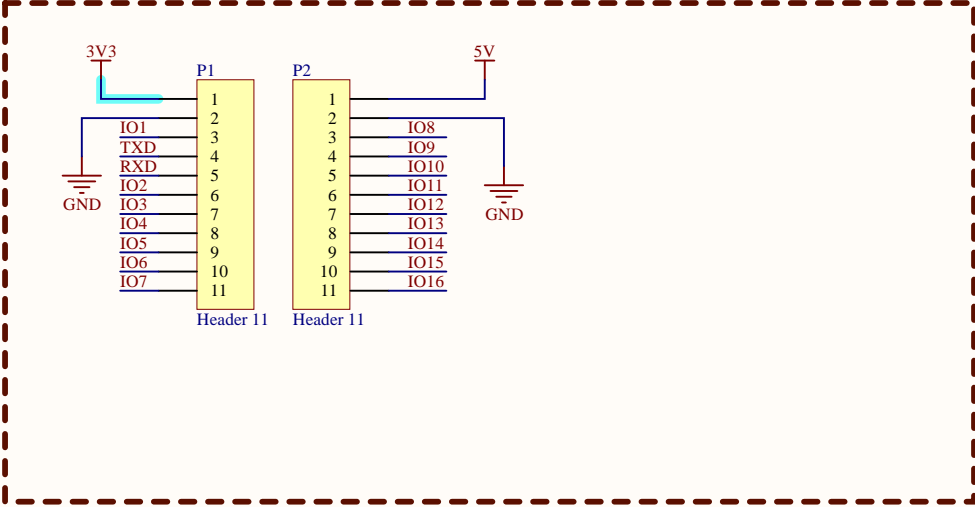
## V REGULATORS



## STATUS LEDS



## PIN HEADERS



Sheet title: **Power & IO**

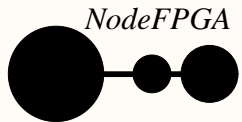
Project title: **RocketFPGA.PrjPcb**

Designer: **Luis Sánchez Velasco**

Date: **08/06/2019**

Revision: **0**

Sheet 3 of 4



# AUDIO CODEC

