NodeFPGA

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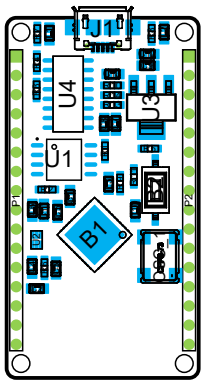
To be updated as project develops

View from Top side (Scale 1:1)



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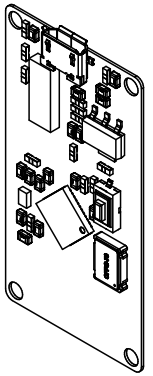
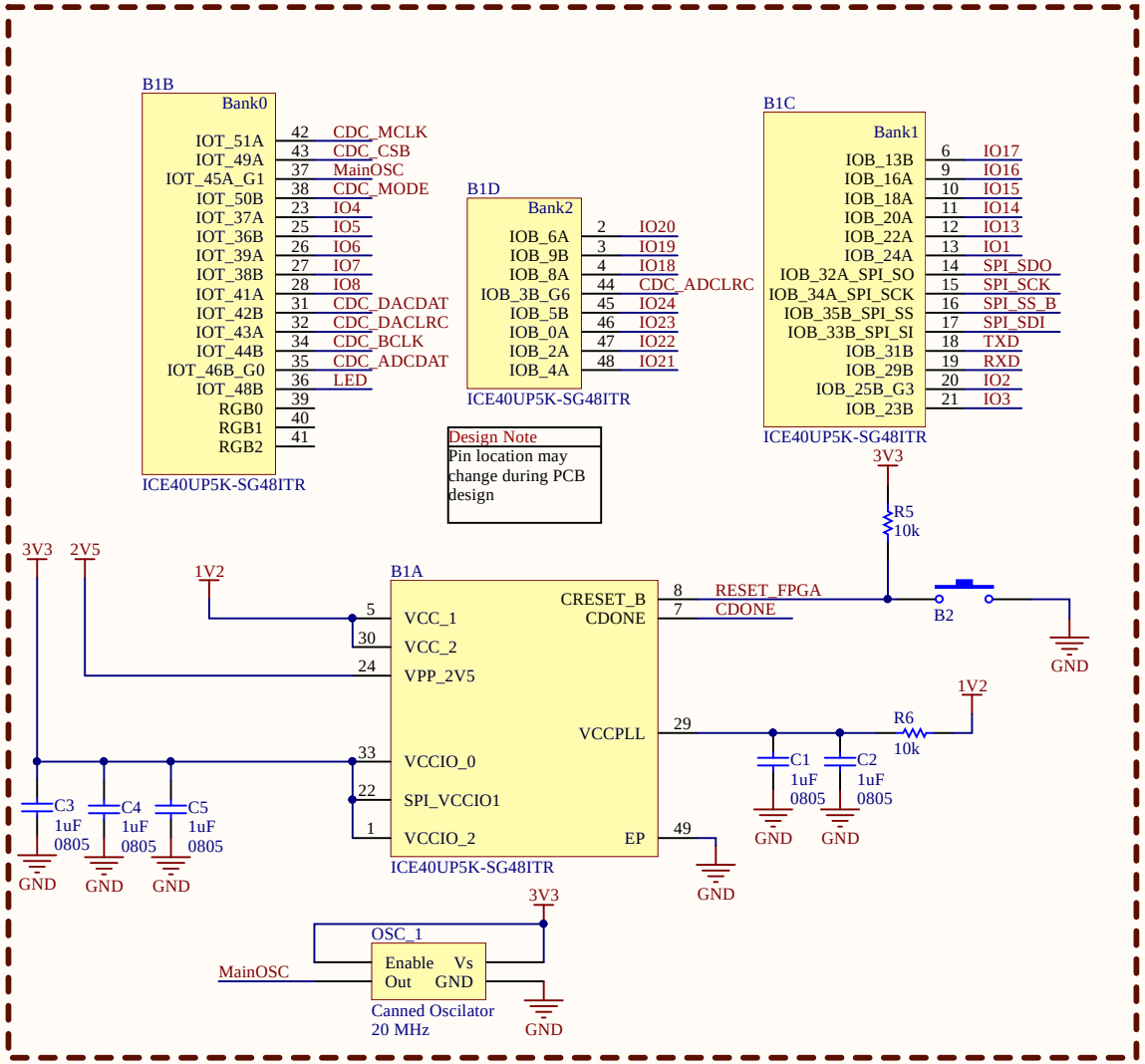
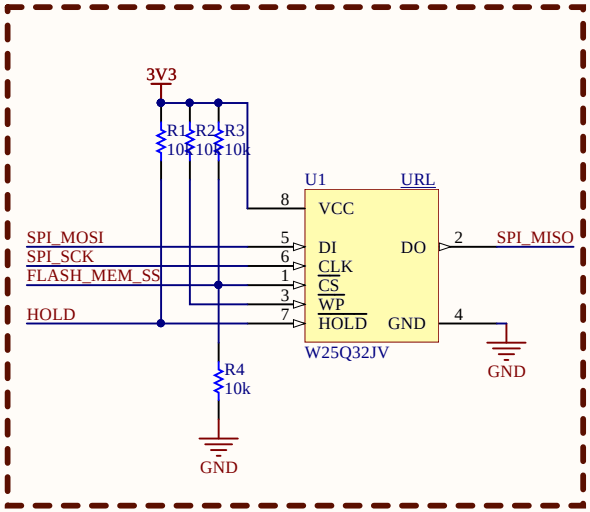


FPGA & Flash Memory

ICE40UP5K



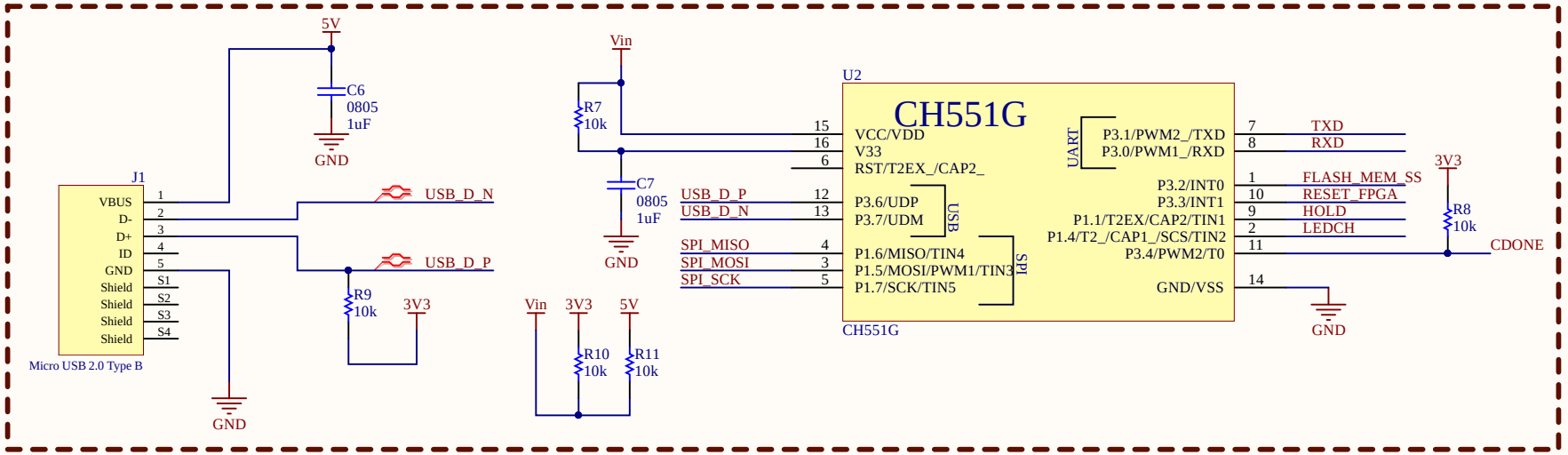
FLASH MEMORY



Sheet title: **FPGA & Flash Memory**			
Project title: **NodeFPGA.PrjPcb**			
Desginer: **Luis Sánchez Velasco**			
Date: **08/06/2019**	Revision: **0**	Sheet 1 of 4	

NodeFPGA

USB & Microcontroller



Sheet title: **USB & Microcontroller**		
Project title: **NodeFPGA.PrjPcb**		
Desginer: **Luis Sánchez Velasco**		
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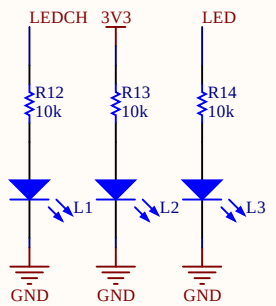
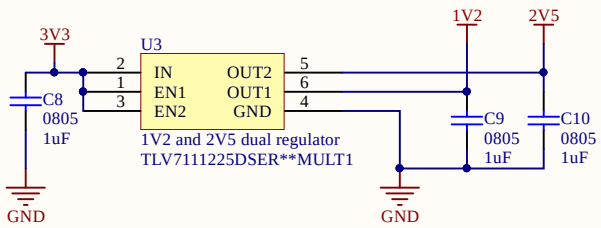
NodeFPGA

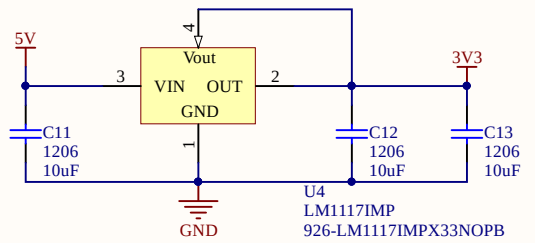
Power & IO

STATUS LEDS

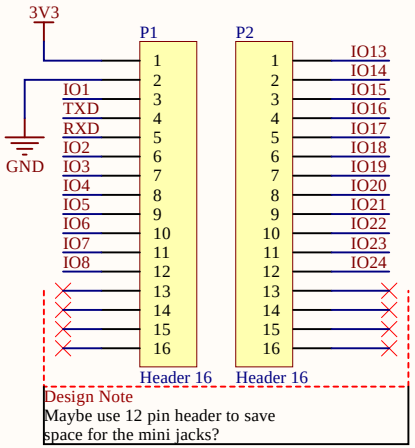


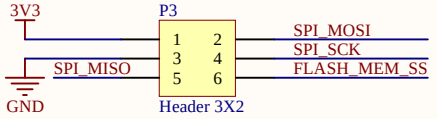
V REGULATORS





PIN HEADERS





Sheet title: **Power & IO**		
Project title: **NodeFPGA.PrjPcb**		
Desginer: **Luis Sánchez Velasco**		
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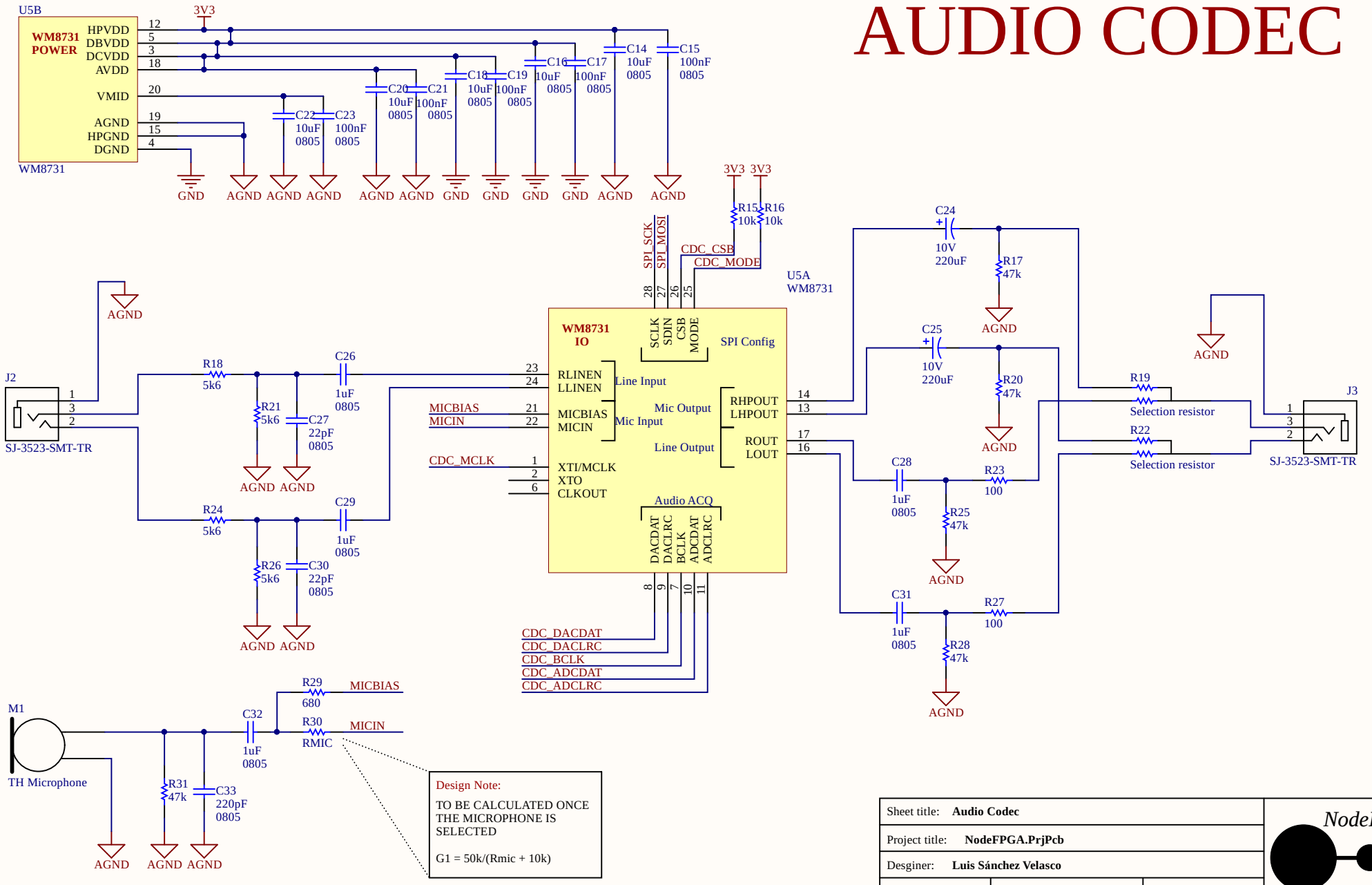
NodeFPGA

AUDIO CODEC



Sheet title: **Audio Codec**			NodeFPGA
Project title: **NodeFPGA.PrjPcb**			
Desginer: **Luis Sánchez Velasco**			
Date: **08/06/2019**	Revision: **0**	Sheet 4 of 4	