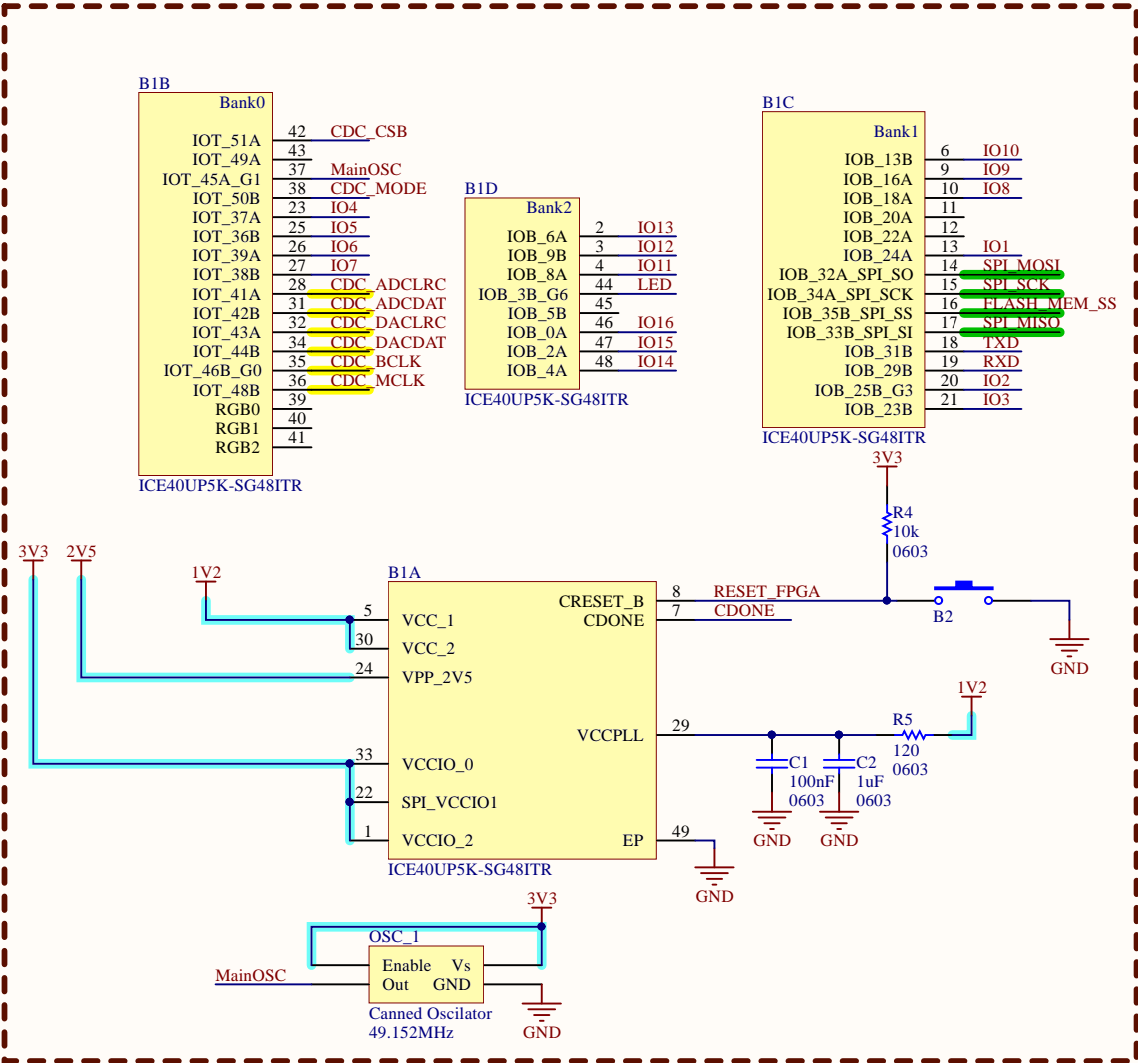
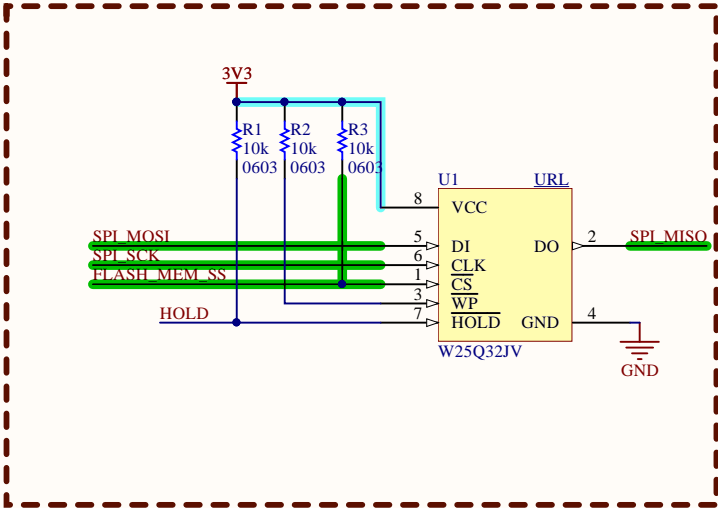


# FPGA & Flash Memory

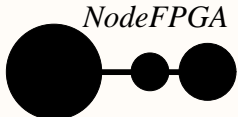
## ICE40UP5K



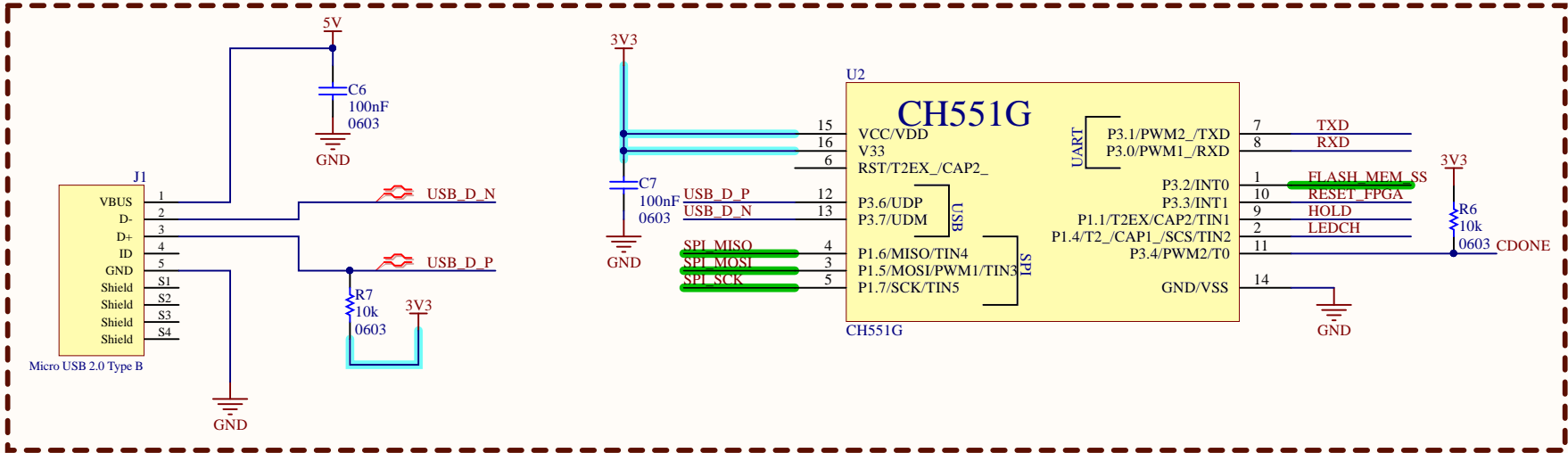
## FLASH MEMORY



Sheet title: <b>FPGA &amp; Flash Memory</b>		
Project title: <b>RocketFPGA.PrjPeb</b>		
Designer: <b>Luis Sánchez Velasco</b>		
Date: <b>08/06/2019</b>	Revision: <b>0</b>	Sheet 1 of 4



# USB & Microcontroller



Sheet title: USB & Microcontroller

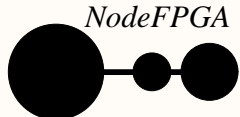
Project title: RocketFPGA.PrjPcb

Designer: Luis Sánchez Velasco

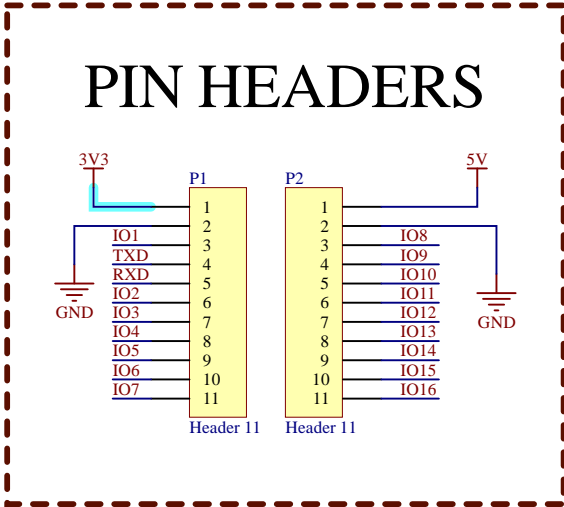
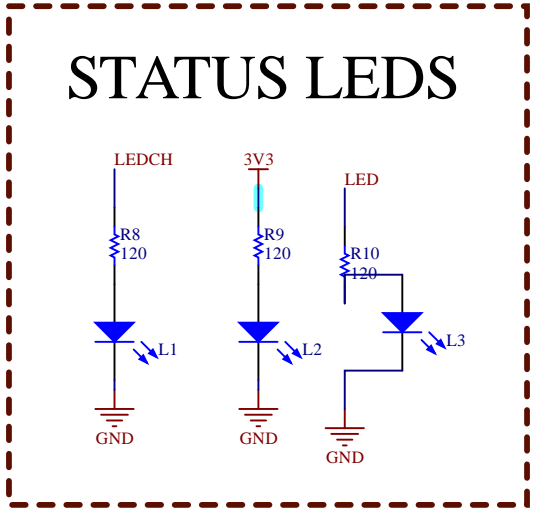
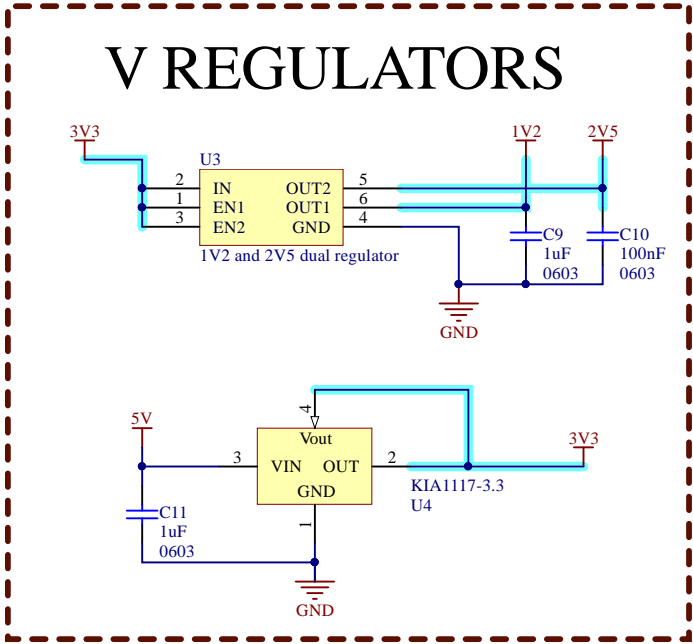
Date: 08/06/2019

Revision: 0

Sheet 2 of 4



# Power & IO



Sheet title: **Power & IO**

Project title: **RocketFPGA.PrjPcb**

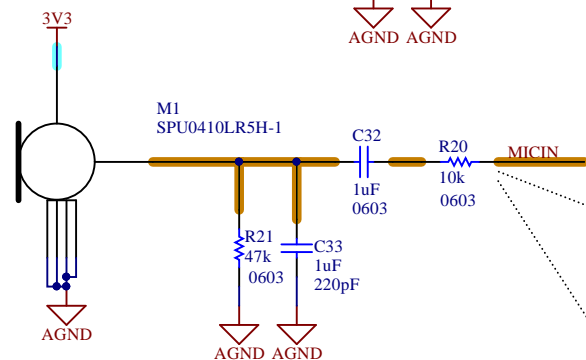
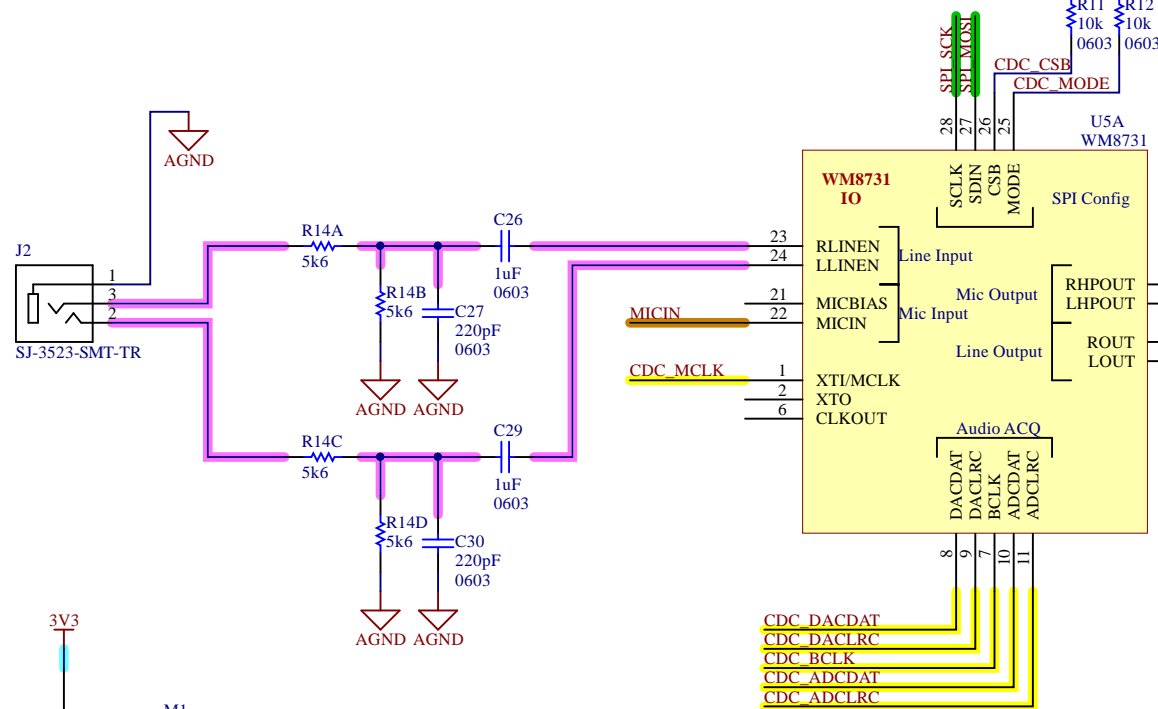
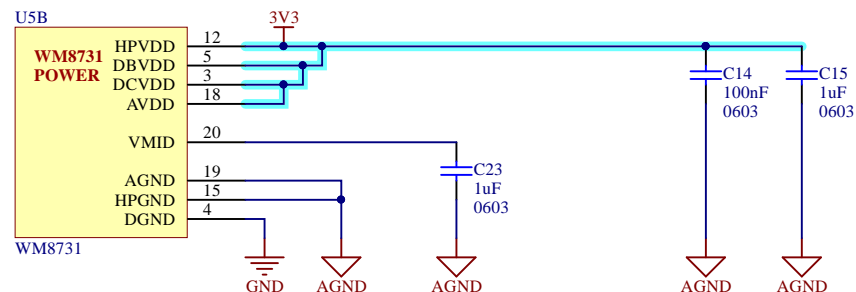
Desginer:

Date:

Revision:

Sheet of

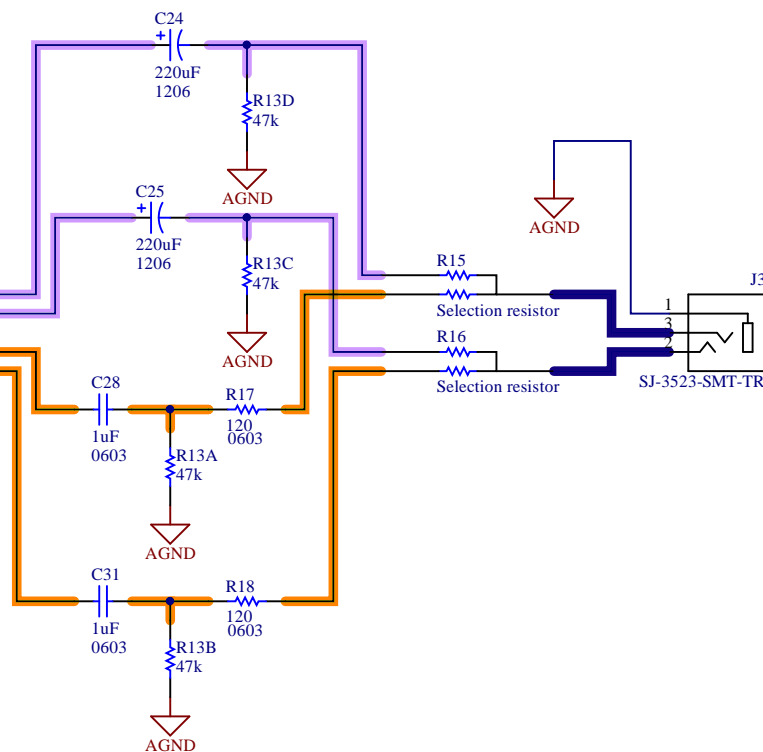
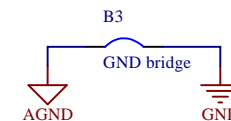




**Design Note:**  
TO BE CALCULATED ONCE  
THE MICROPHONE IS  
SELECTED




$G1 = 50k / (R_{mic} + 10k)$

# AUDIO CODEC



Sheet title:			
Project title: <b>RocketFPGA.PrjPcb</b>			
Desginer:			
Date:	Revision:	Sheet	of



	1	2	3	4															
A					A														
B					B														
C					C														
D				<table><tr><td colspan="2">Sheet title:</td><td colspan="2" rowspan="3"> RocketFPGA The hardcore audio processor</td></tr><tr><td colspan="2">Project title: <b>RocketFPGA.PrjPcb</b></td></tr><tr><td colspan="2">Desginer:</td></tr><tr><td>Date:</td><td></td><td>Revision:</td><td></td><td>Sheet of</td><td></td></tr></table>	Sheet title:		 RocketFPGA The hardcore audio processor		Project title: <b>RocketFPGA.PrjPcb</b>		Desginer:		Date:		Revision:		Sheet of		D
Sheet title:		 RocketFPGA The hardcore audio processor																	
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Date:		Revision:		Sheet of															
	1	2	3	4															