**Project Plan report format – Your first page MUST match this format**

Brief Description of Mode of operation, including selected algorithms

High level sketch. Add details on the following pages if necessary

Name:

Unityid:

StudentID:.

Schedule:

/10

Summary Risk Plan:

**Final Project Report First Page. Must match this format (Title)**

Name:

Unityid:

StudentID:.

1/(delay.area) (ns-1.um-2)

Logic Area: (um2)

Memory: N/A

Delay (ns to run provided provided example).

Clock period:

# cycles”:

Delay (TA provided example. TA to complete)

1/(delay.area) (TA)

**Abstract**

Abstract should briefly summarize that the hardware function is (remember a future employer might be reading this), what your approach was, and the main results achieved.

**Project Title**

Student names

**Abstract**

Repeat this if you want a stand-alone report without the previous page.

Note. This outline is not intended as a rigid structure but as guidance.

**1. Introduction**

* What hardware is being designed here (remember, you might want to show this to a future employer who has not read the description I produced).
* Summary of key innovations if any claimed
* Summary of results achieved.
* Structure of the rest of this report

**2. Micro-Architecture**

* Hardware “algorithmic” approach used.
* High level architecture drawing, and description of data flow
* Details on claimed innovations

**3. Interface Specification**

* Detailed description of top level interface to your design
* Include a table listing each signal, its width and function
* Include an interface timing diagram if needed

**4. Technical Implementation**

* Discussion of high level modeling (if used) and results achieved
* Discussion of any hierarchy
* Discussion, if needed, of detailed implementation

**5. Verification**

* Description of approach used to verify correctness.

**6. Results Achieved**

* Throughput, area, power/energy (if applicable), etc.

**7. Conclusions**

* Summary of project and key results