

|    | VHDL |                         | FFI            |                | Foreign application |                       |                                |
|----|------|-------------------------|----------------|----------------|---------------------|-----------------------|--------------------------------|
| A. |      | UUT                     | VHDL simulator | shared library | C bindings          | Python (ctypes)       | Python script                  |
| B. | UUT  | Verification Components | VHDL simulator | shared library | C bindings          | C application         |                                |
| C. |      | UUT                     | VHDL simulator | shared library | C++ bindings        | oct-file              | Octave                         |
| D. | UUT  | VHDL packages           | VHDL simulator | shared library | C bindings          | XyceCInterface        | YDAC / YADC models<br>Xyce     |
| E. |      | UUT                     | VHDL simulator | shared library | C bindings          | RPC library / service | Network<br>Remote server       |
| F. | UUT  | Verification Components | VHDL simulator | shared library | C/C++ bindings      | DBM plugin / client   | DBM tool<br>Binary application |