

Máster en Sistemas Electrónicos Avanzados (MSEA)
Co-simulación y verificación funcional con
VHDL, C/C++ y Python/m
{control}

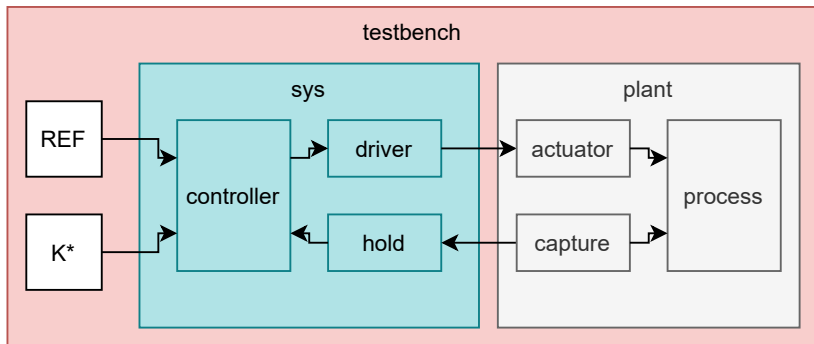
Unai Martinez Corral

 unai.martinezcorral@ehu.eus  umarcor  umarcor

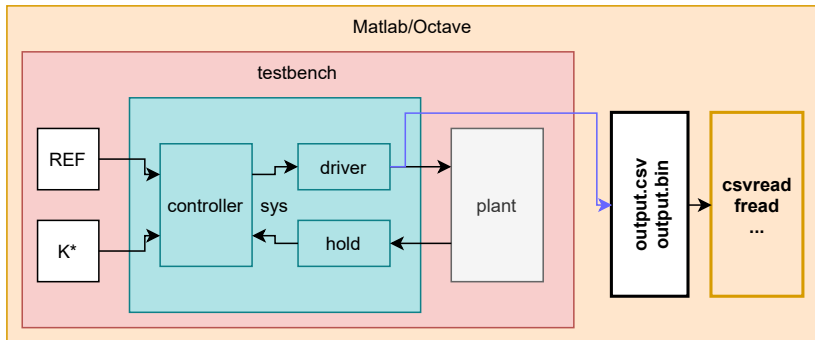
Escuela de Ingeniería de Bilbao
Universidad del País Vasco/Euskal Herriko Unibertsitatea (UPV/EHU)

2021/05

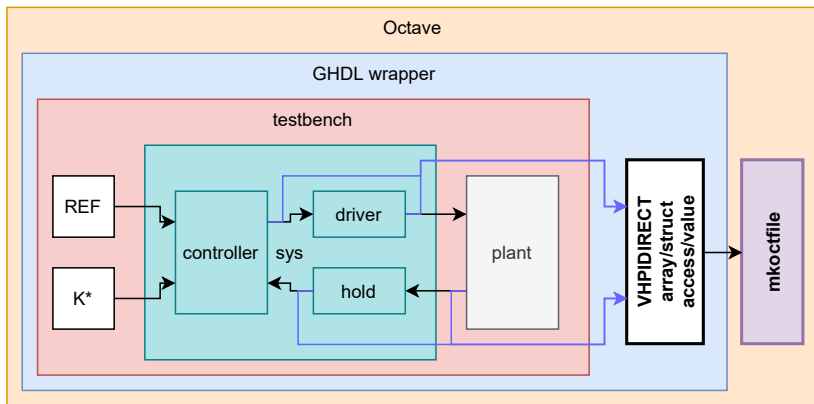
System under test



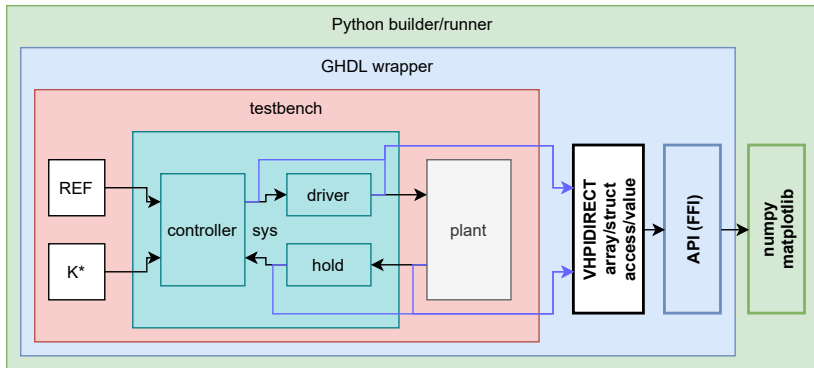
Sharing data through files (text, CSV, binary, etc.)



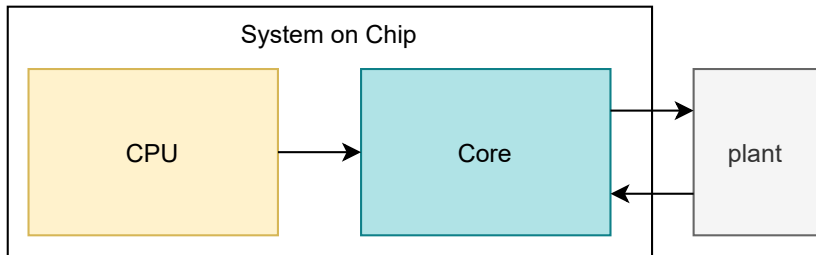
Sharing data through cosimulation (compiled)



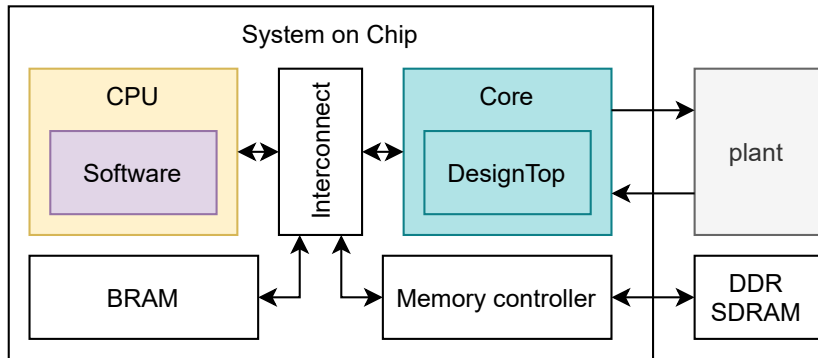
Sharing data through cosimulation (dynamic loading)



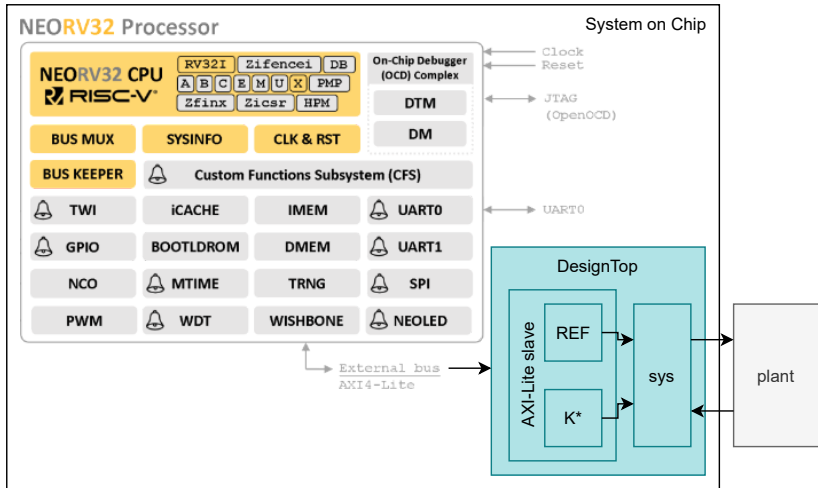
Minimal System on Chip



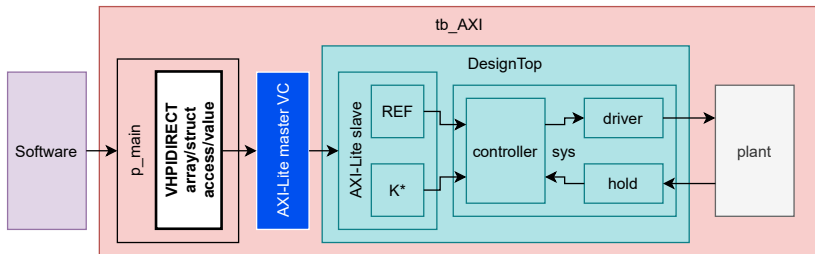
System on Chip (SoC)



NEORV32 SoC



Exercise



Assignment

