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| https://timgsa.baidu.com/timg?image&quality=80&size=b9999_10000&sec=1511703933710&di=7761808446276322df7f6f8ac9ef2865&imgtype=0&src=http%3A%2F%2Fnews.hitwh.edu.cn%2Fuploadfile%2Fimage%2F20130412%2F20130412181667386738.jpg |
| **哈尔滨工业大学（深圳）** |
| **Verilog HDL课程设计报告** |
| **题 目: Systolic Array** |
| **姓 名：** |
| **学 号：** |
| **专 业：微电子学与固体电子学** |
| **二〇一八年二月** |