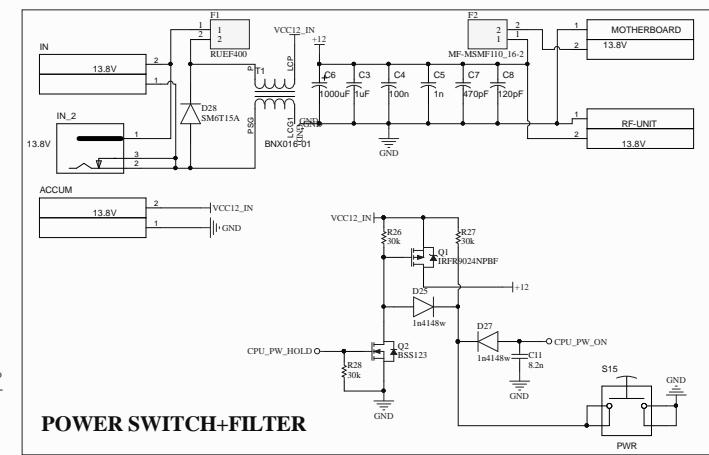
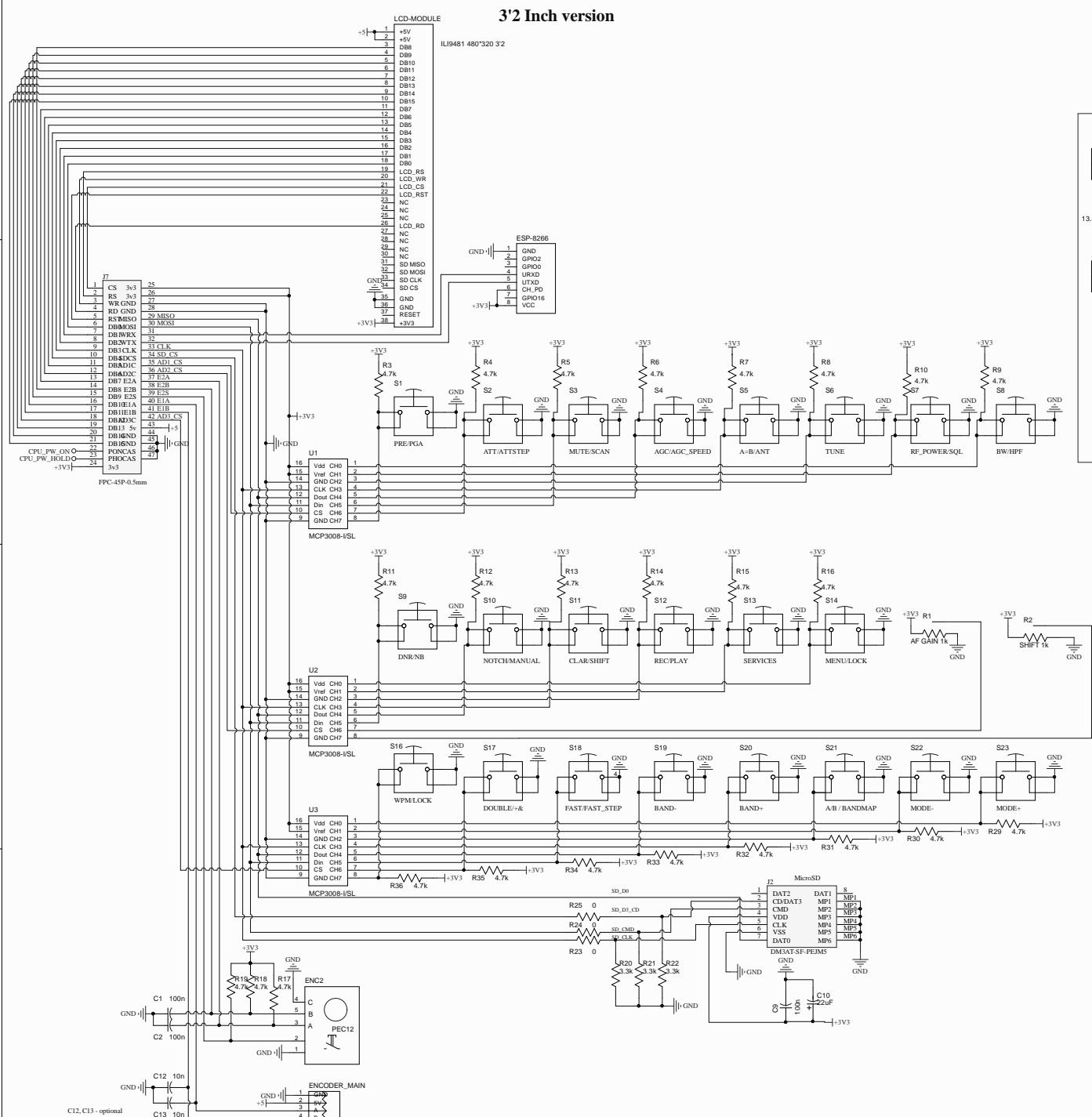
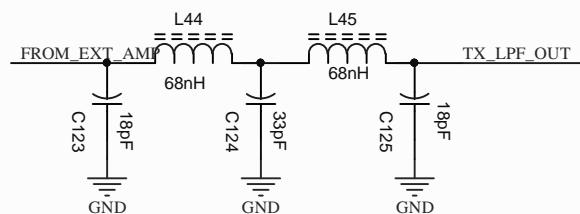


3'2 Inch version

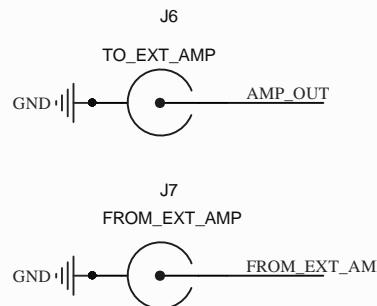


TX_LPF

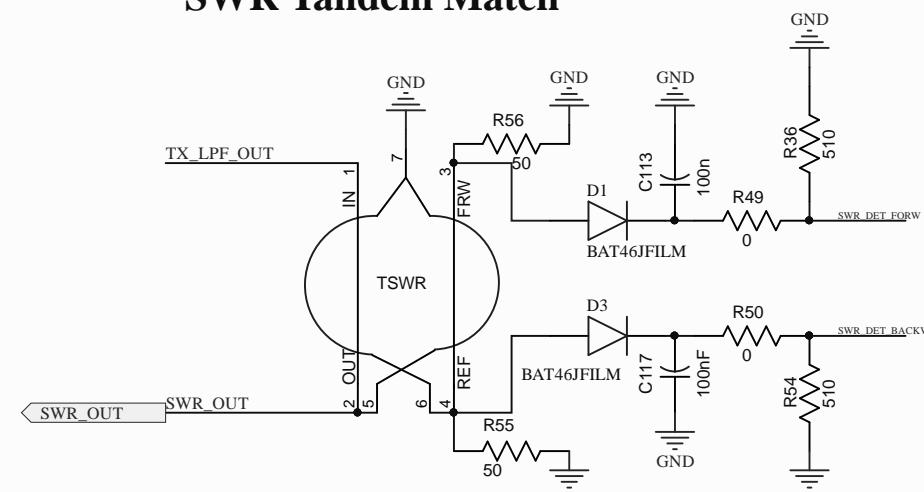
LPF 170Mhz



Катушки намотаны проводом 0.5мм на оправке диаметром 5мм, 3.5 витка



SWR Tandem Match



2 провода 0.9мм пропущено через бинокль

по диагонали пропущено и намотано по 10 витков 0.5мм

Title **SWR Tandem Match**

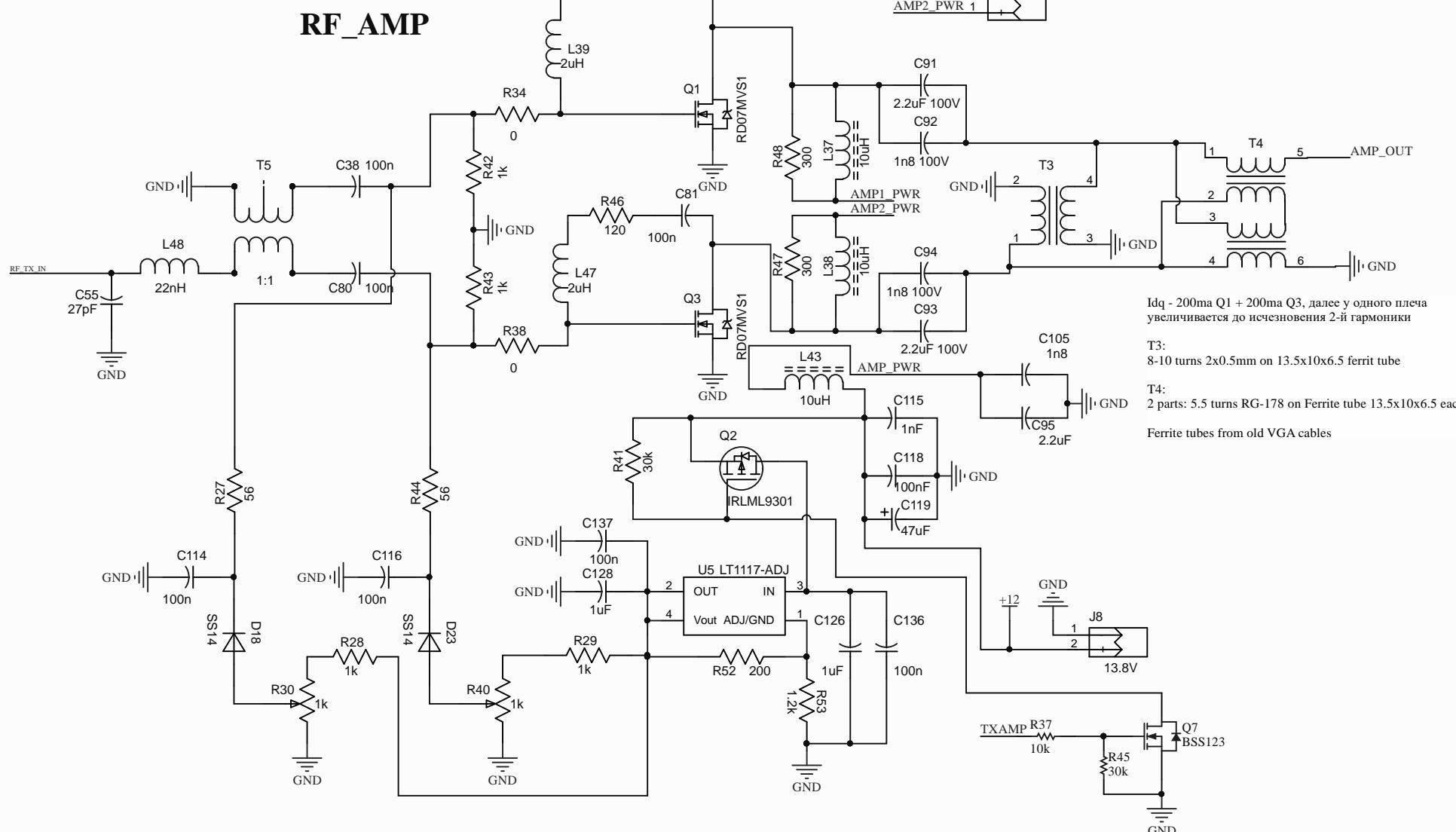
Size: A4 Number:18 Revision:2.0

Date: 25.02.2021 Time: 14:59:08 Sheet:18 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\SWR Tandem Match.SchDoc

Altium

A

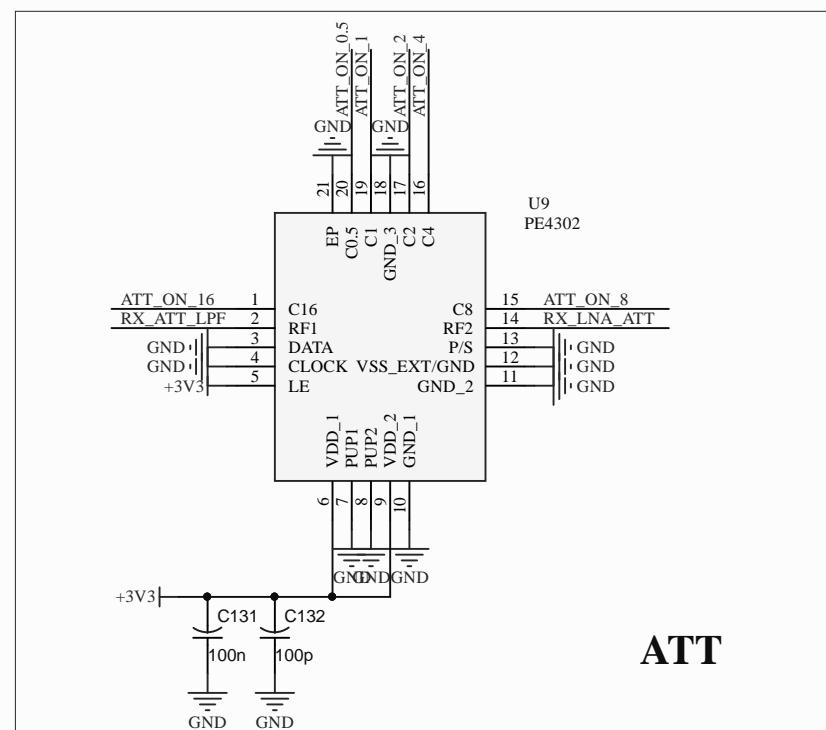
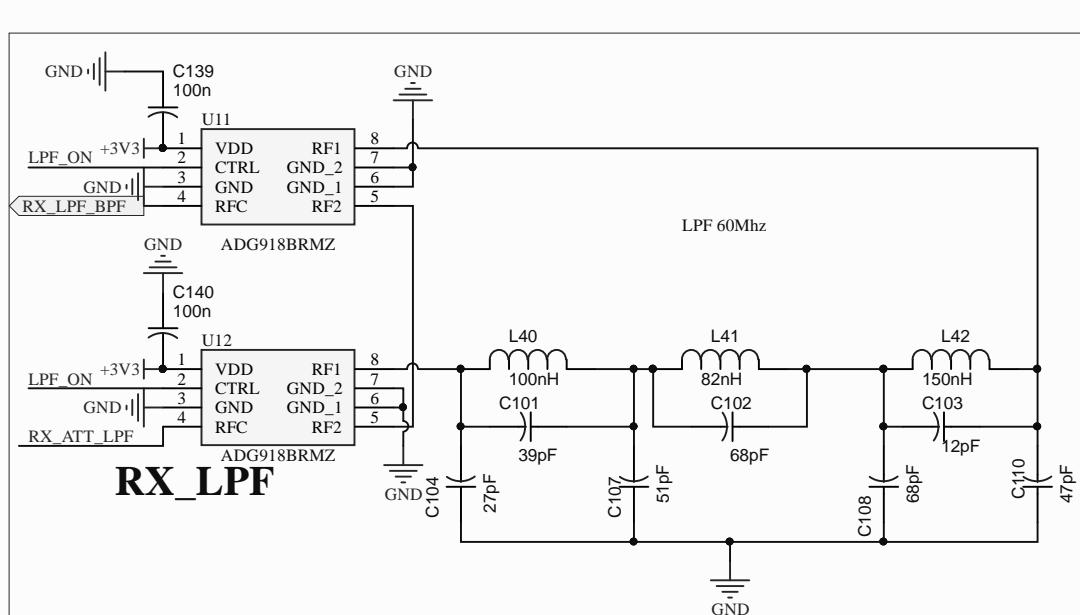
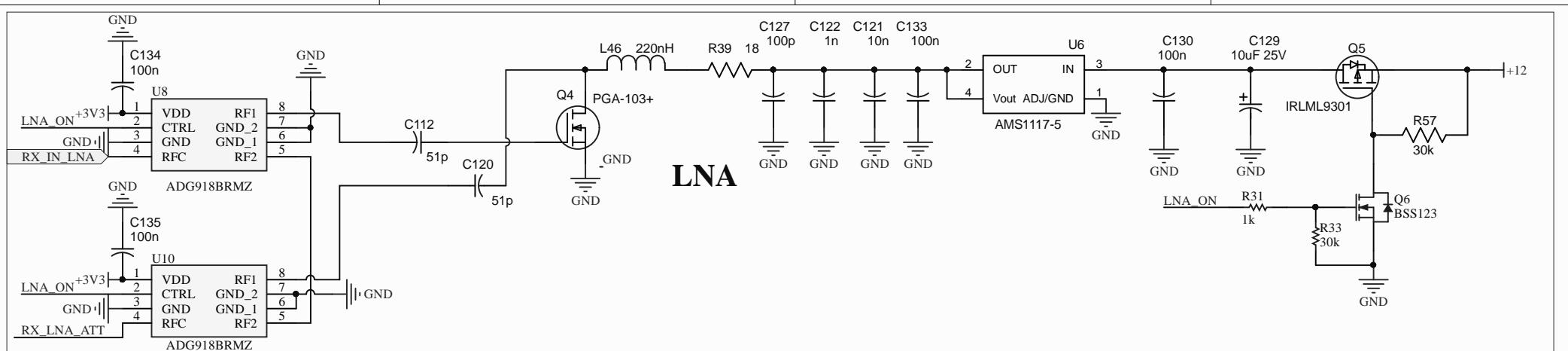
RF_AMPTitle **RF_AMP**

Size: A4 Number:17 Revision2.0

Date: 25.02.2021 Time: 14:59:10 Sheet17 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\RF_AMP.SchDoc

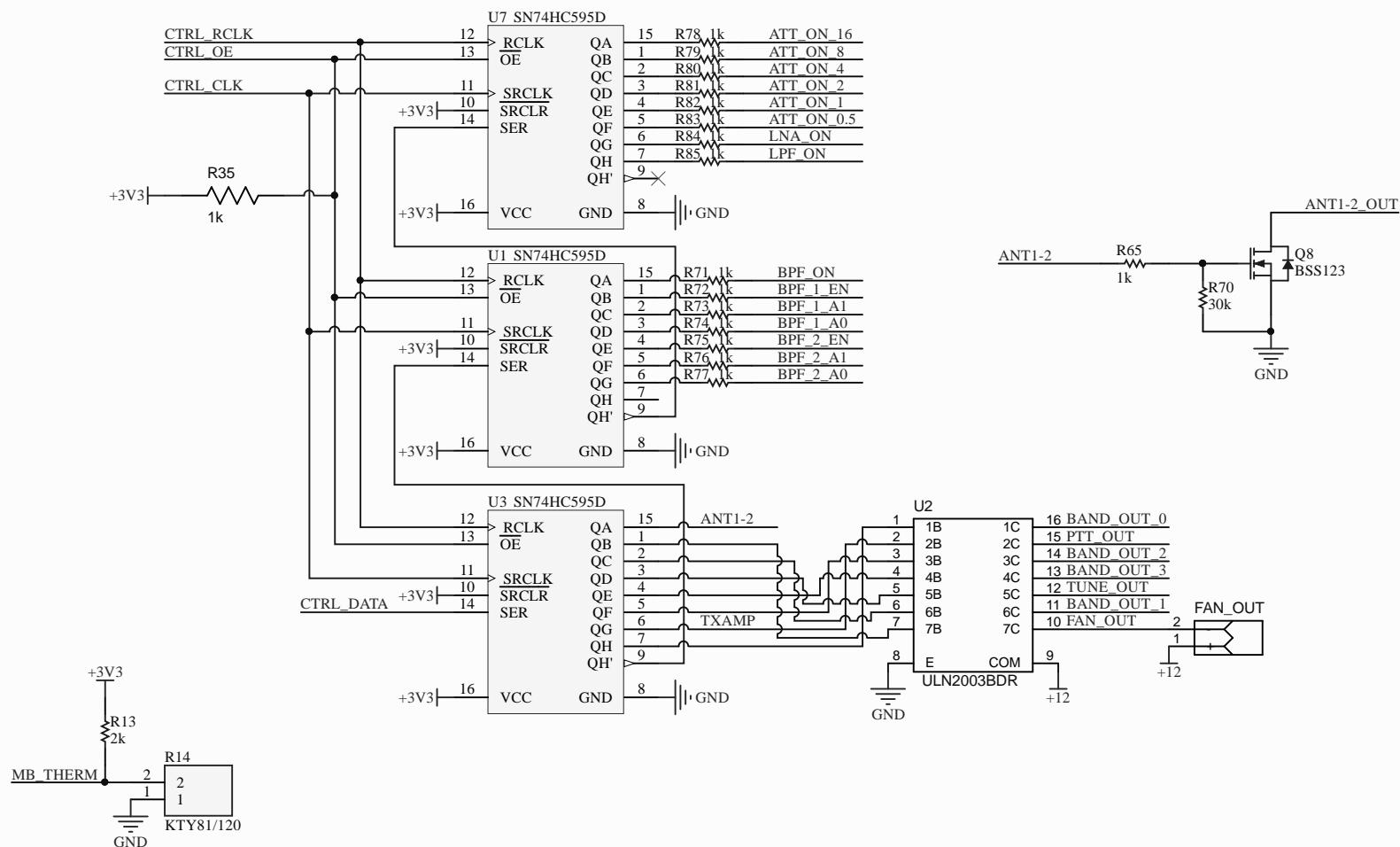
Altium



Title LNA			*
Size: A4	Number:16	Revision2.0	*
Date: 25.02.2021	Time: 14:59:11	Sheet16 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\LNA.SchDoc			

Altium

A

Title **CONTROL**

Size: A4 Number:15 Revision:2.0

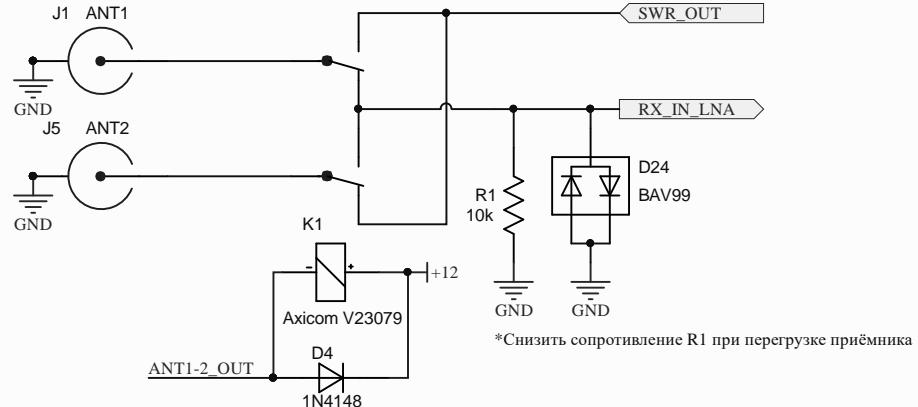
Date: 25.02.2021 Time: 14:59:13 Sheet:15 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\RF-UNIT\CONTROL.SchDoc

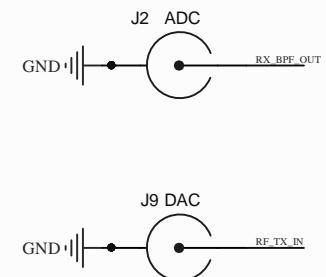
Altium

A

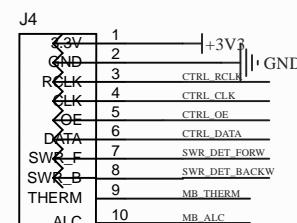
TXRX_COMUTATOR



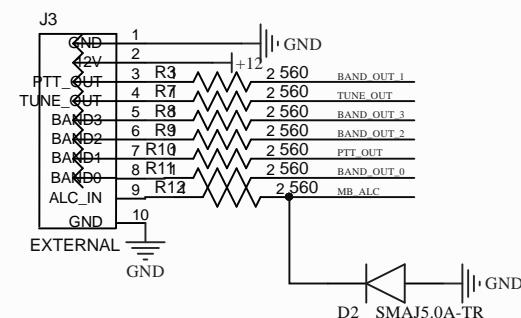
CONN



B



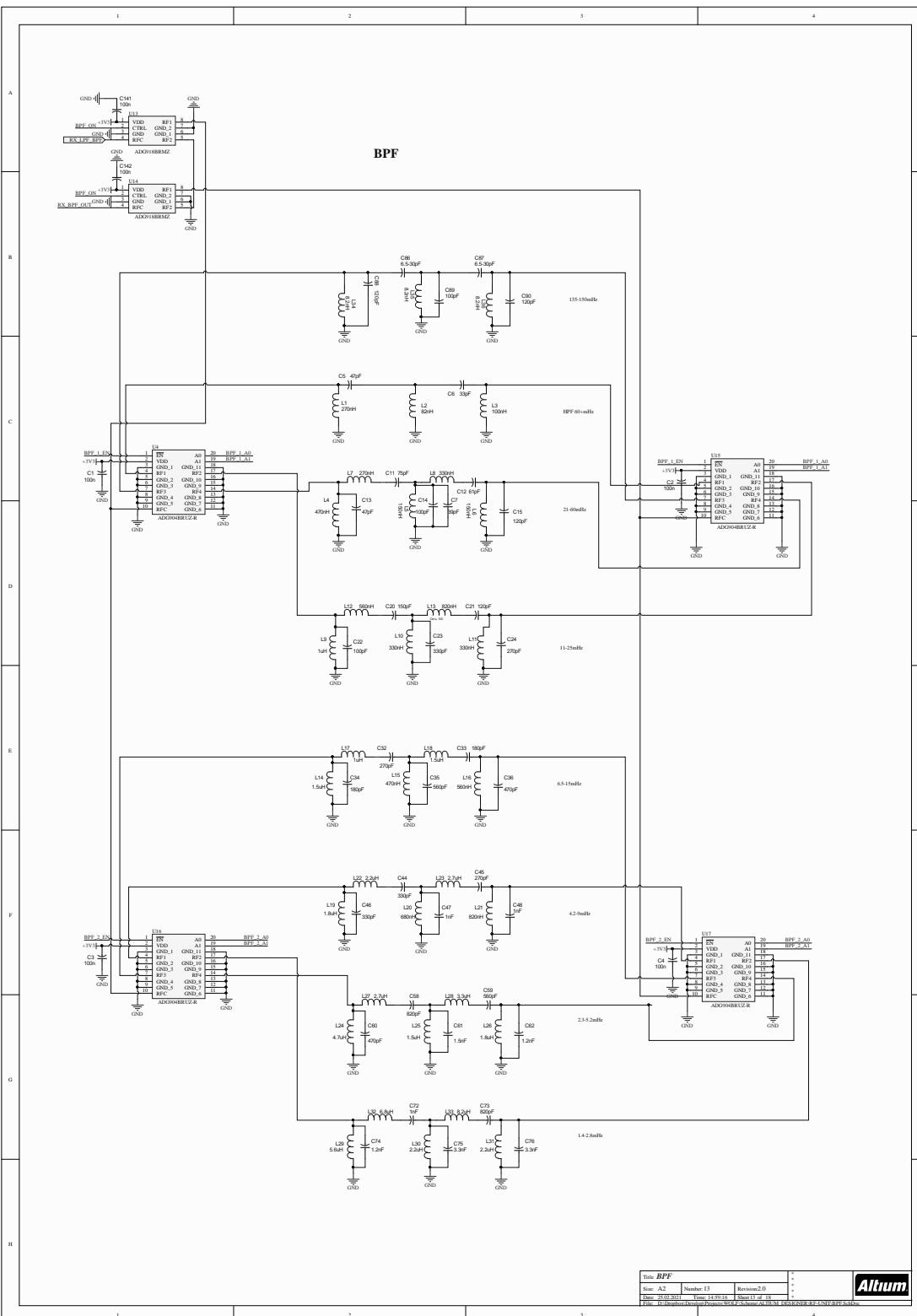
MOTHERBOARD



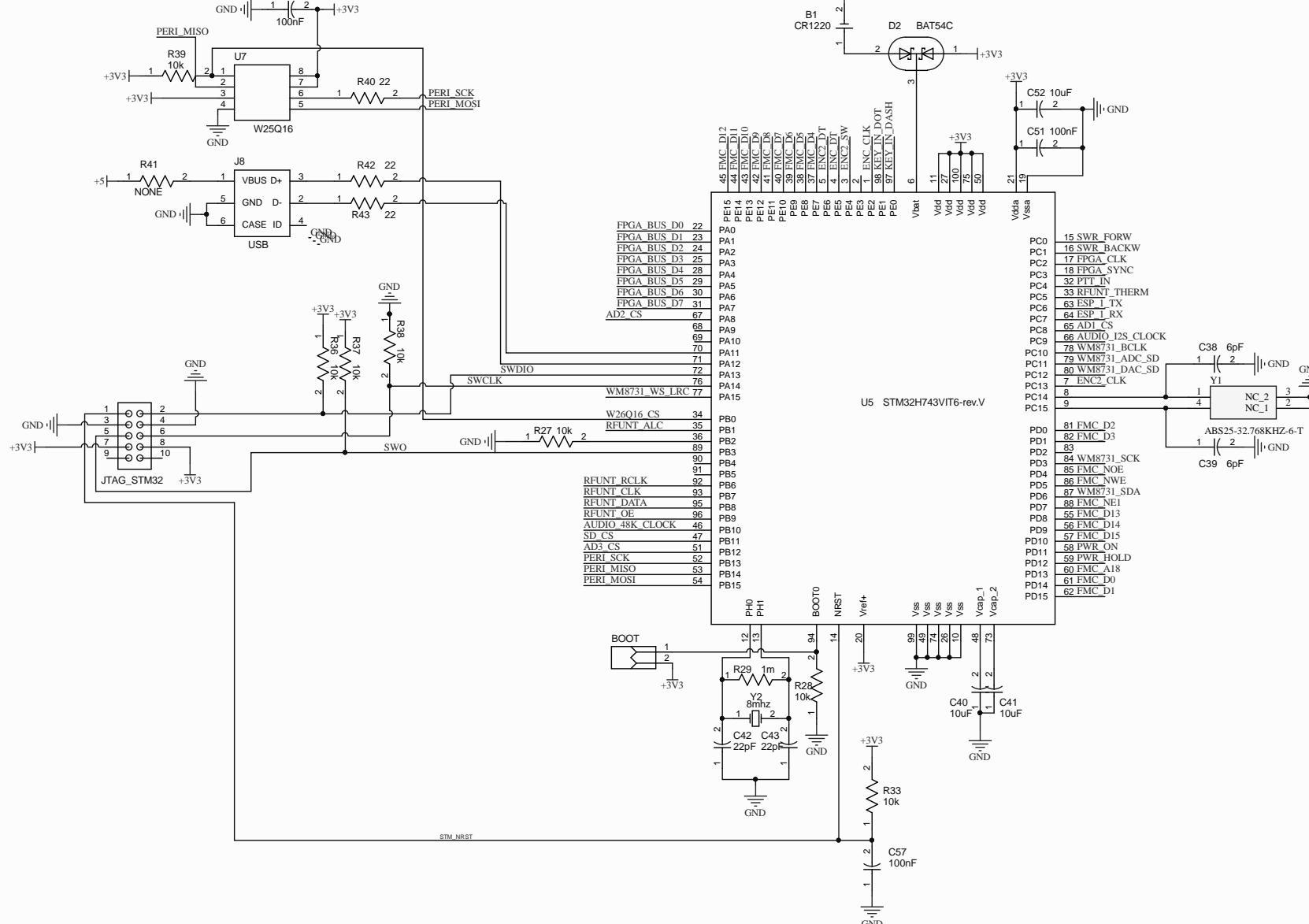
C

D

Title CONN			*
Size: A4	Number:14	Revision2.0	*
Date: 25.02.2021	Time: 14:59:14	Sheet14 of 18	*
File: D:\Dropbox\Develop\Projects\WOLF\Schema\ALTIUM_DESIGNER\RF-UNIT\CONN.SchDoc			



STM32

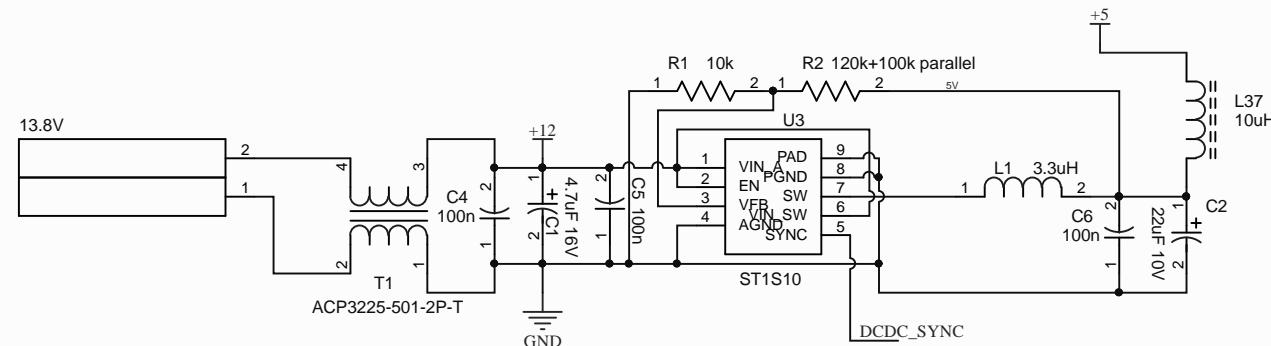


Title STM32

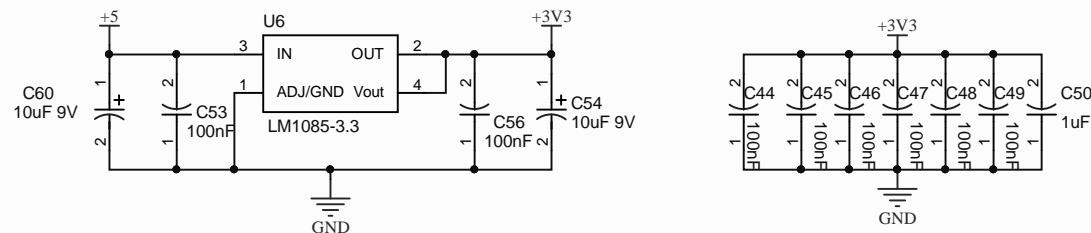
Size: A3 Number: 11 Rev: 1
Date: 25.02.2021 Time: 14:59:18 Sheet: 1
File: D:\Dropbox\Develop\Projects\WOLFS...

Altium.

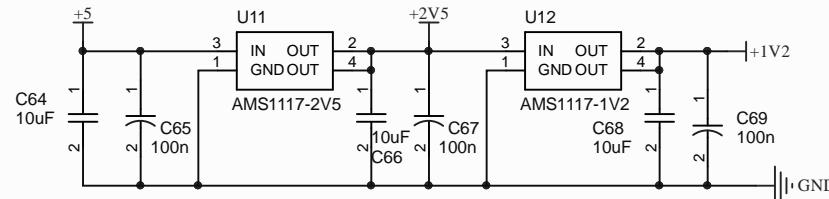
A



B

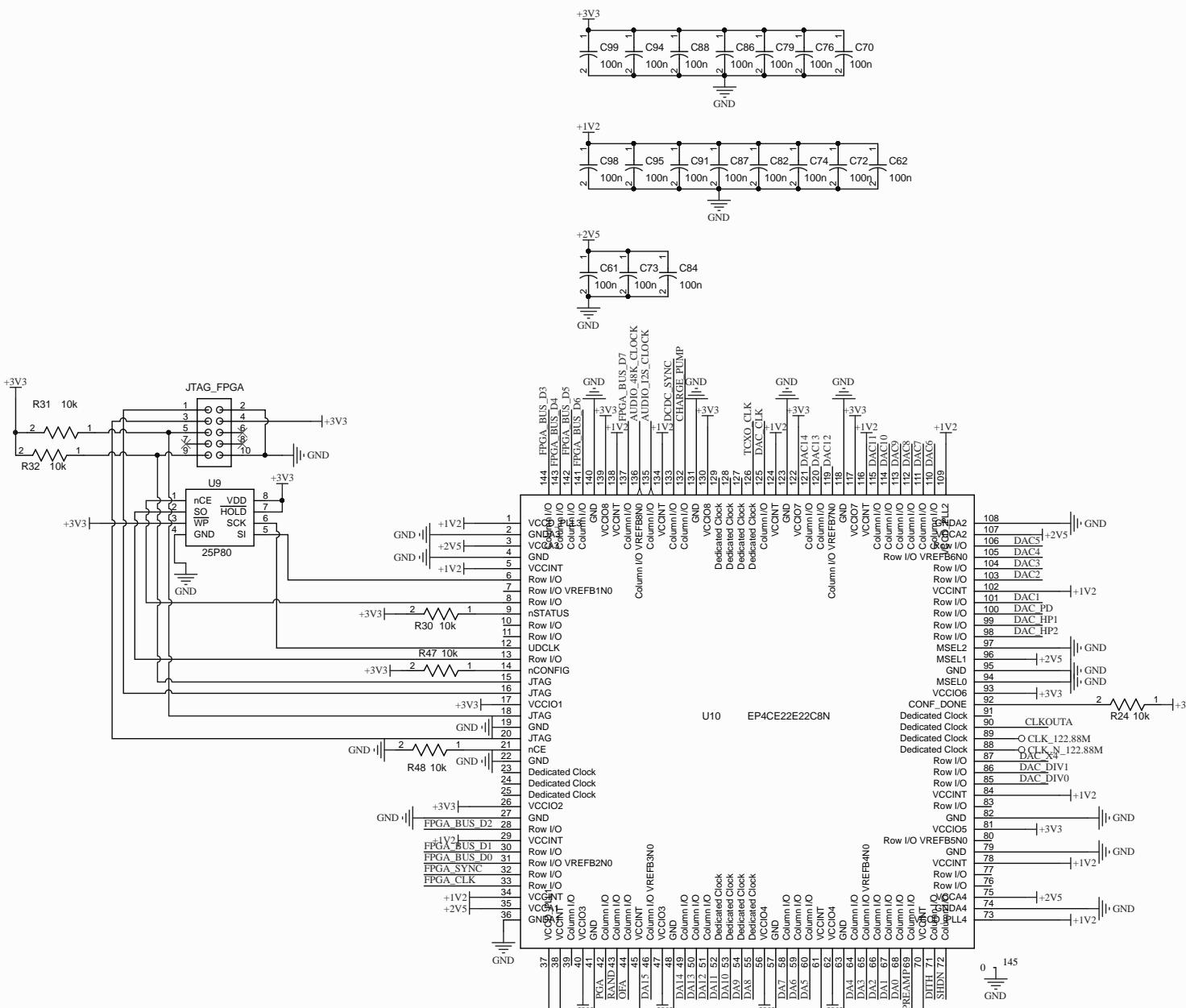


C

Title **POWER**

Size: A4	Number:10	Revision2.0
Date: 25.02.2021	Time: 14:59:20	Sheet10 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\POWER.SchDoc		*

Altium



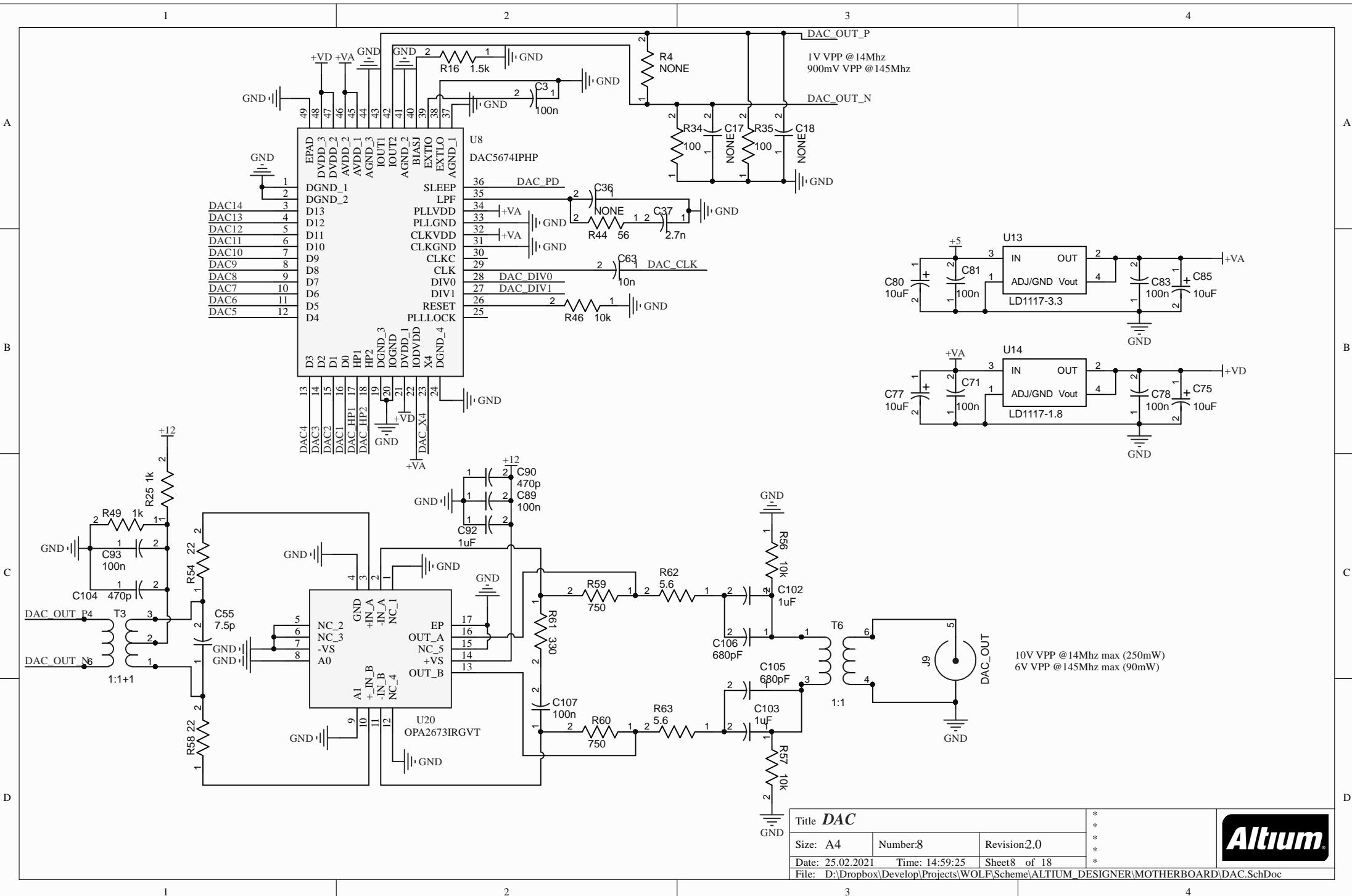
Title *FPGA*

Size: A3
Date: 25.02.20

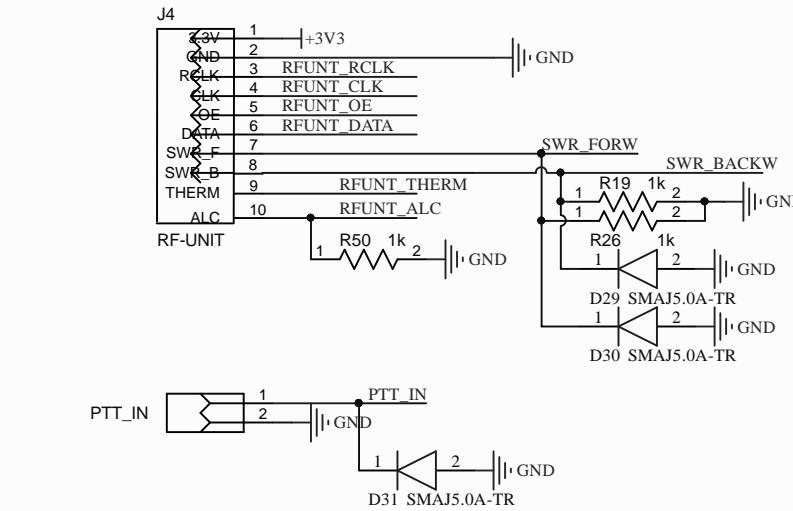
Number:9
21 Time: 14:59:22

*
*
*
*
Revision2.0
Sheet9 of 18

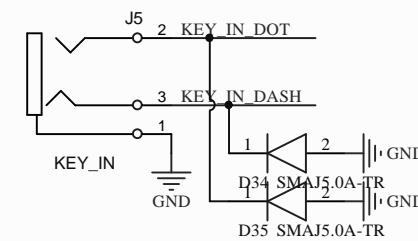
Altium



A



B



C

J6

FMC_NE1	1	CS 3v3
FMC_A18	2	RS 3v3
FMC_NWE	3	WR GND
FMC_NOE	4	RD GND
STM_NRST	5	RST'MISO
FMC_D0	6	30 PERI_MOSI
FMC_D1	7	DB0'MOSI
FMC_D2	8	DB1WRX
FMC_D3	9	DB2WTX
FMC_D4	10	DB3CLK
FMC_D5	11	DB\$DCS
FMC_D6	12	DB\$AD1C
FMC_D7	13	DB\$AD2C
FMC_D8	14	DB7E2A
FMC_D9	15	DB8E2B
FMC_D10	16	DB9E2S
FMC_D11	17	DB10E1A
FMC_D12	18	DB11E1B
FMC_D13	19	DBAD3C
FMC_D14	20	DB12E1A
FMC_D15	21	DB135v
PWR_ON	22	DB1GND
PWR_HOLD	23	DB1GND
+3V3	24	3v3

FPC-45P-0.5mm

D

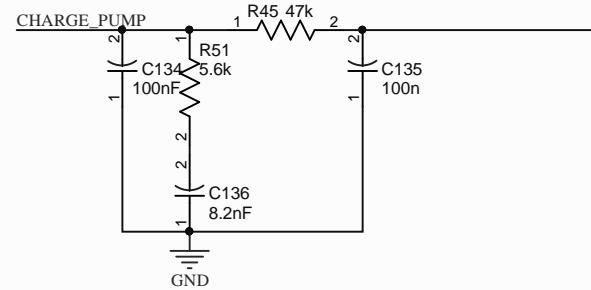
D

Title **CONNECTIONS**

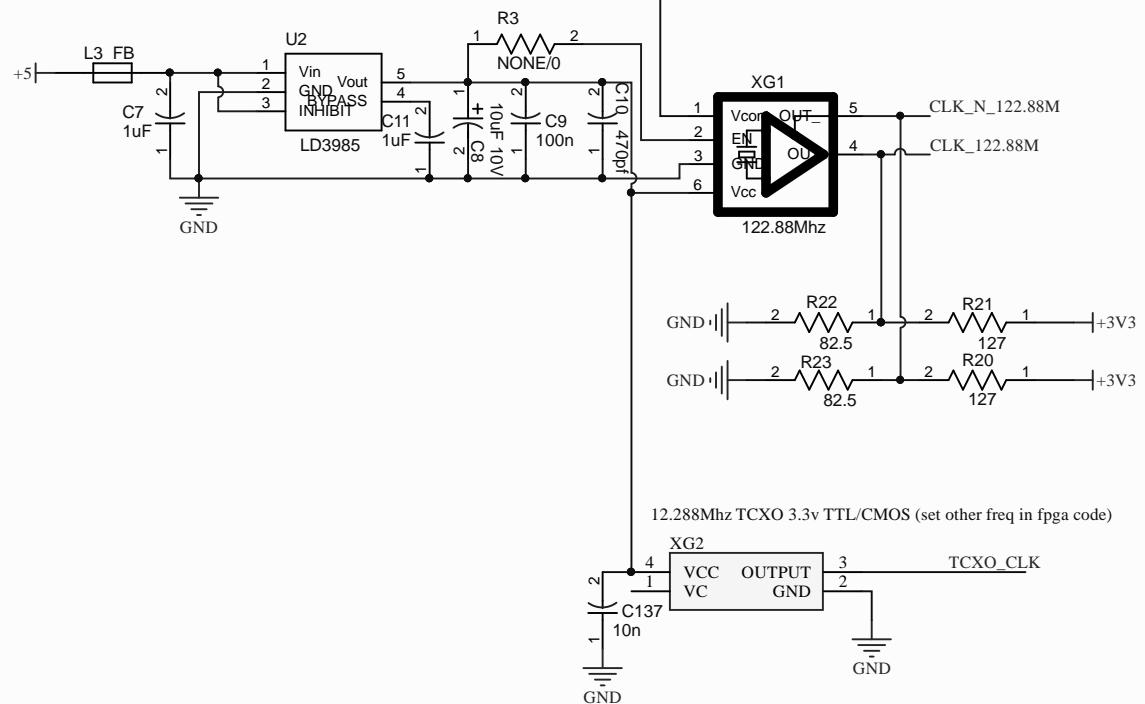
Size: A4	Number:7	Revision2.0
Date: 25.02.2021	Time: 14:59:27	Sheet7 of 18
File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CONNECTIONS.SchDoc		

Altium

A



B



C

D

Title **CLOCK_GENERATOR**

Size: A4 Number:6 Revision 2.0

Date: 25.02.2021 Time: 14:59:29 Sheet 6 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\CLOCK_GENERATOR.SchDoc

Altium

A

B

C

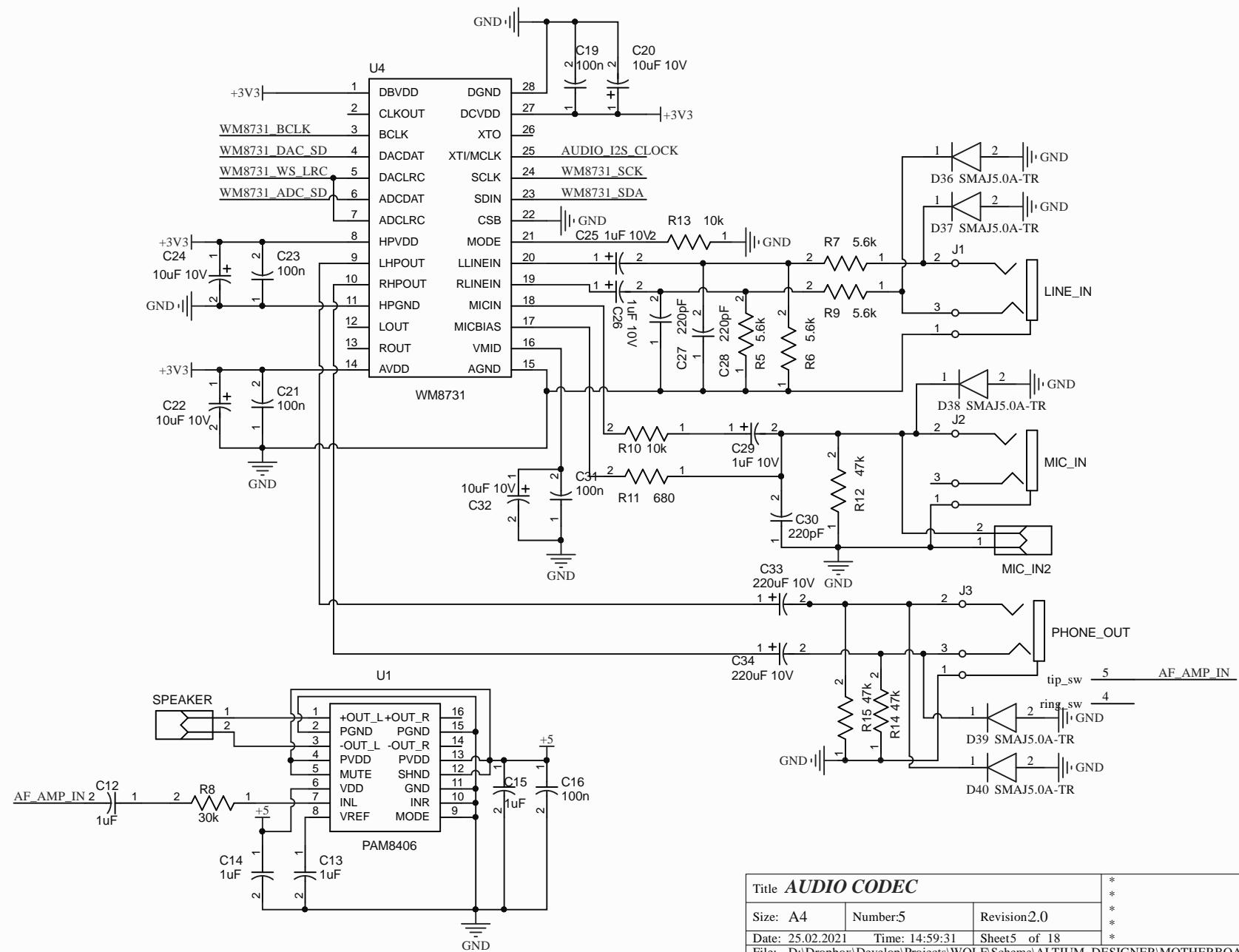
D

A

B

C

D

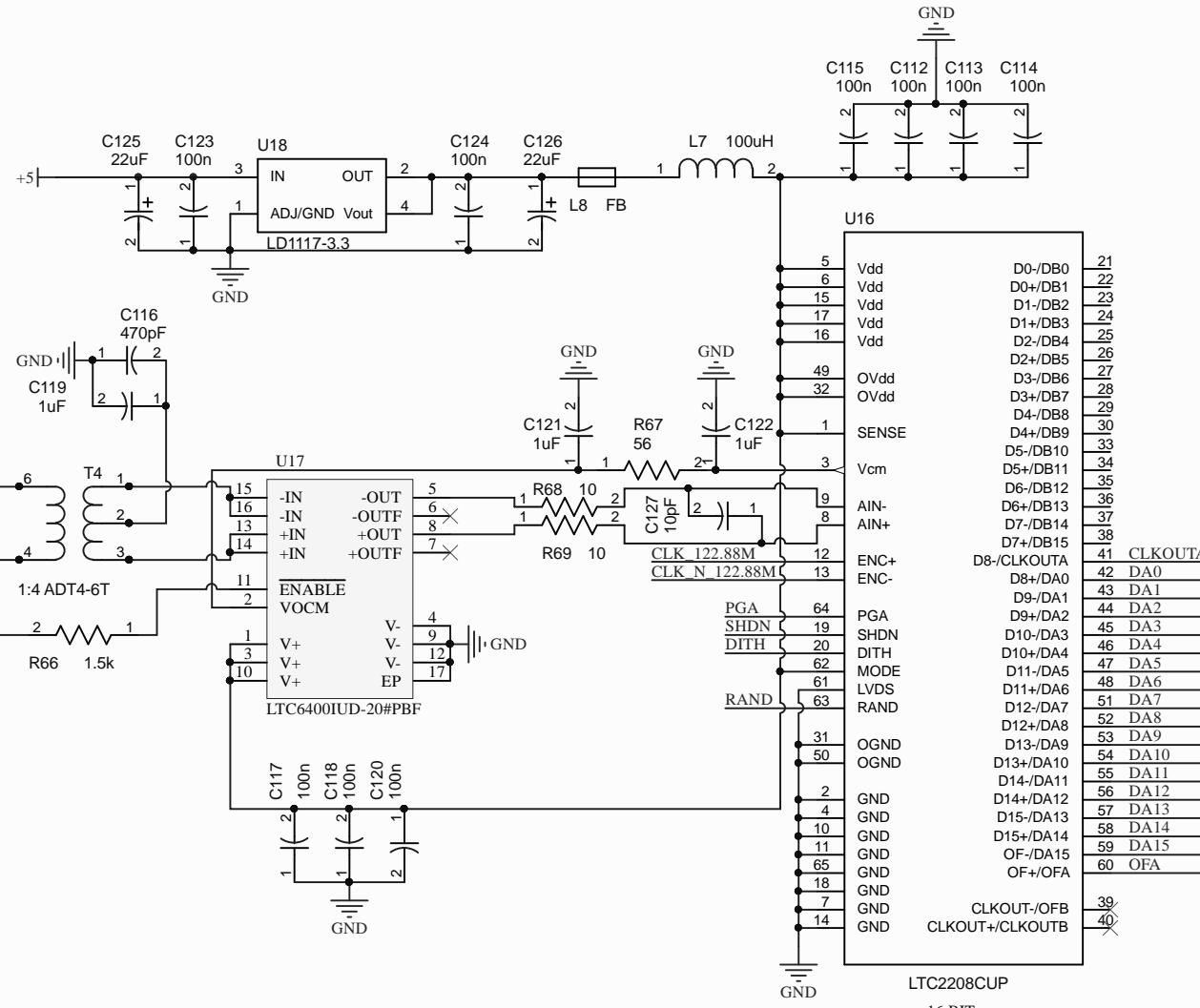
Title **AUDIO CODEC**

Size: A4 Number:5 Revision:2.0

Date: 25.02.2021 Time: 14:59:31 Sheet 5 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\AUDIO_CODEC.SchDoc

Altium

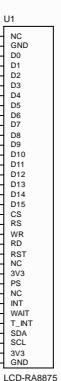
Title **ADC**

Size: A4 Number:3 Revision 2.0

Date: 25.02.2021 Time: 14:59:33 Sheet 3 of 18

File: D:\Dropbox\Develop\Projects\WOLF\Scheme\ALTIUM_DESIGNER\MOTHERBOARD\ADC.SchDoc

Altium



7Inch version

