

Pseudo-Random Noise Generation for FPGA with Python/Amaranth



Time & Frequency department

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slides and references at

https://github.com/oscimp/amaranth_twstft

Outline

- 1 About Pseudo-Random Noise
- 2 Python implementation
- 3 Amaranth implementation

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What is a PRN?

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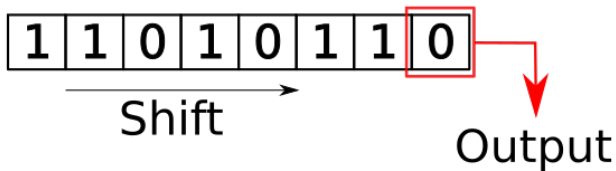
A signal with a repetition period long enough so it seems to be random

- N-periodic bit sequence
- Good auto-correlation properties
- Easy to differentiate from otherly generated PRNs

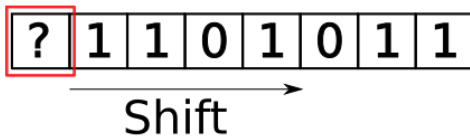
Linear Feedback Shift Register

1	1	0	1	0	1	1	0
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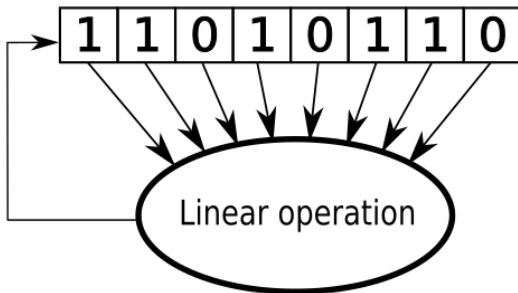
Linear Feedback Shift Register



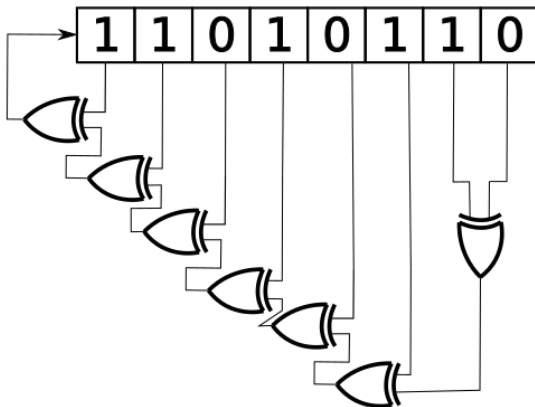
Linear Feedback Shift Register



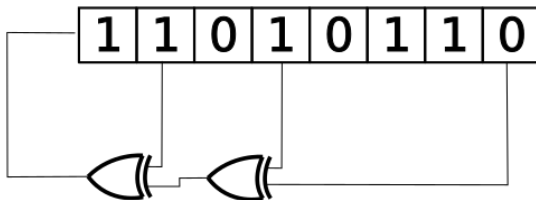
Linear Feedback Shift Register



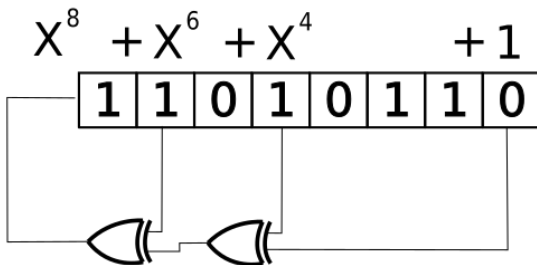
Linear Feedback Shift Register



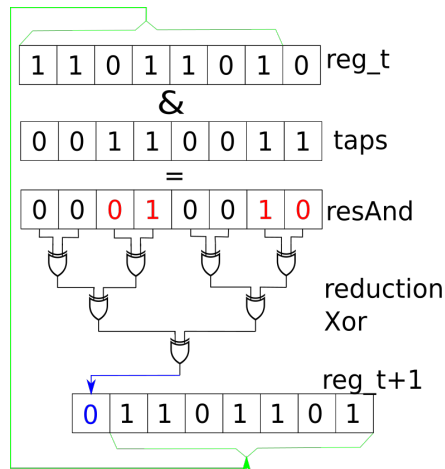
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In the end...

- Understand the concept of linear feedback shift register
- Code your own version of LFSR
 - with regular python
 - with amaranth for your FPGA