

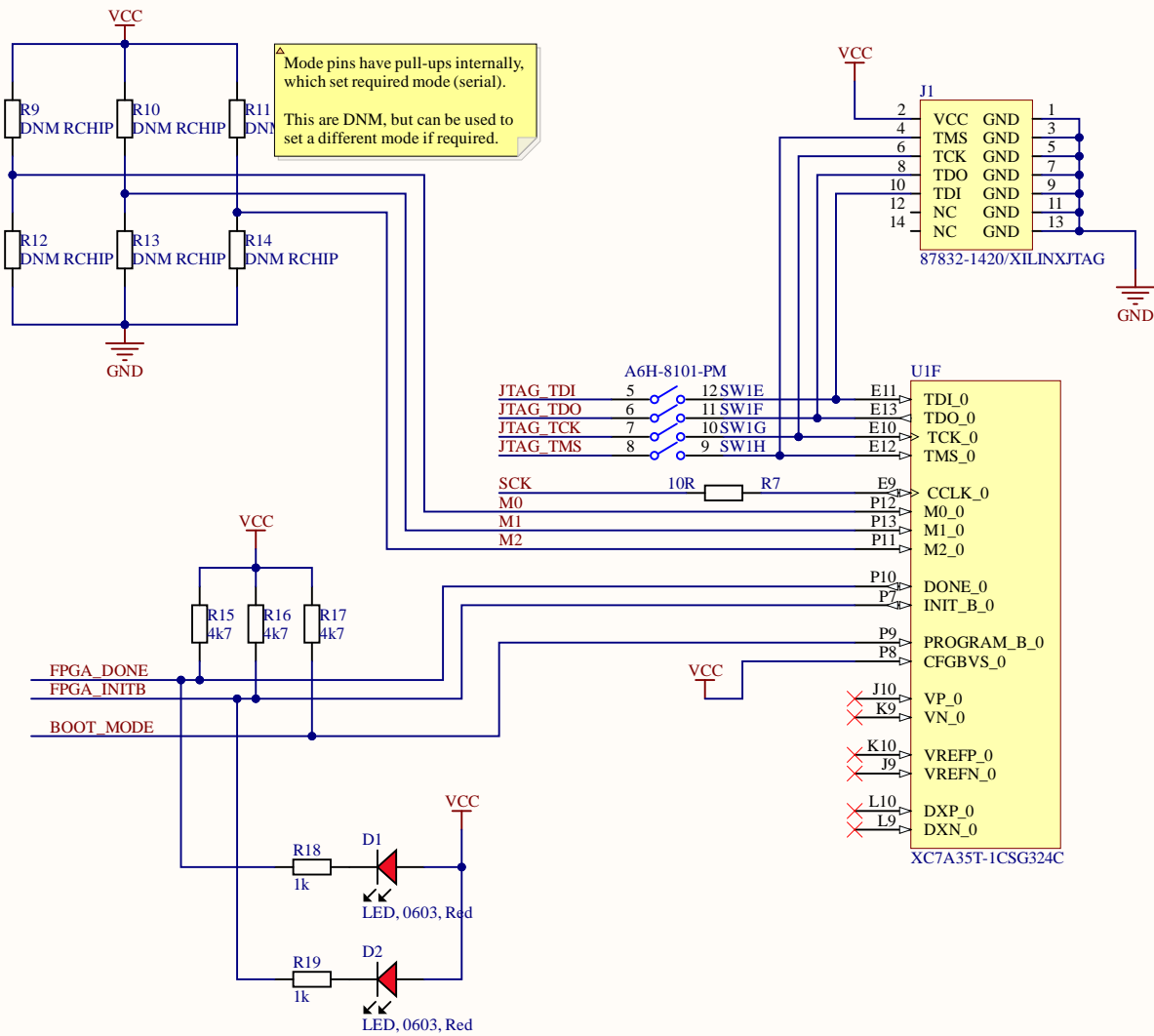


Title: <b>Power Rails</b>			 Approved: YES	
Rev: 04	Project: <b>CW31T A35 Target</b>		License: CC SA	
Date: 2023-03-27	Time: 8:20:57 PM	Sheet 2 of 4	Copyright © NewAE Technology Inc.	
File: cw312_xc7a35t_fpga_pwr.SchDoc				
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Title: **Configuration**

Approved: YES

Rev: 04

Project: CW31T A35 Target

License: CC SA

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Time: 8:20:57 PM

Sheet 3 of 4

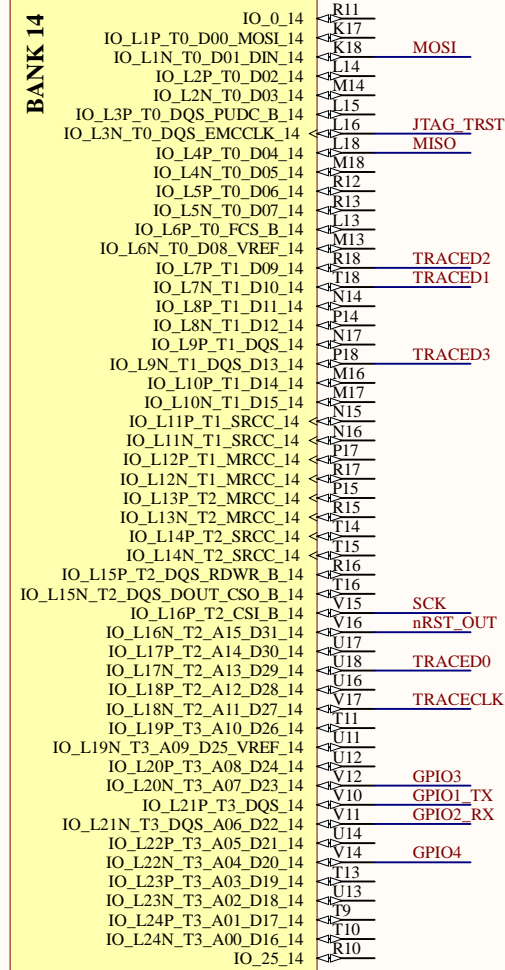
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File: cw312\_xc7a35t\_fpga\_cfg.SchDoc

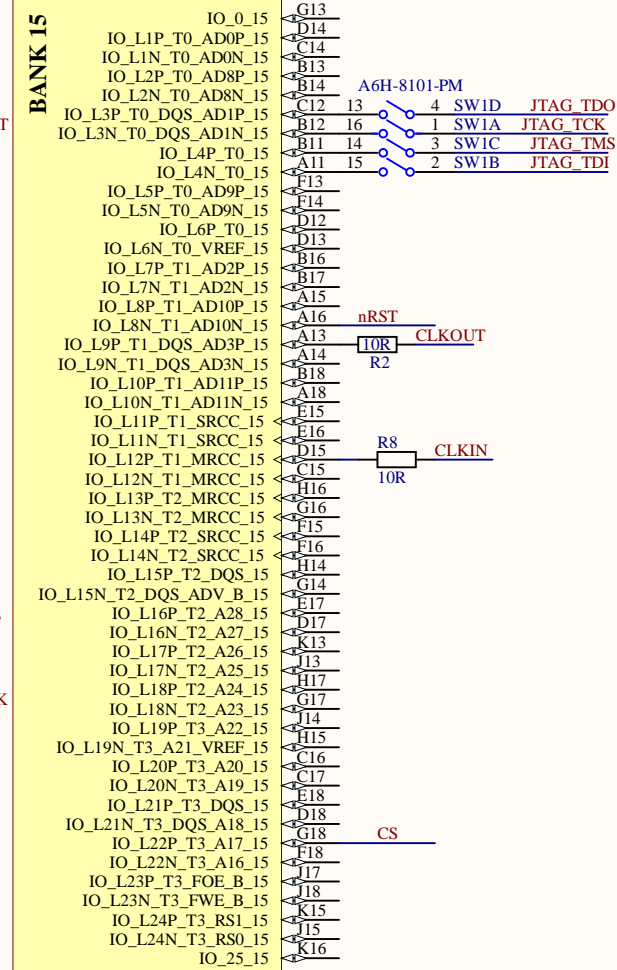


U1A



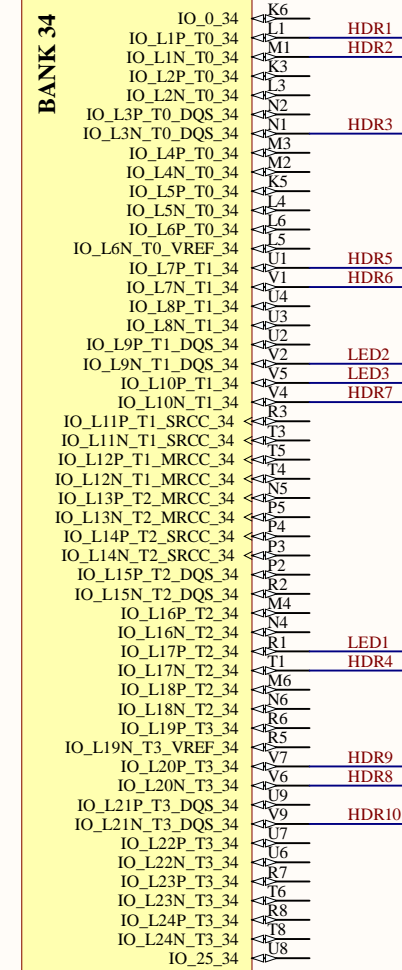
XC7A35T-ICSG324C

U1B



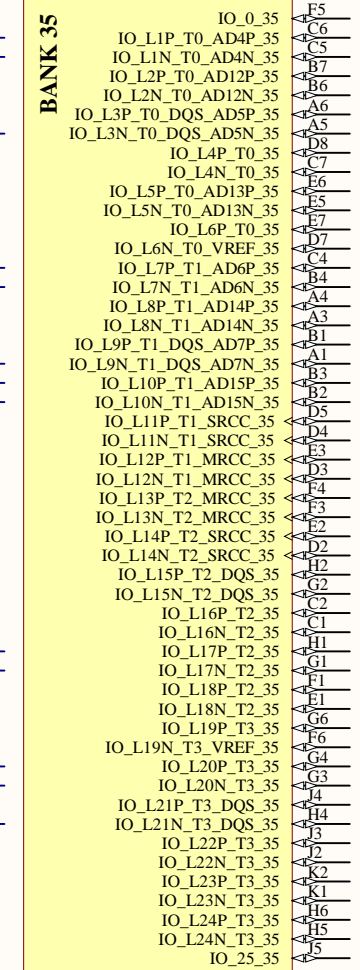
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U1D



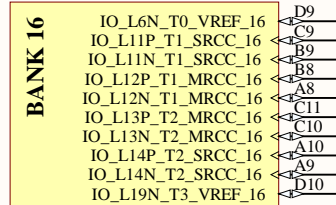
XC7A35T-ICSG324C

U1E



XC7A35T-ICSG324C

U1C



XC7A35T-ICSG324C

Bank 16 not on FTG256 package.  
Do not use pins here to keep  
bitstream compatibility with eventual  
FTG256.

Bank 35 not on FTG256 package.  
Do not use pins here to keep  
bitstream compatibility with eventual  
FTG256.

Title: I/O Pins

Approved: NO

Rev: 04

Project: CW31T A35 Target

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File: cw312\_xc7A35t\_fpga\_iol.SchDoc

