Title: **CW312 Connections**

Approved: NO

Rev: 03

Project: **CW31T A35 Target**

License: CC SA

Date: 2023-03-03

Time: 9:20:34 PM

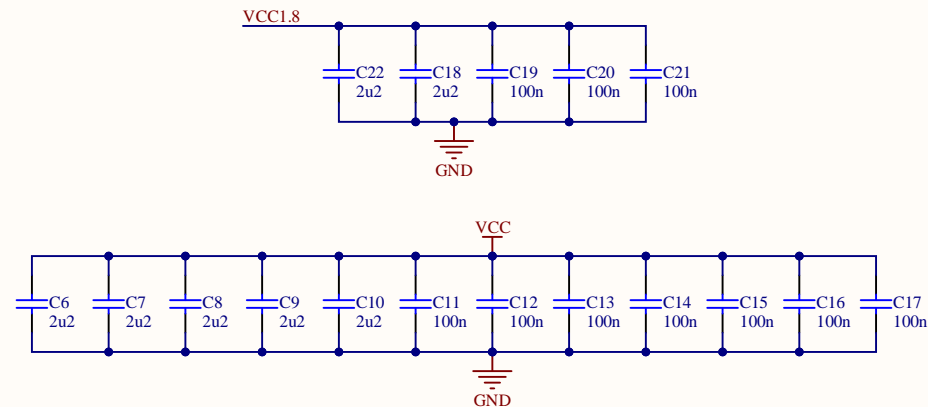
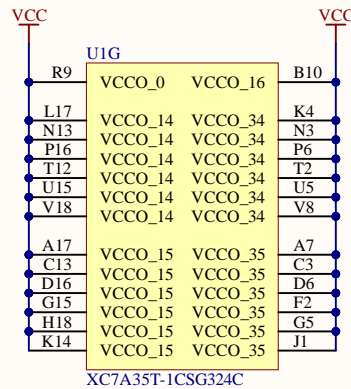
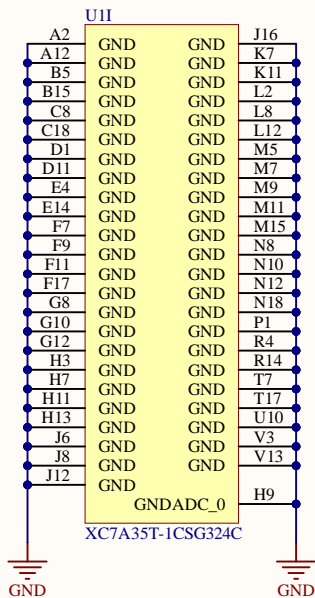
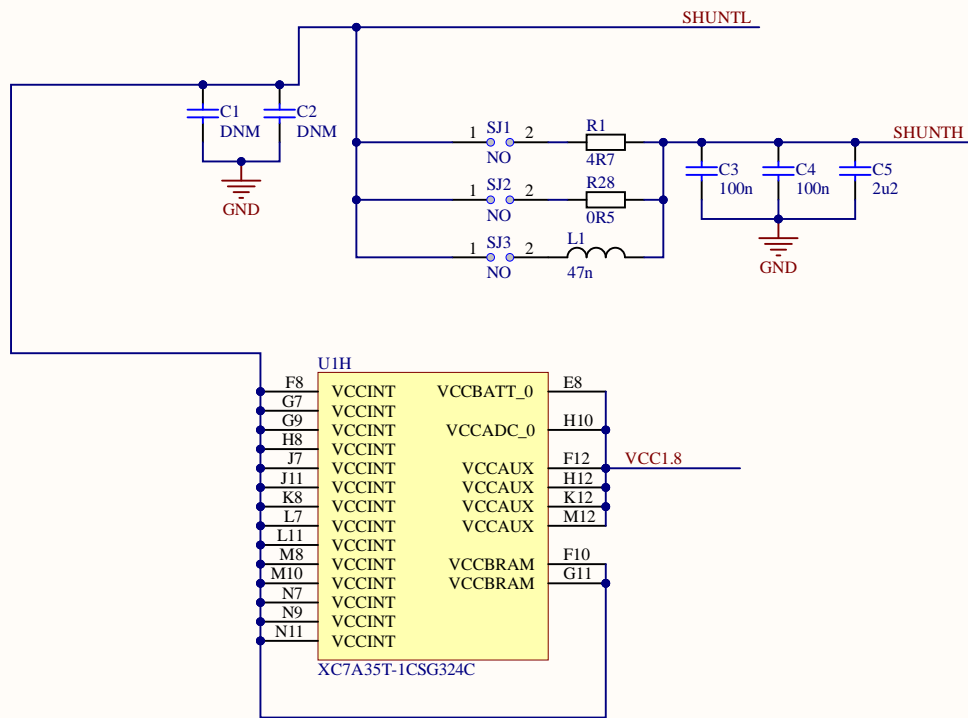
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FID1
FIDU-ROUND-1.0MM
FID2
FIDU-ROUND-1.0MM
FID3
FIDU-ROUND-1.0MM

Title: **Power Rails**

Approved: NO

Rev: 03

Project: **CW31T A35 Target**

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Date: 2023-03-03

Time: 9:20:34 PM

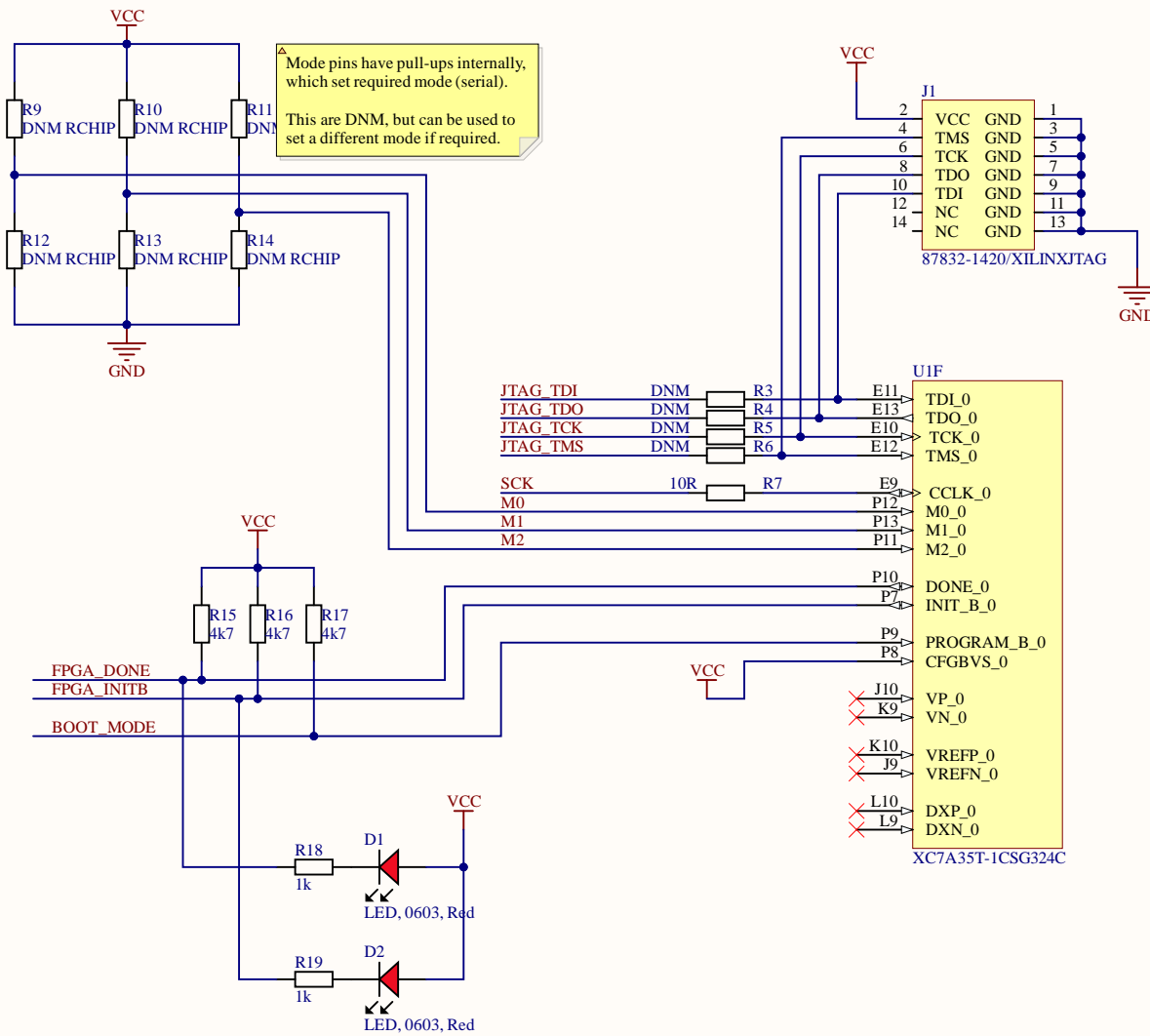
Sheet 2 of 4

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File: cw312_xc7a35t_fpga_pwr.SchDoc





Title: **Configuration**

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Project: CW31T A35 Target

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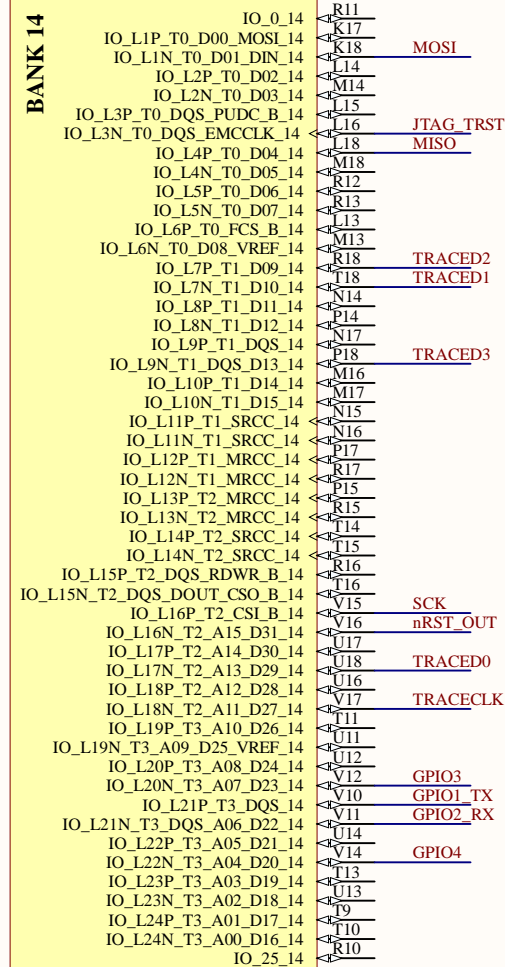
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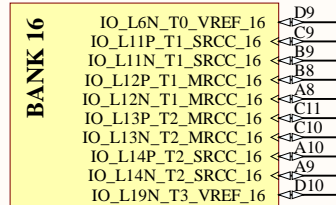


U1A



XC7A35T-ICSG324C

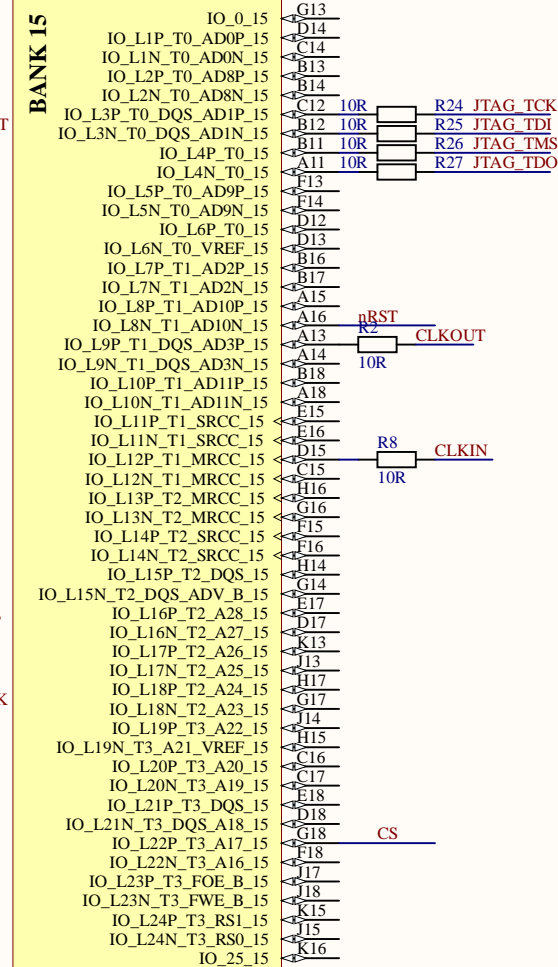
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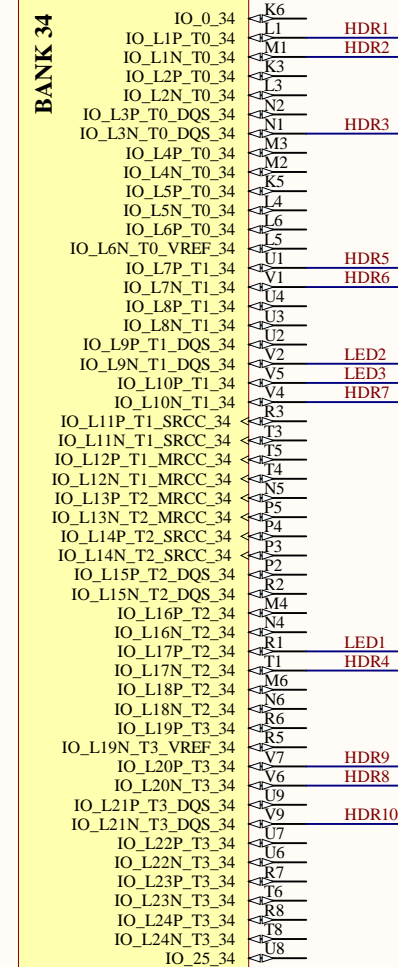
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Do not use pins here to keep
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U1B



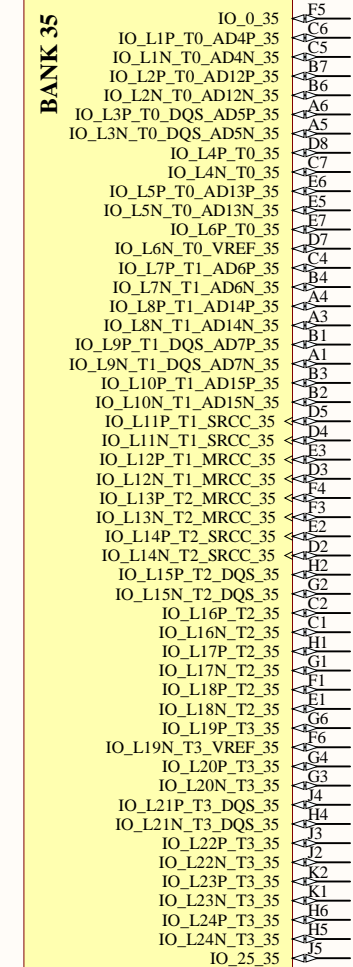
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U1D



XC7A35T-ICSG324C

U1E



XC7A35T-ICSG324C

Bank 35 not on FTG256 package.
Do not use pins here to keep
bitstream compatibility with eventual
FTG256.

Title: I/O Pins

Approved: NO

Rev: 03

Project: CW31T A35 Target

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Date: 2023-03-03

Time: 9:20:34 PM

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