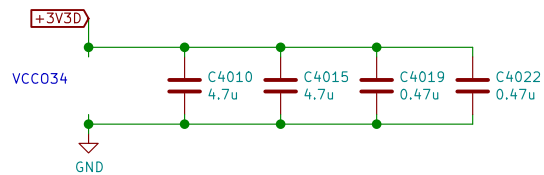
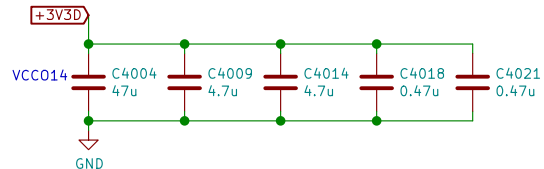
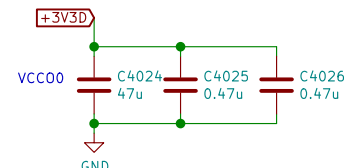
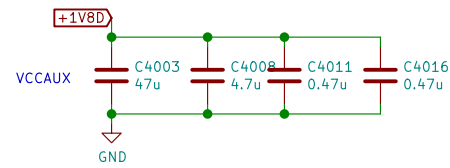
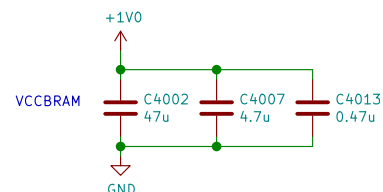
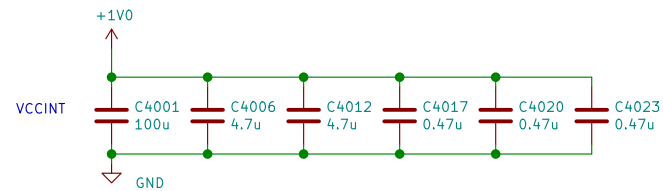


Bank 0: 3V3 (config)
Bank 14: 3V3 (due to config)
Bank 34: 3V3 (LVDS)
VCCINT: 1V0
VCCBRAM: 1V0
VCCAUX: 1V8



Decoupling Capacitors Recommendation from UG483, Table 2-1



Sheet: /FPGA/FPGA Power/
File: FPGA_Power.sch

Title:

Size: A4 Date:
KiCad E.D.A. kicad (5.0.1-3-g963ef8bb5)

Rev:
Id: 4/7

