

Sheet: SPI

File: spiSheet.sch

Sheet: FPGA

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Sheet: SRAM

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Sheet: Clock

File: clockSheet.sch

Sheet: Power

File: powerSheet.sch

Sheet: io

File: ioSheet.sch

Sheet: /
File: ice40hx1k.sch

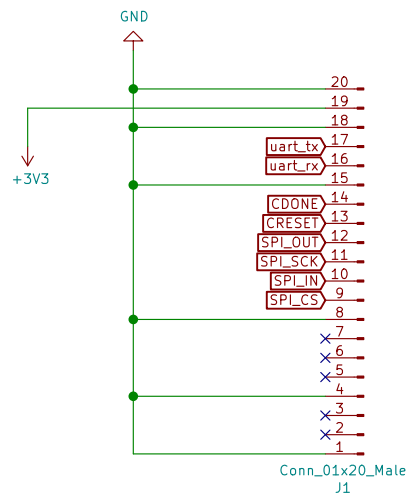
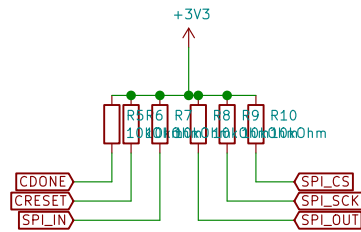
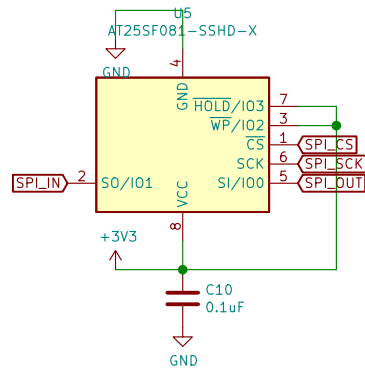
Title:

Size: A4

Date:

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KiCad E.D.A.	kiCad 5.99.0+really5.1.10+dfsg1-1

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Sheet: /SPI/
File: spiSheet.sch

Title:

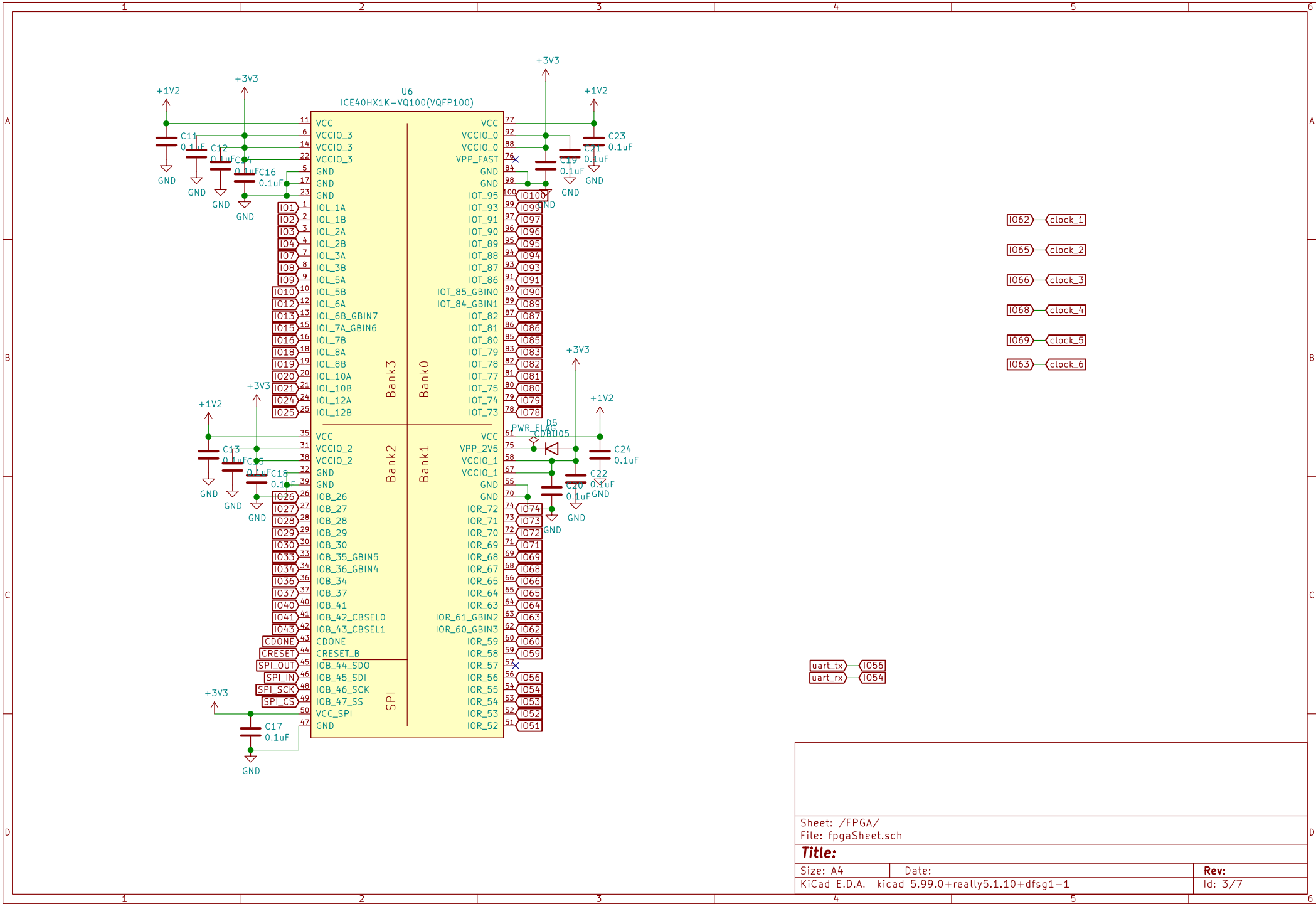
Size: A4

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KiCad E.D.A. kicad 5.99.0+really5.1.10+dfsg1-1

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Id: 2/7



Sheet: /FPGA/
File: fpgaSheet.sch

Title:

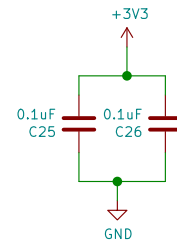
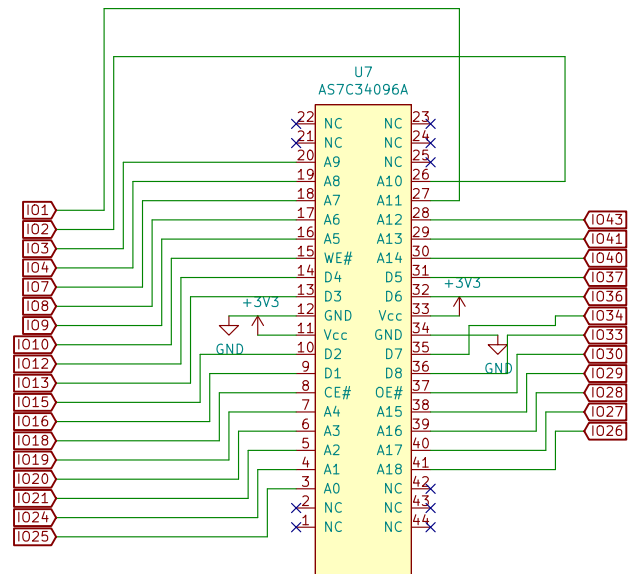
Size: A4

Date:

KiCad E.D.A. kicad 5.99.0+really5.1.10+dfsg1-1

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Sheet: /SRAM/
File: sramSheet.sch

Title:

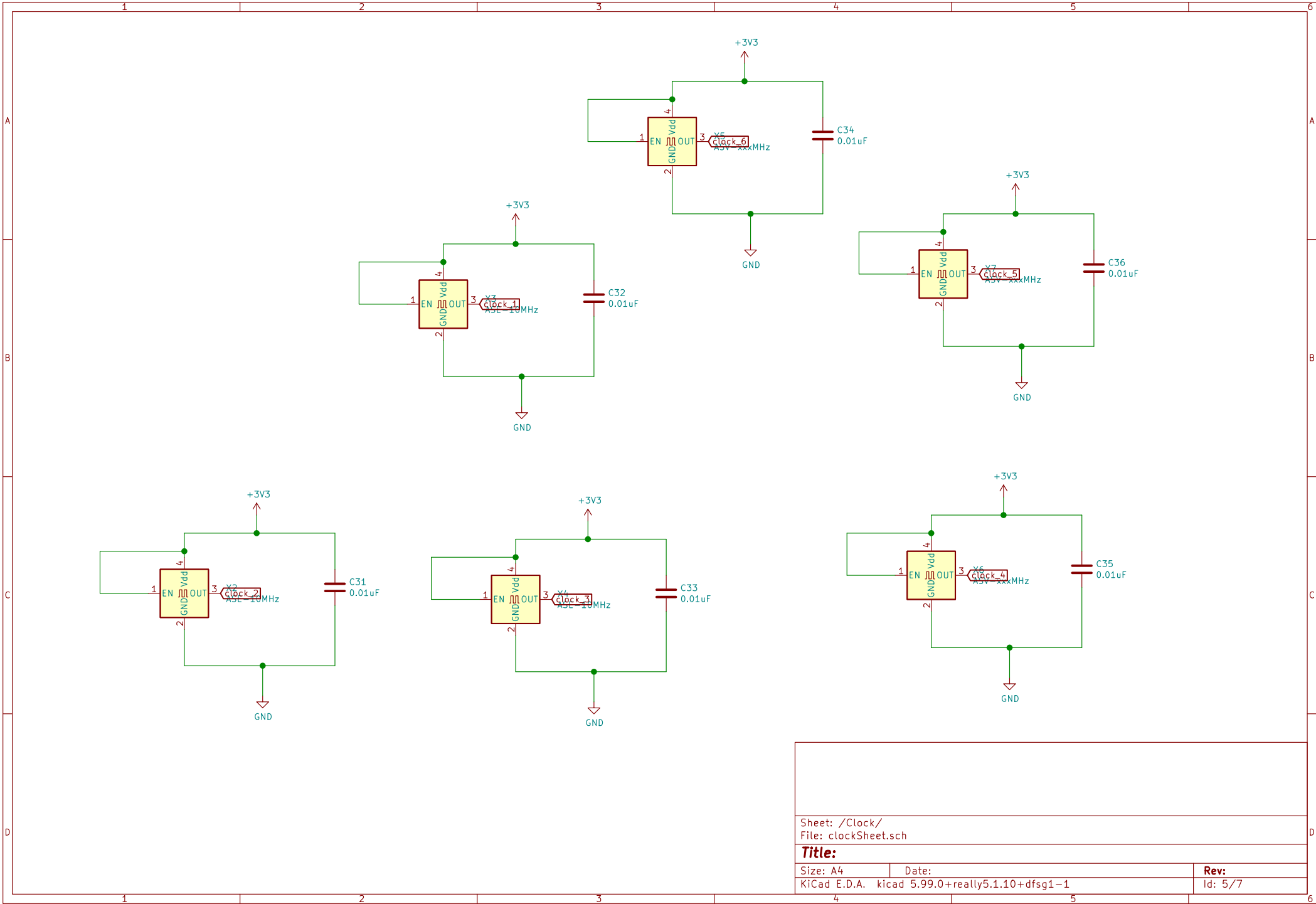
Size: A4

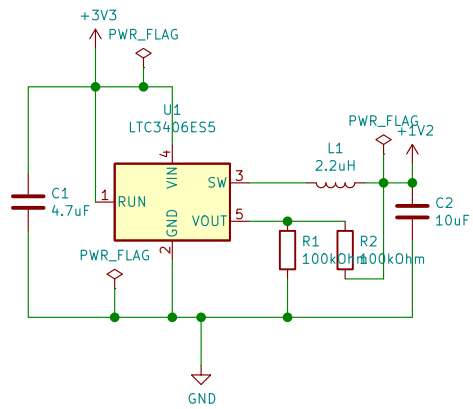
Date:

KiCad E.D.A. kicad 5.99.0+really5.1.10+dfsg1-1

Rev:

Id: 4/7





Sheet: /Power/
File: powerSheet.sch

Title:

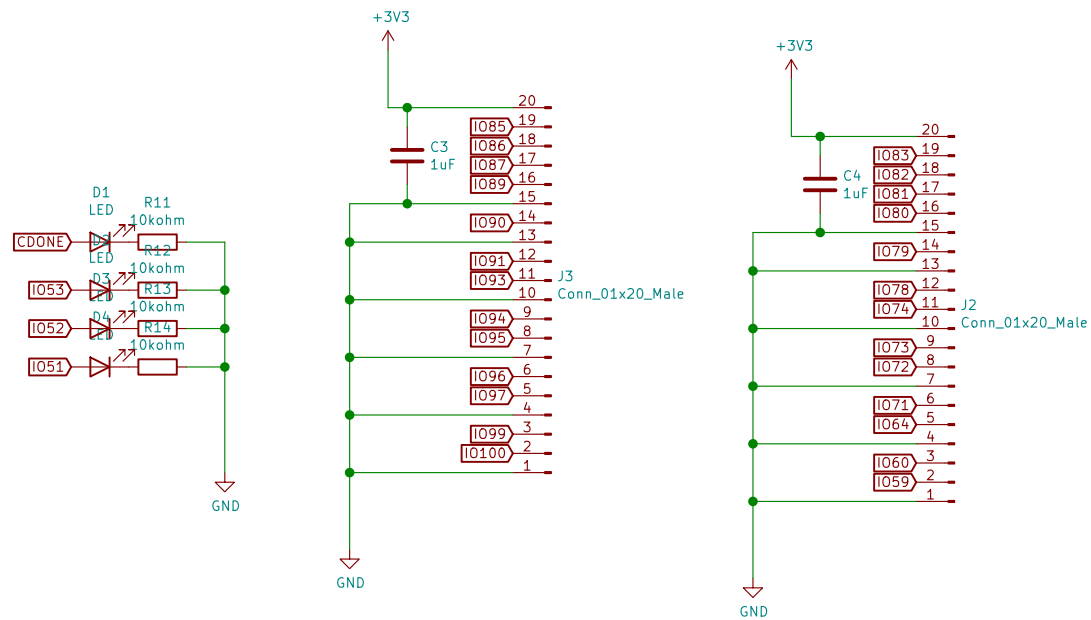
Size: A4

Date:

KiCad E.D.A. kicad 5.99.0+really5.1.10+dfsg1-1

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Sheet: /io/
File: ioSheet.sch

Title:

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