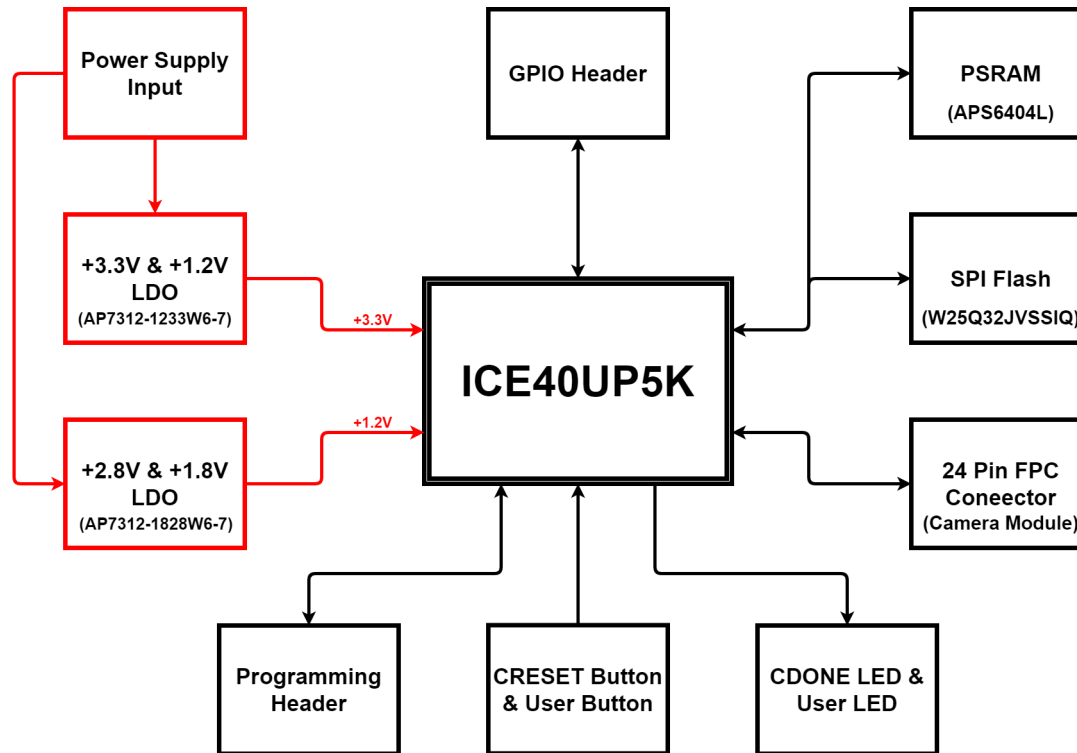


Block Diagram



Sheet: power_supply

File: power_supply.sch

Sheet: ICE40UP5K

File: ICE40UP5K_SG481TR.sch

Sheet: io_headers

File: io_headers.sch

Sheet: OV26400_spi-flash_SRAM

File: OV2640_spi-flash_SRAM.sch



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Sheet: /

File: fpga-play.sch

Title: fpga.play

Size: A4 Date: 2020-09-04

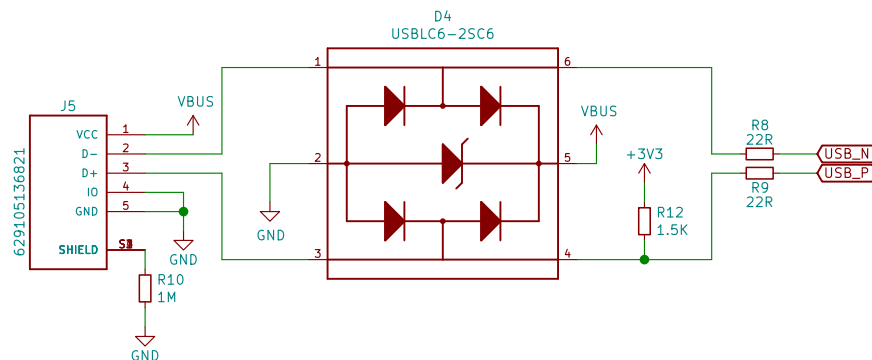
KiCad E.D.A. kicad (5.1.6)-1

Rev: 0.1

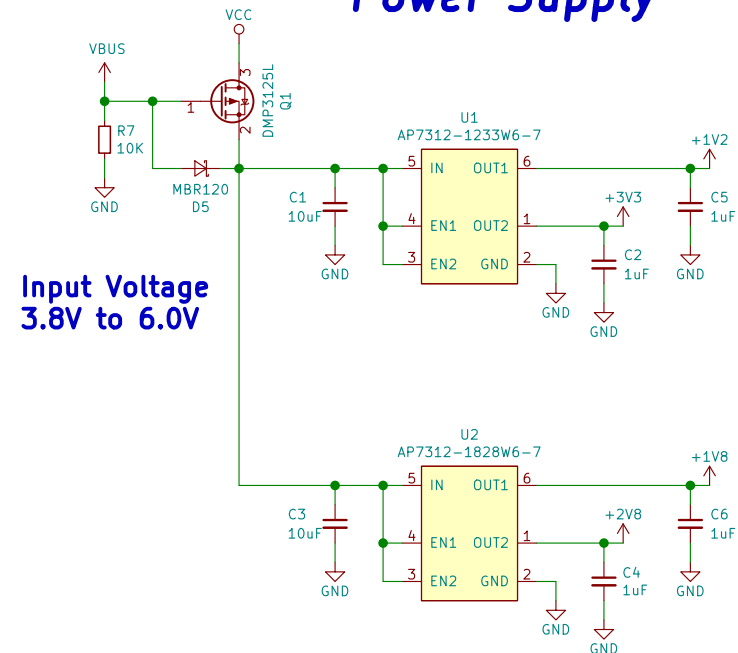
Id: 1/5



USB Bootloader



Power Supply



Input Voltage
3.8V to 6.0V



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Sheet: /power_supply/
File: power_supply.sch

Title: fpga.play

Size: A4 Date: 2020-09-04

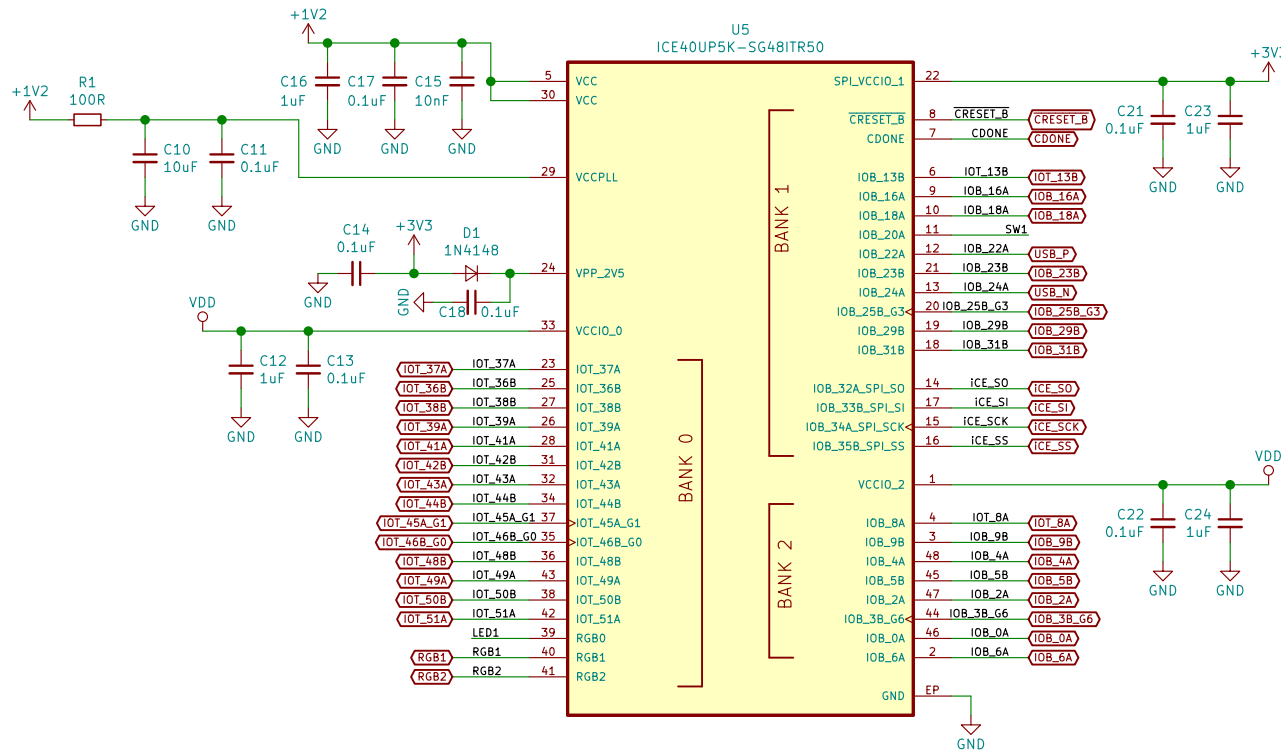
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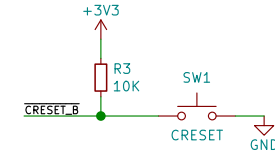
Rev: 0.1

Id: 2/5

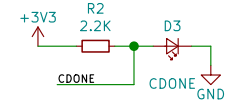
ICE40UP5K



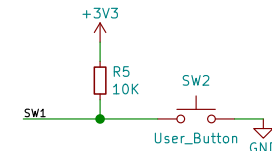
CRESET Button



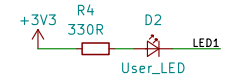
CDONE LED



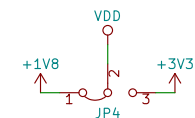
User Button



User LED

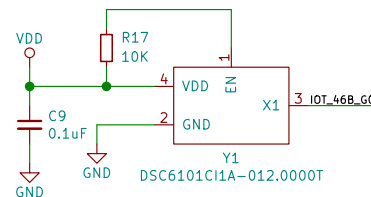


Bank 0&2 Voltage Selection



Note:
While using the camera jumper must be on pin 1 and 2.

Crystal Osillator



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Sheet: /ICE40UP5K/
File: ICE40UP5K_SG481TR.sch

Title: fpga.play

Size: A4 Date: 2020-09-04

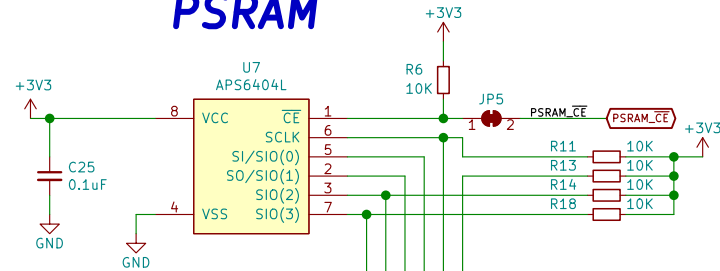
KiCad E.D.A. kicad (5.1.6)-1

Rev: 0.1

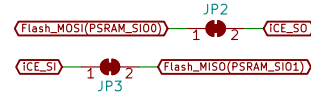
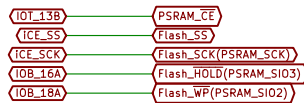
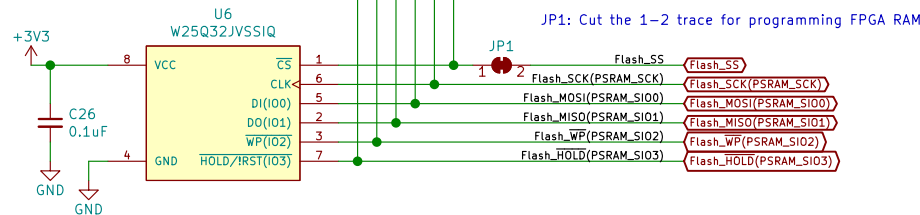
Id: 3/5



PSRAM



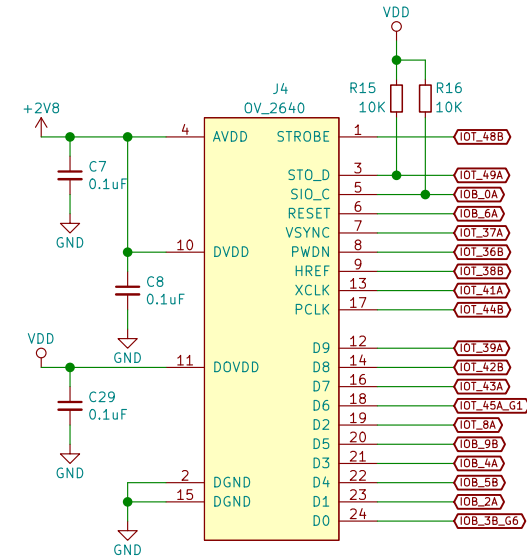
Flash Memory



== For programming Flash
 || For programming FPGA RAM

For programming FPGA RAM directly cut the 1-2 traces from JP2 and JP3 and connect them as 1-1 and 2-2.

24 Pin FPC Connector



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Sheet: /OV26400_spi-flash_SRAM/
 File: OV26400_spi-flash_SRAM.sch

Title: fpga.play

Size: A4 Date: 2020-09-04

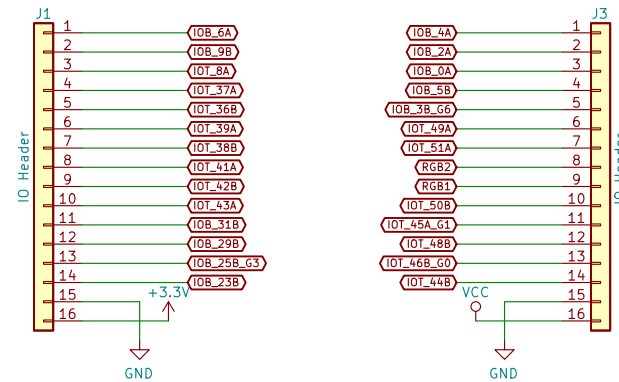
KiCad E.D.A. kicad (5.1.6)-1

Rev: 0.1

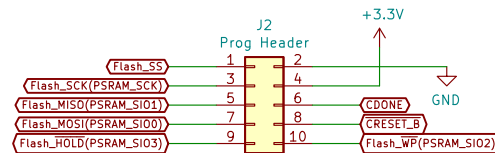
Id: 4/5



GPIO Header



Programming Header



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Sheet: /io_headers/
File: io_headers.sch

Title: fpga.play

Size: A4 Date: 2020-09-04

KiCad E.D.A. kicad (5.1.6)-1

Rev: 0.1

Id: 5/5

