

# **GOLDS-UFSC Documentation**

GOLDS-UFSC Documentation SpaceLab, Universidade Federal de Santa Catarina, Florianópolis - Brazil

#### **GOLDS-UFSC Documentation**

January, 2021

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#### **Revision Control:**

Version	Author	Changes	Date
0.1	Gabriel M. Marcelino	Document creation	2020/06/05



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## Nomenclature

**ADCS** Attitude Determination and Control System.

**EDC** Environmental Data Collection.

**EPS** *Electrical Power System.* 

**OBDH** *On-Board Data Handling.* 

TTC Telemetry, Tracking and Command Module.

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### Introduction

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## 1.1 Mission Description

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## 1.2 Mission Objectives

- 1. To serve as a host platform for the EDC payload.
- 2. Validate the EDC payload in orbit.
- 3. Validate EDC functionality in orbit.
- 4. Validate core-satellite functions in orbit.
- 5. Evaluate the behavior of the core modules.
- 6. Perform experiments on radiation effects in electronic components in orbit.
- 7. Serve as relay for amateur radio communications.

### Mission Requirements

- 1. The power system shall be able to harvest solar energy.
- 2. The power system shall be able to store energy for use when GOLDS-UFSC is eclipsed.
- 3. The power system shall supply energy to all other modules.
- 4. The data handling system shall communicate with the other modules and store their data.
- 5. The communications system shall send a beacon signal periodically using VHF radio.
- 6. The communications system shall send the CubeSat telemetry using UHF radio.
- 7. The communications system shall be able to receive telecommands and respond to them accordingly.
- 8. The attitude system shall be able to perform a 1-axis stabilization of the CubeSat.
- 9. GOLDS-UFSC shall have the capability to receive and execute a shutdown telecommand, therefore ceasing all transmissions.
- 10. The downlink transmissions shall be done once at a time, either telemetry or beacon.
- 11. The ground station shall operate under the proper radio frequency communication licenses.
- 12. GOLDS-UFSC shall comply with international and Brazilian radio license agreements and restrictions.
- 13. The team shall build and operate a ground station for full communication with GOLDS-UFSC.

## Mission Schedule

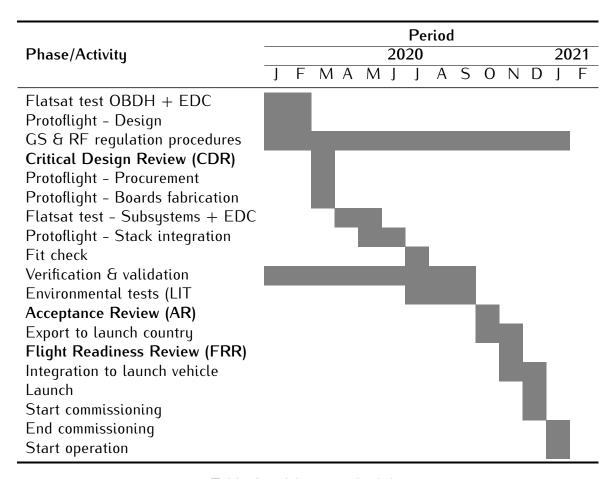


Table 3.1: Mission schedule.

## **Overall Description**

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## 4.1 General Diagrams

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### 4.2 General Behaviour

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### 4.3 Orbit Parameters

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## 4.4 Power Budget

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## 4.5 Link Budget

#### 4.5.1 VHF Link

• Direction: Downlink

• Frequency: 145,97 MHz

• Modulation: MSK

• Datarate: 1200 bps

• Output Power: 30 dBm (1 W)

• Protocol: NGHam

#### 4.5.2 UHF Links

#### Main UHF Link

• Direction: Downlink and uplink

• Frequency: 436,9 MHz

• Modulation: MSK

• Datarate: 4800 bps

• Output power: 30 dBm (1 W)

• Protocol: NGHam

#### **EDC UHF Link**

• Direction: Uplink

• Frequency: 401.635 MHz

• Modulation: ????

• Datarate: ???? bps

## 4.6 PC-104 Bus

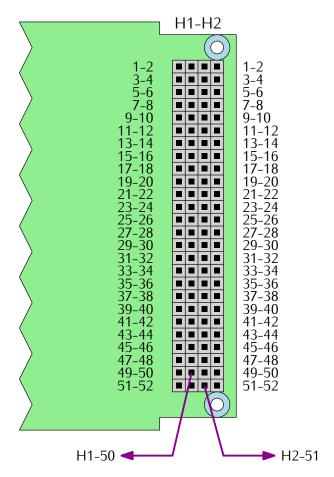


Figure 4.1: Reference diagram of the PC-104 bus.

Pin Row	H1 Odd	H1 Even	H2 Odd	H2 Even
1-2	-	_	-	_
3-4	_	_	EDC_1_EN	EDC_2_EN
5-6	-	_	BE_UART_RX	_
7-8	RA_GPIO_0	RA_GPIO_1	BE_UART_TX	GPIO_0
9-10	RA_GPIO_2	_	_	_
11-12	RA_RESET	RA_EN	BE_SPI_MOSI	BE_SPI_CLK
13-14	-	_	BE_SPI_CS	BE_SPI_MISO
15-16	-	_	_	_
17-18	EDC_UART_RX/TX	PLX_EN	-	GPIO_1
19-20	EDC_UART_TX/RX	GPIO_2	-	GPIO_3
21-22	-	_	-	GPIO_4
23-24	-	_	-	_
25-26	-	_	-	-
27-28	-	_	-	-
29-30	GND	GND	GND	GND
31-32	GND	GND	GND	GND
33-34	-	_	-	-
35-36	RD_SPI_CLK	_	ANT_VCC	ANT_VCC
37-38	RD_SPI_MISO	_	-	-
39-40	RD_SPI_MOSI	RD_SPI_CS	-	-
41-42	PL_I2C_SDA	_	-	GPIO_5
43-44	PL_I2C_SCL	_	-	-
45-46	OBDH_VCC	OBDH_VCC	BAT_VCC	BAT_VCC
47-48	EDC_VCC	EDC_VCC	-	-
49-50	RD_VCC	RD_VCC	EPS_I2C_SDA	-
51-52	BE_VCC	BE_VCC	EPS_I2C_SCL	-

Table 4.1: PC-104 bus pinout.

Signal	Pin(s)	Used By	Description
GND	H1-29, H1-30,	All	Ground reference
	H1-31, H1-32,		
	H2-29, H2-30,		
BAT_VCC	H2-31, H2-32 H2-45, H2-46	EPS	Dattary tarminals (1)
ANT_VCC	H2-35, H2-36	EPS, ANT	Battery terminals (+) Antenna power supply (3.3 V)
OBDH_VCC	H1-45, H1-46	EPS, OBDH	OBDH power supply (3.3 V)
EDC_VCC	H1-47, H1-48	EPS, EDC 1,	EDC power supply (5 V)
LDC_VCC	111 17,111 10	EDC 2	LDC power supprig (5 V)
RD_VCC	H1-49, H1-50	EPS, TTC	Main radio power supply (5 V)
BE_VCC	H1-51, H1-52	EPS, TTC	Beacon power supply (6 V)
RD_SPI_CLK	H1-35	OBDH, TTC	CLK signal of the main radio SPI bus
RD_SPI_MISO	H1-37	OBDH, TTC	MISO signal of the main radio SPI bus
RD_SPI_MOSI	H1-39	OBDH, TTC	MOS signal of the main
ND_31 1_10031	111-33	ODDII, ITC	radio SPI bus
RD_SPI_CS	H1-40	OBDH, TTC	CS signal of the main radio
5_000		00011, 110	SPI bus
EPS_I2C_SDA	H2-49	OBDH, EPS	SDA signal of the EPS I2C
			bus
EPS_I2C_SCL	H2-51	OBDH, EPS	SCL signal of the EPS I2C bus
BE_UART_RX	H2-5	EPS, TTC	EPS TX, Beacon RX (UART bus)
BE_UART_TX	H2-7	EPS, TTC	EPS RX, Beacon TX (UART
EDC UART TX/RX	H1-25	OBDH, EDC	bus) OBDH TX, EDCs RX (UART
	111 25	1, EDC 2	bus)
EDC_UART_RX/TX	H1-27	OBDH, EDC	OBDH RX, EDCs TX (UART
,		1, EDC 2	bus)
EDC_1_EN	H2-3	OBDH, EDC	EDC 1 enable signal
		1	-
EDC_2_EN	H2-4	OBDH, EDC	EDC 2 enable signal
		2	
PLX_EN	H1-18	OBDH,	Payload X enable (GPIO)
DL 120 CD4	114 44	Payload X	
PL_I2C_SDA	H1-41	OBDH,	SDA signal of the payload
PL_I2C_SCL	H1-43	Payload X OBDH,	I2C bus
1 L_12C_3CL	עד-ווו	Payload X	SCL signal of the payload I2C bus
GPIO_N	H2-8, H2-18,	OBDH	GPIO pin (not used)
GI 10_1V	H1-20, H2-20, H2-22, H2-42		a. To pur (not useu)

Table 4.2: PC-104 bus signal description.

**Subsystems** 

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## 5.1 On-Board Data Handling

OBDH [1]

## 5.2 Telemetry, Tracking and Command Module

TTC [2]

## 5.3 Electrical Power System

**EPS** [3]

### 5.3.1 Battery Module

[4]

## 5.4 Attitude Determination and Control System

**ADCS** 

#### 5.5 Mechanical Structure

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### 5.6 Interconnection Modules

#### 5.6.1 PC-104 Interconnection Boards

[5]

## 5.6.2 External Connection Boards

[6]

## 5.7 Payloads

## 5.7.1 Environmental Data Collection

EDC [7]

## Test Plan and Results

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# **Ground Segment**

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# Operation Planning

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