## iCEBreaker Bitsy v1.1a

Open Source FPGA development board icebreaker-fpga.org

Pin Name FPGA Package Pin Number **FPGA Pin Name** Differential Pair 3.3V (500mA total) /<sub>IN</sub> (1.45 to 5.5 V) RGB[0-2]  $\mathsf{C}_{\mathsf{DONE}}$  $C_{\mathsf{RESET}}$ GND 00 N 3.3V [0B\_16a [0B\_18a .0B\_22a 10B\_9b 6a . . .

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