



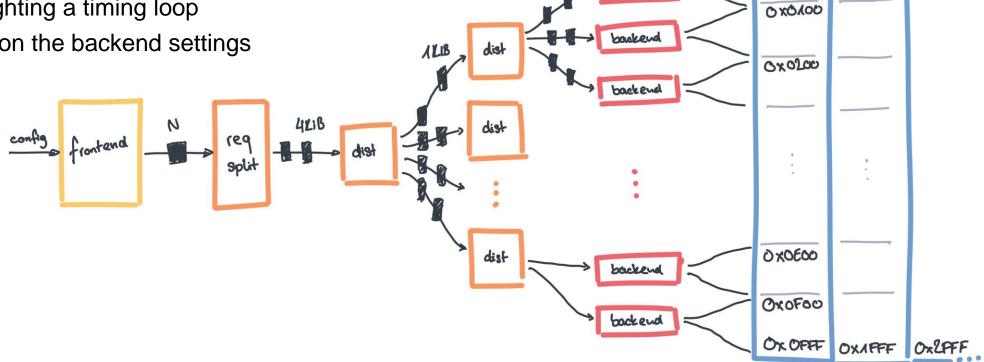
## **DMA**

- All DMA modules are implemented and tested
- Writing is in progress



We are fighting a timing loop

Depends on the backend settings





0x0000

bodeend

0x2000

OX 1000