pertise Applied | Answers Delivered

TVS Diode Arrays (SPA® Diodes)

General Purpose ESD Protection - SP1001 Series

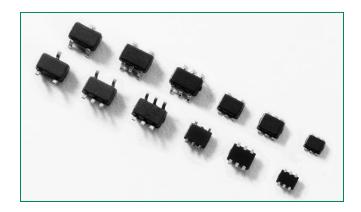
SP1001 Series - 8pF 15kV Unidirectional TVS Array

AUTOMOTIVE GRADE

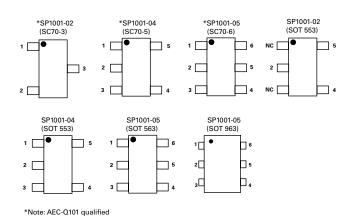
RoHS



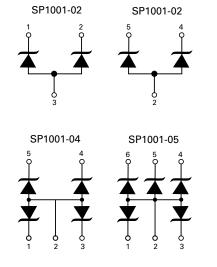
GREEN



Pinout



Functional Block Diagram



Life Support Note:

Not Intended for Use in Life Support or Life Saving Applications

The products shown herein are not designed for use in life sustaining or life saving applications unless otherwise expressly indicated.

Description

Zener diodes fabricated in a proprietary silicon avalanche technology protect each I/O pin to provide a high level of protection for electronic equipment that may experience destructive electrostatic discharges (ESD). These robust diodes can safely absorb repetitive ESD strikes above the maximum level specified in the IEC 61000-4-2 international standard (Level 4, ±8kV contact discharge) without performance degradation. Their very low loading capacitance also makes them ideal for protecting highspeed signal pins.

Features

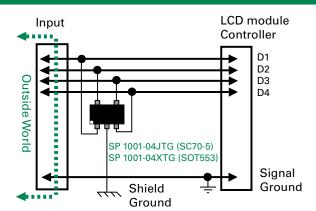
- Low capacitance of 8pF (TYP) per I/O
- ESD protection of ±15kV contact discharge, ±30kV air discharge, (Level 4, IEC 61000-4-2)
- EFT protection, IEC 61000-4-4, 40A (5/50ns)
- Low leakage current of

- 0.5 to 0.1µA (MAX) at 5V
- Small package saves board space
- Lightning protection, IEC 61000-4-5, 2nd edition 2A (8/20µs)
- AEC-Q101 qualified (SC70-x packages)
- RoHS compliant and leadfree

Applications

- Computer Peripherals
- Mobile Phones
- Digital Cameras
- Desktops/Notebooks
- LCD/PDPTVs
- Set Top Boxes
- DVD Players
- MP3/PMP

Application Example



Additional Information







Samples



Absolute Maximum Ratings

Symbol	Parameter	Value	Units
I _{PP}	Peak Current (t _p =8/20µs)	2	А
T _{OP}	Operating Temperature	-40 to 125	°C
T _{STOR}	Storage Temperature	-55 to 150	°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

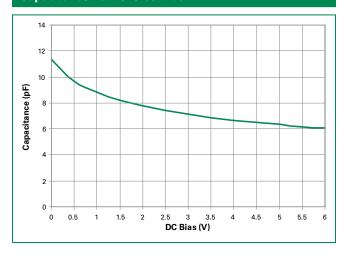
Thermal Information		
Parameter	Rating	Units
Storage Temperature Range	-55 to 150	°C
Maximum Junction Temperature	150	°C
Maximum Lead Temperature (Soldering 20s-40s)	260	°C

Electrical Characteristics (T_{OP} = 25°C)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Forward Voltage Drop	V _F	I _F =10mA	0.7	0.9	1.2	V
Reverse Voltage Drop	V _R	I _R =1mA	6.0		8.5	V
Reverse Standoff Voltage	V _{RWM}	I _R ≤1μA			5.5	V
Reverse Leakage Current	I _{LEAK}	V _R =5V			0.1	μА
Clamp Voltage ¹	\/	$I_{pp}=1A, t_p=8/20 \mu s, Fwd$		8.0	11.0	V
Clamp voltage	V _C	I_{pp} =2A, t_p =8/20µs, Fwd		9.7	13.0	V
Dynamic Resistance	R _{DYN}	(V _{C2} - V _{C1}) / (I _{PP2} - I _{PP1})		1.7		Ω
FCD With stand Valtage 12	N/	IEC61000-4-2 (Contact)	±15			kV
ESD Withstand Voltage ^{1,2}	V _{ESD}	IEC61000-4-2 (Air)	±30			kV
		Reverse Bias=0V		12		pF
Diode Capacitance ¹	C _D	Reverse Bias=2.5V		8		pF
		Reverse Bias=5V		7		pF

Notes

Capacitance vs. Reverse Bias



Design Consideration

Because of the fast rise-time of the ESD transient, placement of ESD devices is a key design consideration. To achieve optimal ESD suppression, the devices should be placed on the circuit board as close to the source of the ESD transient as possible. Install the ESD suppressors directly behind the connector so that they are the first board-level circuit component encountered by the ESD transient. They are connected from signal/data line to ground.

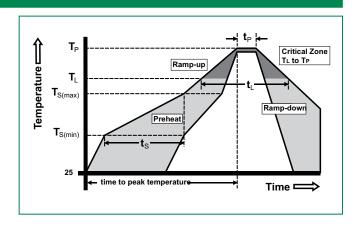
¹ Parameter is guaranteed by device characterization

²A minimum of 1,000 ESD pulses are applied at 1s intervals between the anode and common cathode of each diode

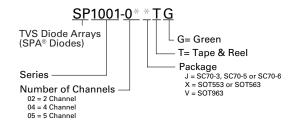


Soldering Parameters

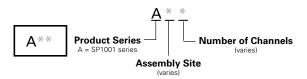
Reflow Co	ndition	Pb – Free assembly
	-Temperature Min (T _{s(min)})	150°C
Pre Heat	-Temperature Max (T _{s(max)})	200°C
	-Time (min to max) (t _s)	60 – 180 secs
Average ra	amp up rate (Liquidus)Temp k	3°C/second max
T _{S(max)} to T _L	- Ramp-up Rate	3°C/second max
Reflow	-Temperature (T _L) (Liquidus)	217°C
Reliow	-Temperature (t _L)	60 – 150 seconds
PeakTemp	erature (T _P)	260+0/-5 °C
Time with	in 5°C of actual peak ure (t _p)	20 - 40 seconds
Ramp-dov	vn Rate	6°C/second max
Time 25°C	to peakTemperature (T _P)	8 minutes max.
Do not exc	ceed	260°C



Part Numbering System



Part Marking System



Part Number	Package

Ordering Information

Part Number	Package	Marking	Min. Order Qty.
SP1001-02JTG	SC70-3	A*2	3000
SP1001-02XTG	SOT553	A*2	3000
SP1001-04JTG	SC70-5	A*4	3000
SP1001-04XTG	SOT553	A*4	3000
SP1001-05JTG	SC70-6	A*5	3000
SP1001-05VTG	SOT963	A*5	8000
SP1001-05XTG	SOT563	A*5	3000

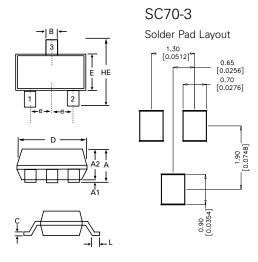
Product Characteristics

Lead Plating	Matte Tin (SC70-x) Pre-Plated Frame (SOT5x3, SOT963)
Lead Material	Copper Alloy
Lead Coplanarity	0.0004 inches (0.102mm)
Substitute Material	Silicon
Body Material	Molded Epoxy
Flammability	UL 94 V-0

- 1. All dimensions are in millimeters
- 2. Dimensions include solder plating.
- 3. Dimensions are exclusive of mold flash & metal burr.
- 4. Blo is facing up for mold and facing down for trim/form, i.e. reverse trim/form.
- 5. Package surface matte finish VDI 11-13.



Package Dimensions — SC70



SC70-5

Solder Pad Layout

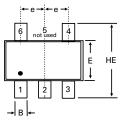
Package		SC7	70-3	
Pins		:	3	
JEDEC		MO-	-203	
	Millin	neters	Incl	hes
	Min	Max	Min	Max
Α	0.80	1.10	0.031	0.043
A1	0.00	0.10	0.000	0.004
A2	0.70	1.00	0.028	0.039
В	0.15	0.30	0.006	0.012
С	0.08	0.25	0.003	0.010
D	1.85	2.25	0.073	0.089
E	1.15	1.35	0.045	0.053
е	0.66	BSC	0.026	BSC
HE	2.00	2.40	0.079	0.094
L	0.26	0.46	0.010	0.018

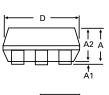
SC70-5

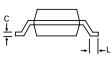
Max 0.043 0.004 0.039 0.012 0.010 0.089 0.053

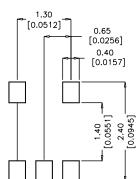
0.094

0.018







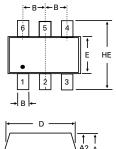


	Pins		Ę	5	
	JEDEC		MO-	-203	
0.65		Millin	neters	Inc	hes
[0.0256]		Min	Max	Min	M
0.40 [0.0157]	Α	0.80	1.10	0.031	0.0
	A1	0.00	0.10	0.000	0.0
Ì	A2	0.70	1.00	0.028	0.0
 }	В	0.15	0.30	0.006	0.0
1.40 [0.0551] 2.40 [0.0945]	С	0.08	0.25	0.003	0.0
1. [0.0] 2. [0.0]	D	1.85	2.25	0.073	0.0
	E	1.15	1.35	0.045	0.0
	е	0.65	BSC	0.026	BSC
	HE	2.00	2.40	0.079	0.0

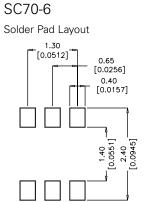
0.26

Package

L





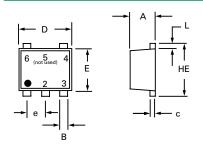


Package		SC7	70-6	
Pins		(6	
JEDEC		MO-	-203	
	Millin	neters	Inc	hes
	Min	Max	Min	Max
Α	0.80	1.10	0.031	0.043
A 1	0.00	0.10	0.000	0.004
A2	0.70	1.00	0.028	0.039
В	0.15	0.30	0.006	0.012
С	0.08	0.25	0.003	0.010
D	1.85	2.25	0.073	0.089
E	1.15	1.35	0.045	0.053
е	0.65	BSC	0.026	BSC
HE	2.00	2.40	0.079	0.094
L	0.26	0.46	0.010	0.018

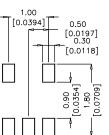
0.46

0.010

Package Dimensions — SOT553 and SOT563



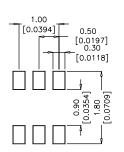




Package		SOT	553	
Pins		Į.	5	
	Millin	neters	Inc	hes
	Min	Max	Min	Max
Α	0.50	0.60	0.020	0.024
В	0.17	0.27	0.007	0.011
С	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	1.10	1.30	0.043	0.051
е	0.50	BSC	0.020	BSC
L	0.10	0.30	0.004	0.012
HE	1.50	1.70	0.059	0.067

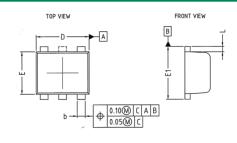
B → ← c

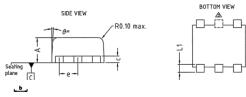




Package	SOT 563				
Pins	6				
	Millimeters Inches				
	Min	Max	Min Max		
Α	0.50	0.60	0.020	0.024	
В	0.17	0.27	0.007	0.011	
С	0.08	0.18	0.003	0.007	
D	1.50	1.70	0.059	0.067	
Е	1.10	1.30	0.043	0.051	
е	0.50 BSC 0.020 BSC) BSC	
L	0.10	0.30	0.004	0.012	
HE	150	1.70	0.059	0.067	

Package Dimensions — SOT963





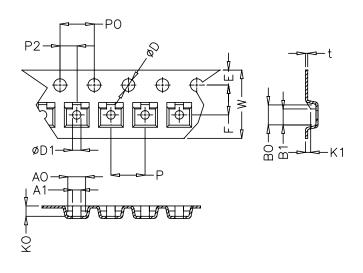
<u>. b.</u> ,					
		m			ch
_ L L L L		Mîn	Max	Min	Max
	b	0.200	0.220	0.0079	0.008
□	е	0.350	(typ)	0.013	8(typ)
	Е	0.688	(ref)	0.027	7(ref)
1	E1	1.062	(ref)	0.042	(ref)
	L1	0.187	0.206	0.0074	0.008
					-

Recommanded Solder Pad Layout

Package	SOT 963					
Pins	6					
	Millimeters			Inches		
	Min	Nom	Max	Min	Nom	Max
Α	0.44	0.48	0.50	0.0173	0.0189	0.0197
В	0.10	0.15	0.20	0.004	0.006	0.008
С	0.05	0.10	0.15	0.002	0.004	0.006
D	0.95	1.00	1.05	0.037	0.039	0.041
Е	0.75	0.80	0.85	0.029	0.031	0.033
E1	0.95	1.00	1.05	0.037	0.039	0.041
е	0.35 BSC				0.014 BS	С
L	0.05	0.10	0.15	0.002	0.004	0.006
L1	0.125	0.15	0.175	0.005	0.006	0.007
ø	3°	5°	7°	3 °	5°	7°

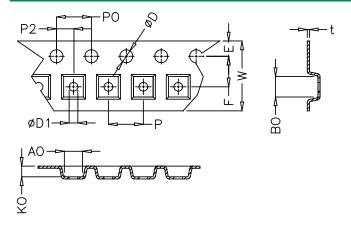


Embossed Carrier Tape & Reel Specification — SC70-3



Dimensions					
	Millimetres		Inches		
	Min	Max	Min	Max	
E	1.65	1.85	0.065	0.073	
F	3.45	3.55	0.135	0.139	
P2	1.95	2.05	0.077	0.081	
D	1.40	1.60	0.055	0.063	
D1	1.00	1.25	0.039	0.049	
P0	3.90	4.10	0.154	0.161	
10P0	40.0 =	± 0.20	1.574 ± 0.008		
W	7.70	8.10	0.303	0.318	
P	3.90	4.10	0.153	0.161	
A0	2.30	2.50	0.090	0.098	
A 1	1.00 Ref		0.039 Ref		
В0	2.30	2.50	0.090	0.098	
B1	1.90 Ref		0.074		
K0	1.10	1.30	0.043	0.051	
K1	0.60 Ref		0.023 Ref		
t	0.27 max		0.010		

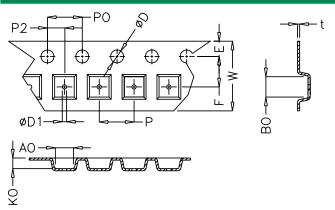
Embossed Carrier Tape & Reel Specification — SC70-5 and SC70-6



Dimensions					
	Millimetres		Inches		
	Min	Max	Min	Max	
E	1.65	1.85	0.065	0.073	
F	3.45	3.55	0.135	0.139	
P2	1.95	2.05	0.077	0.081	
D	1.40	1.60	0.055	0.063	
D1	1.00	1.25	0.039	0.049	
P0	3.90	4.10	0.154	0.161	
10P0	40.0 ± 0.20		1.574 ± 0.008		
W	7.70	8.10	0.303	0.318	
Р	3.90	4.10	0.153	0.161	
A0	2.14	2.34	0.084	0.092	
В0	2.24	2.44	0.088	0.096	
K0	1.12	1.32	0.044	0.052	
t	0.27 max 0.010 max			max	

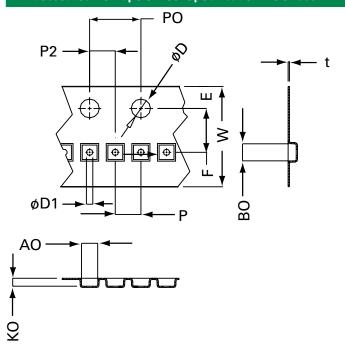


Embossed Carrier Tape & Reel Specification — SOT553 and SOT563



Dimensions						
	Millimetres		Inches			
	Min	Max	Min	Max		
E	1.65	1.85	0.065	0.073		
F	3.45	3.55	0.135	0.139		
P2	1.95	2.05	0.077	0.081		
D	1.40	1.60	0.055	0.063		
D1	0.45	0.55	0.017	0.021		
P0	3.90	4.1	0.154	0.161		
10P0	40.0 ± 0.20		1.574 ± 0.008			
W	7.70	8.10	0.303	0.318		
Р	3.90	4.10	0.153	0.161		
A0	1.73	1.83	0.068	0.072		
В0	1.73	1.83	0.068	0.072		
K0	0.64	0.74	0.025	0.029		
t	0.22 max		.009	max		

Embossed Carrier Tape & Reel Specification - SOT963



Dimensions					
Complete	Millimetres		Inches		
Symbol	Min	Max	Min	Max	
E	1.65	1.85	0.065	0.073	
F	3.45	3.55	0.136	0.140	
D1	0.45	0.55	0.018	0.022	
D	1.50 min		0.059 min		
P0	3.90	4.10	0.154	0.161	
10P0	40.0 ± 0.20		1.575 ± 0.008		
Р	1.95	2.05	0.077	0.081	
P2	1.95	2.05	0.077	0.081	
W	7.90	8.20	0.311	0.323	
A0	1.11	1.21	0.044	0.048	
В0	1.11	1.21	0.044	0.048	
K0	0.58	0.68	0.023	0.027	
t	0.22 max 0.009 max) max	

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