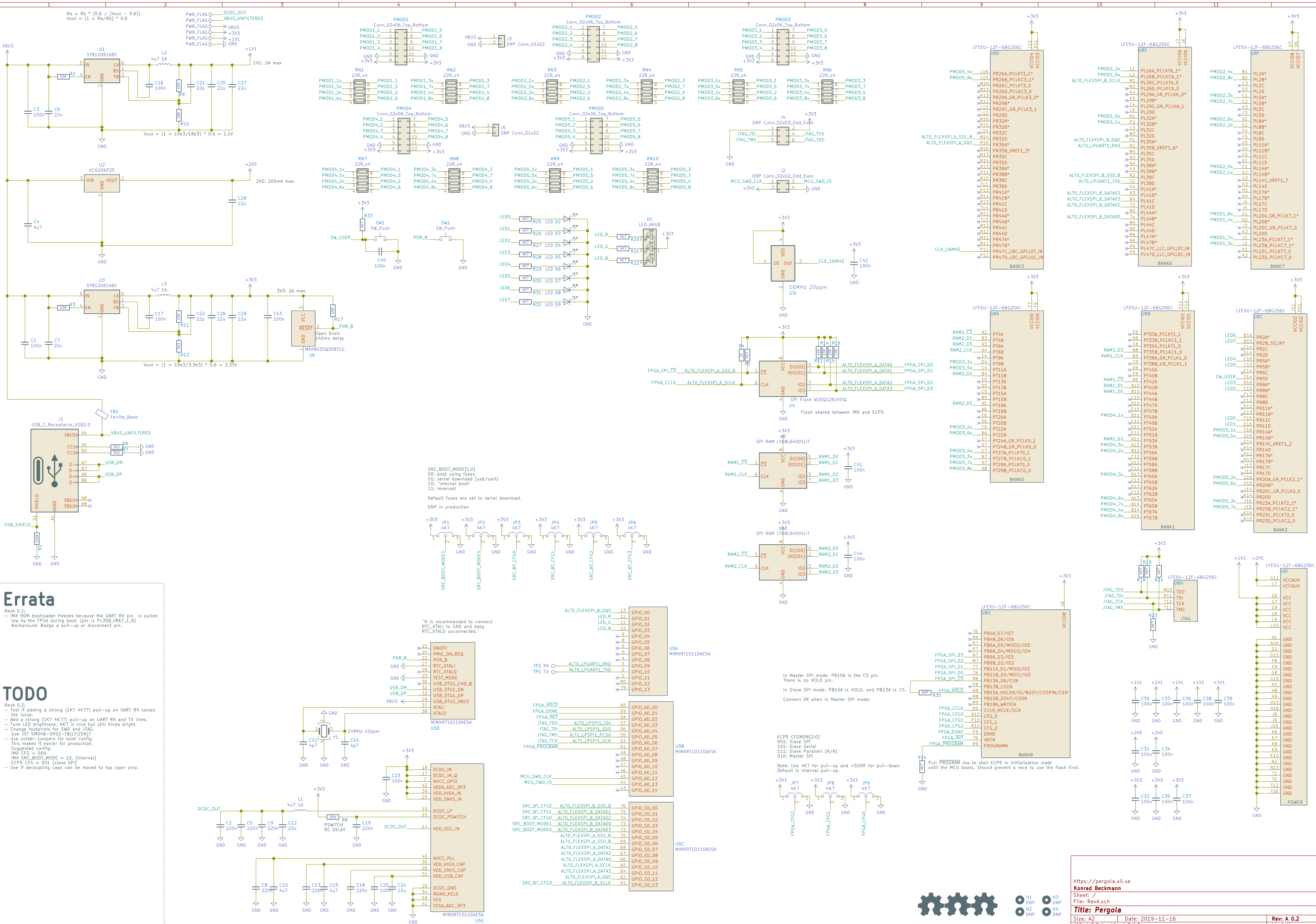


Errata

RevA 0.1:
- IMX ROM bootloader freezes because the UART RX pin is pulled low by the FPGA during boot. (pin is PL35B_VREF1_6)
Workaround: Bodge a pull-up or disconnect pin.

TODO

RevA 0.2:
- Test if adding a strong (1K? 4K7?) pull-up on UART RX solves the issue.
- Add a strong (1K? 4K7?) pull-up on UART RX and TX lines.
- Tune LED brightness. 4K7 is nice but still kinda bright.
- Change footprints for SWB and JTAG.
- Use JST SMD48-SR55-TBL(7)SW7.
- Use solder-jumpers for boot config.
- This makes it easier for production.
Suggested config:
IMX_CFG = 000.
IMX_SRC_BOOT_MODE = 10. (internal)
ECPS_CFG = 001 (slave SPI)
- See if decoupling caps can be moved to top layer only.



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Konrad Beckmann

Sheet: /

File: RevA.sch

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Rev: A 0.2

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