
Raisin64 Documentation

Release 0.1

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CONTENTS:

1	Indices and tables	3
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The Raisin64 (RISC Architecture with In-order Superscalar INterlocked-pipeline) is a pure 64-bit CPU design created as part of an academic project. Architecturally similar to the [MIPS R10000](#) and [POWER3](#) architectures,

INDICES AND TABLES

- `genindex`
- `modindex`
- `search`