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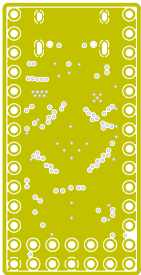
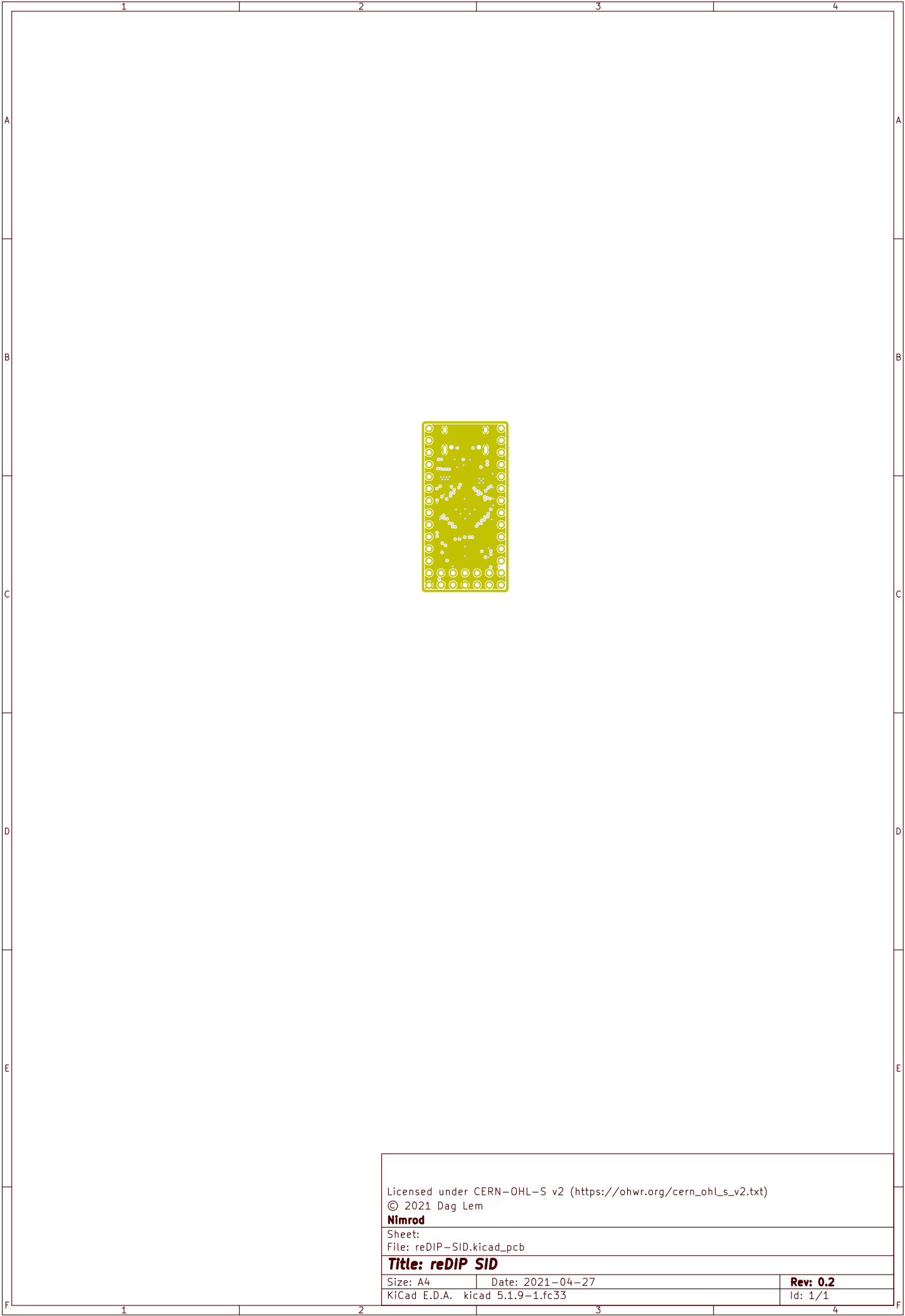
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Sheet:  
File: reDIP-SID.kicad\_pcb

**Title: reDIP SID**

Size: A4      Date: 2021-04-27  
KiCad E.D.A.    kicad 5.1.9-1.fc33

**Rev: 0.2**  
Id: 1/1



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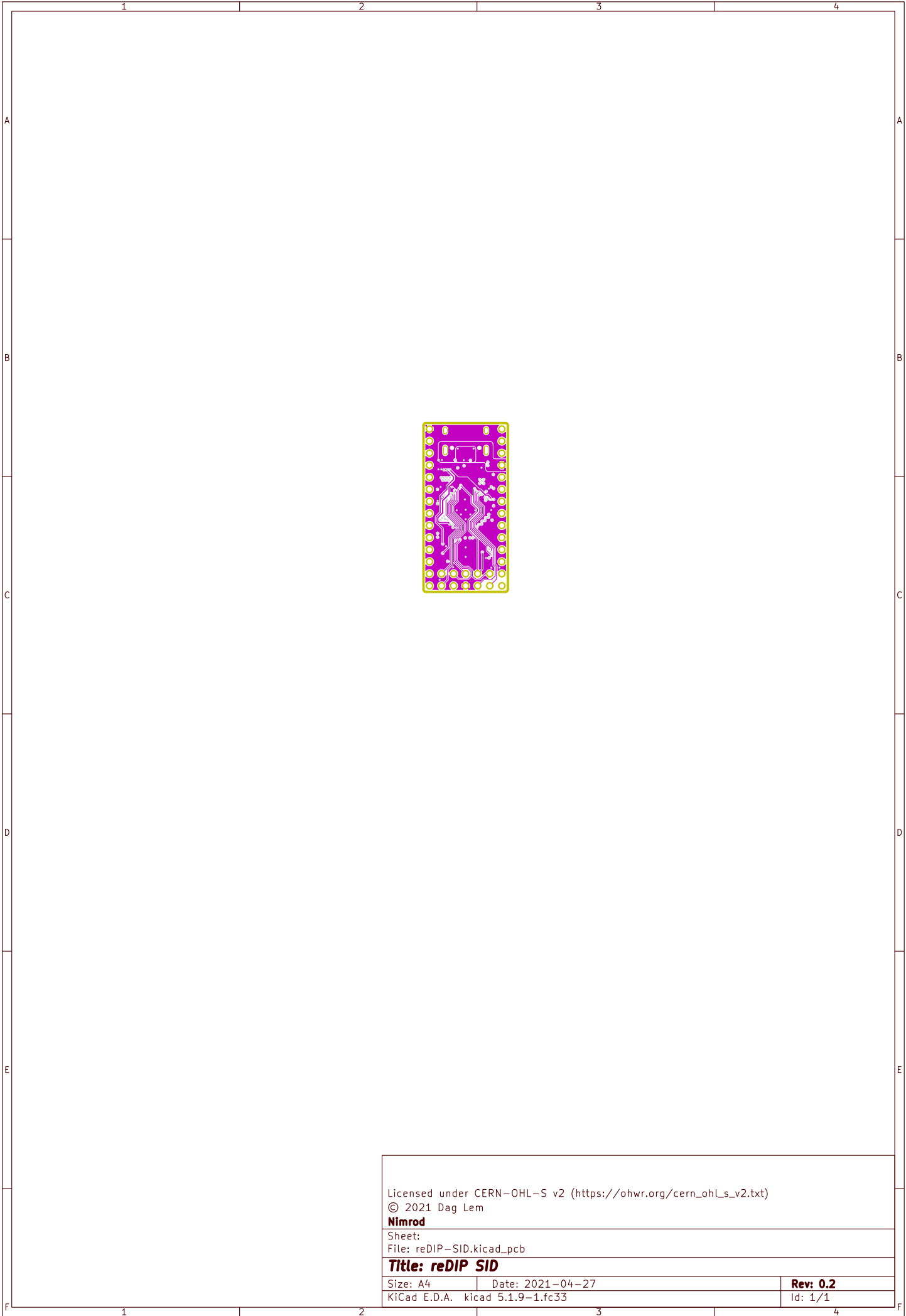
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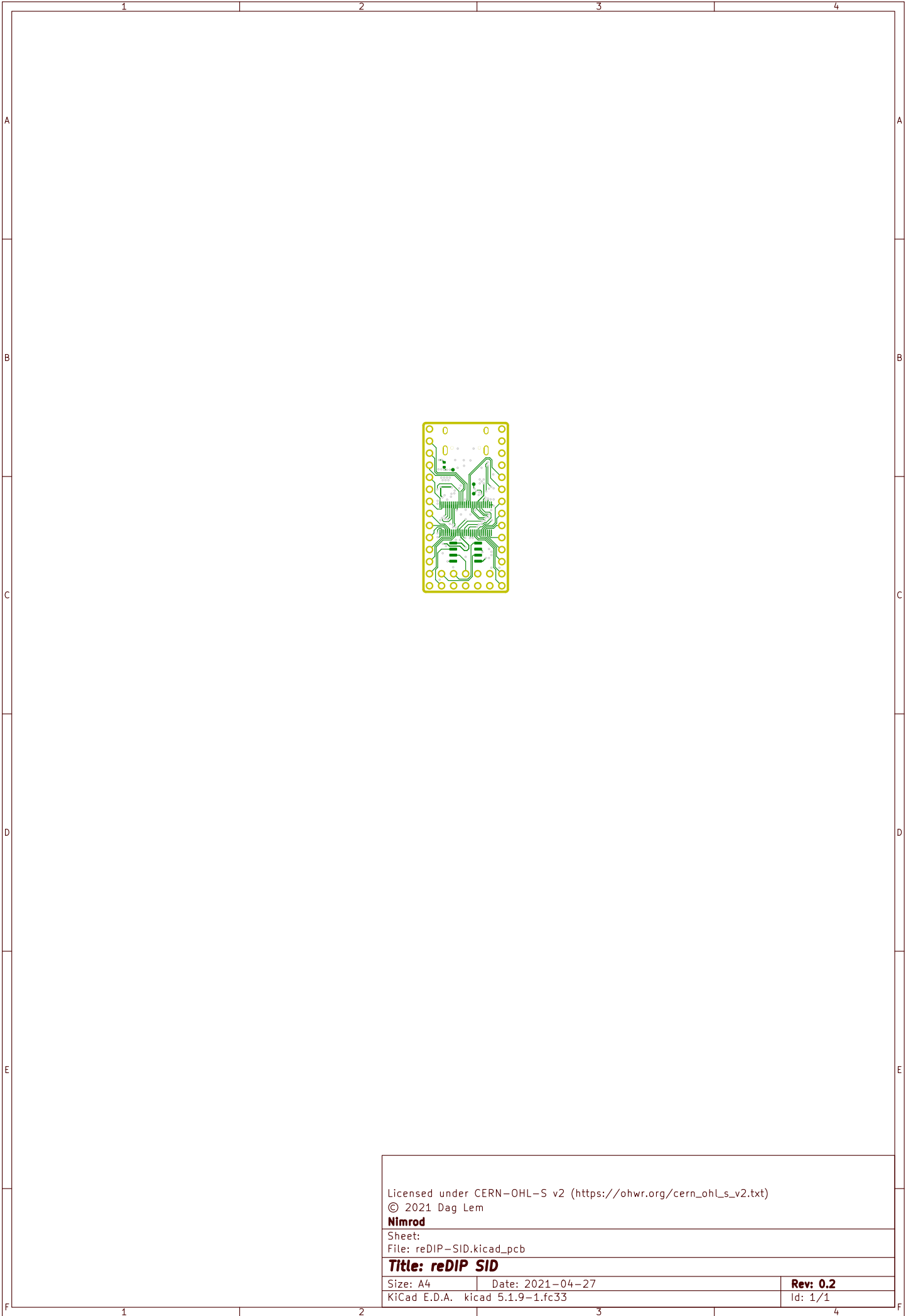
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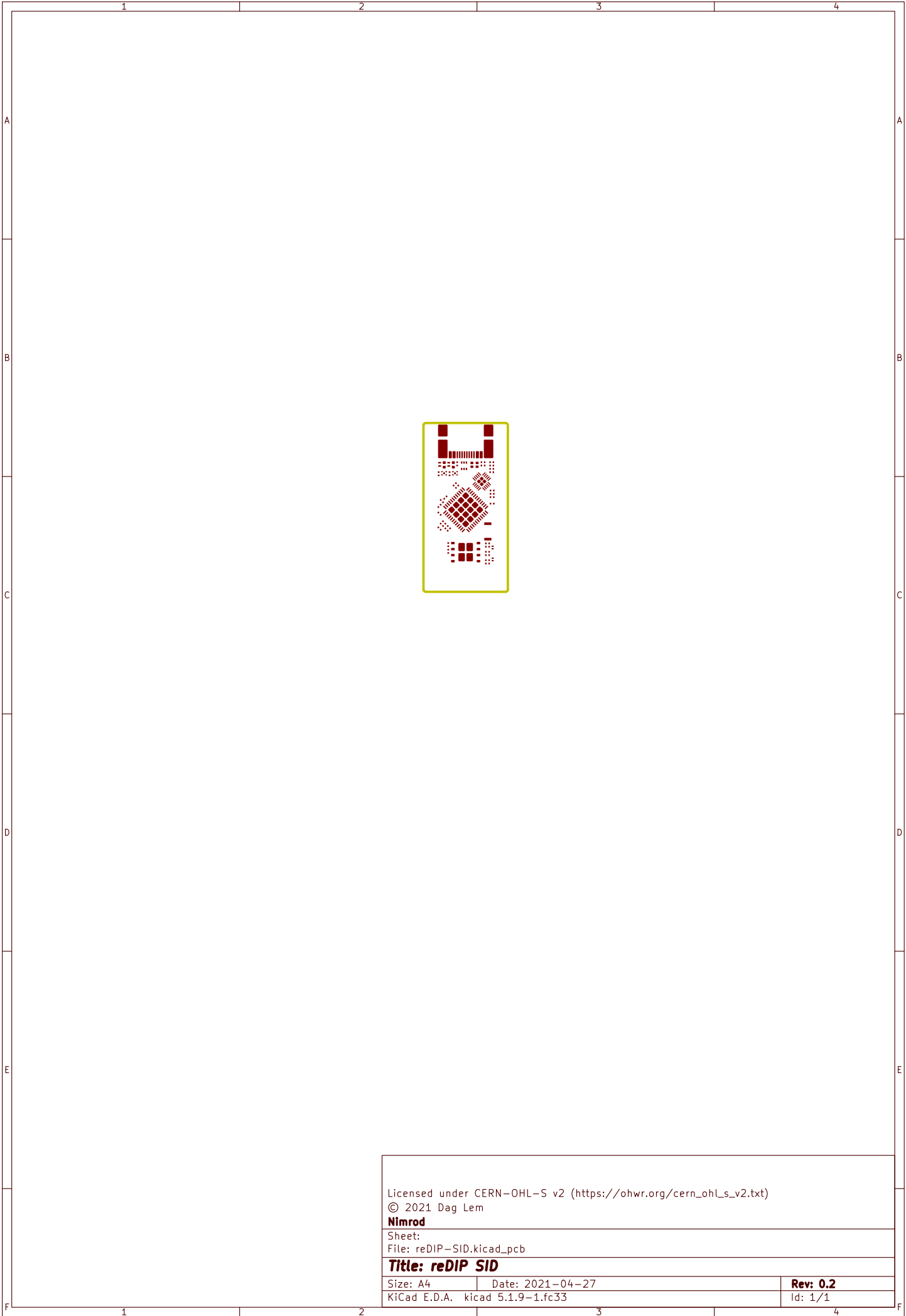
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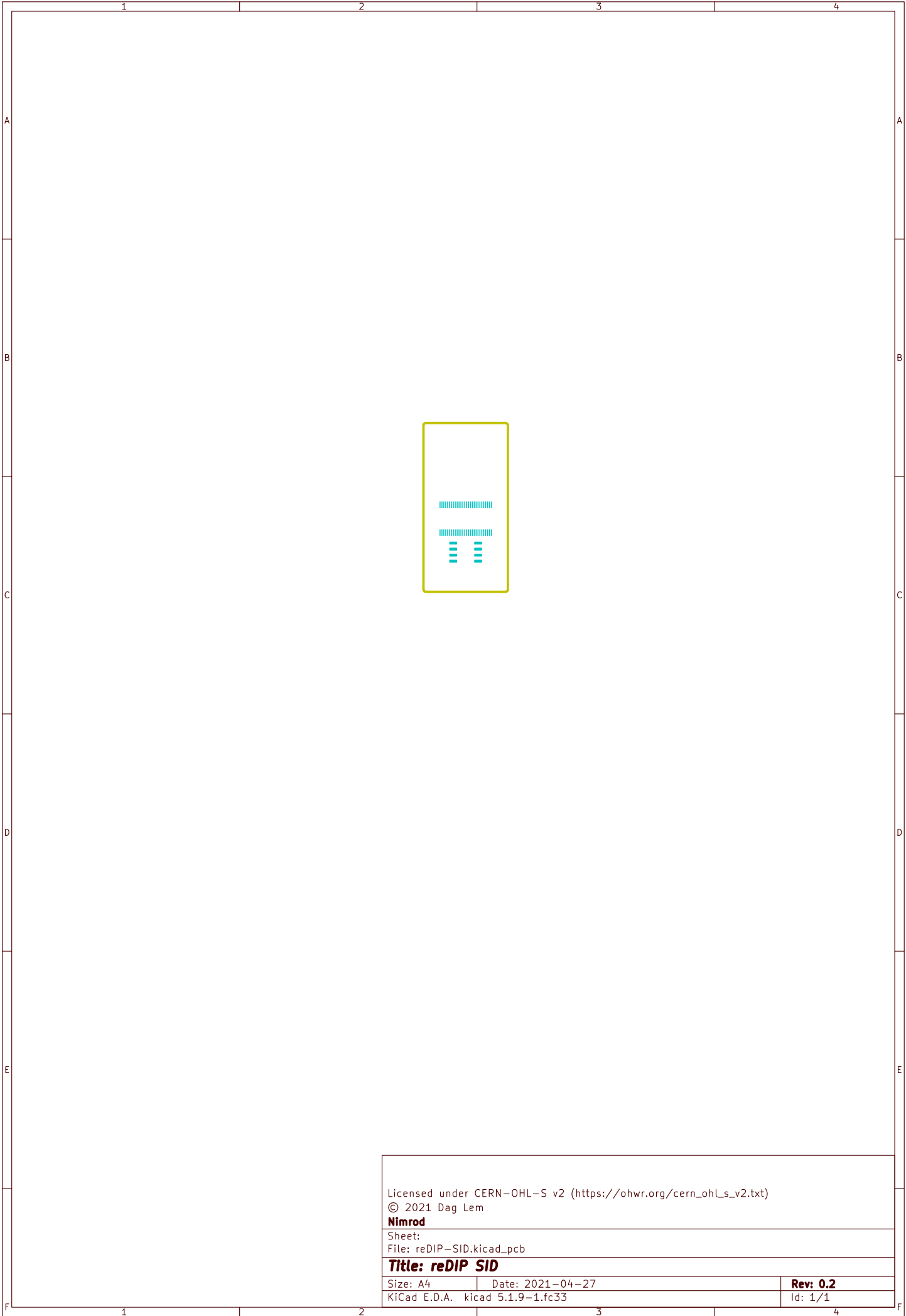
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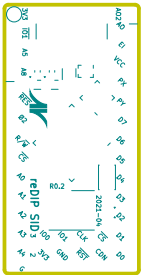
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	1	2	3	4
A				
B				
C	<div data-bbox="746 741 887 1014">A detailed PCB layout diagram for a device labeled 'reDIP SID'. The diagram shows a rectangular board with various components including resistors (R0.2, R0.1), capacitors (C0.1, C0.2), and integrated circuits. It includes a top layer (A0.1) and a bottom layer (B0.1). The board is surrounded by a yellow border.</div>			
D				
E				
F	1	2	3	4

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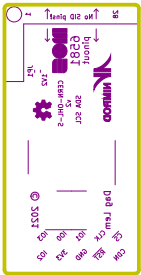
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B				
C				
D				
E				
F				



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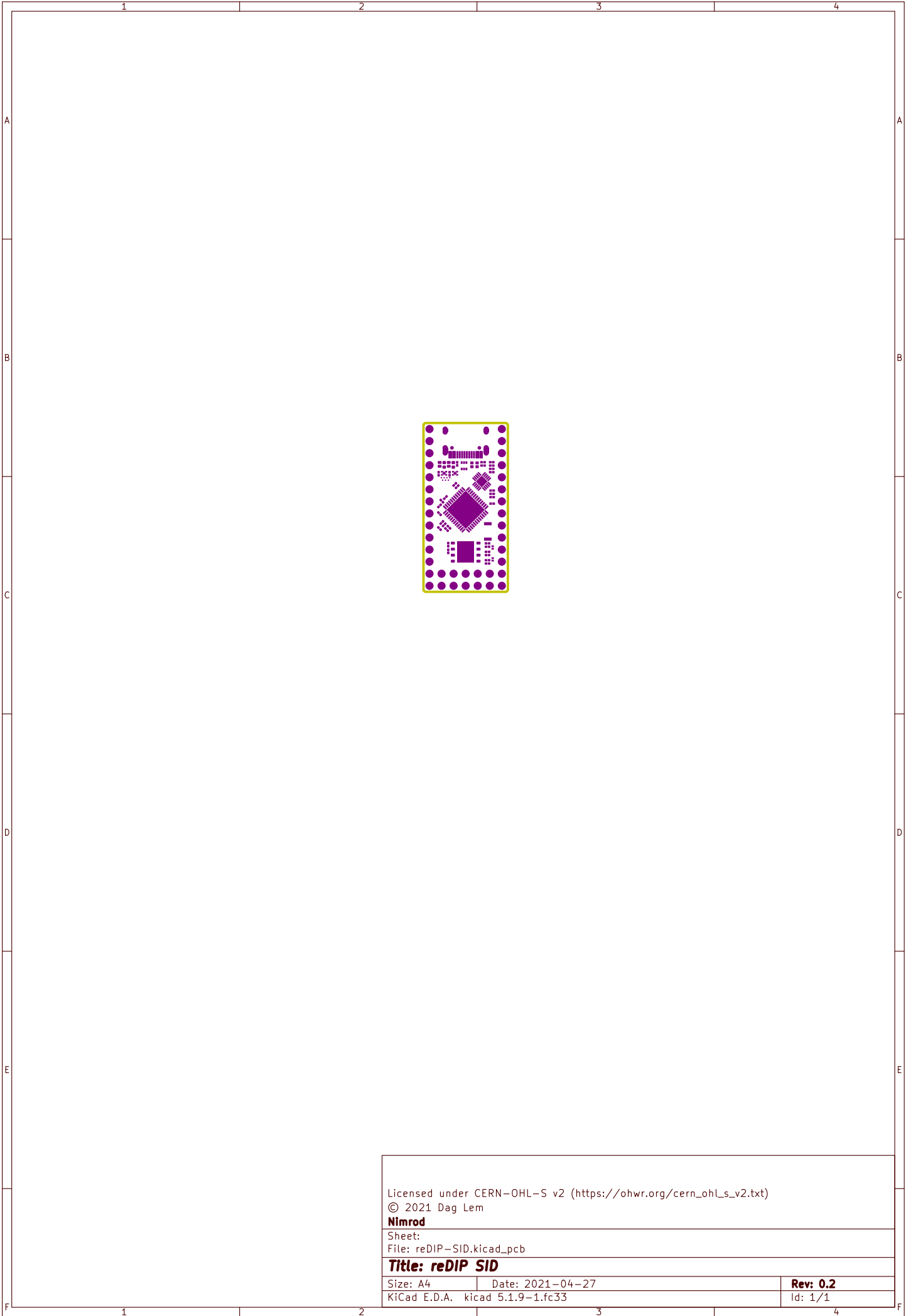
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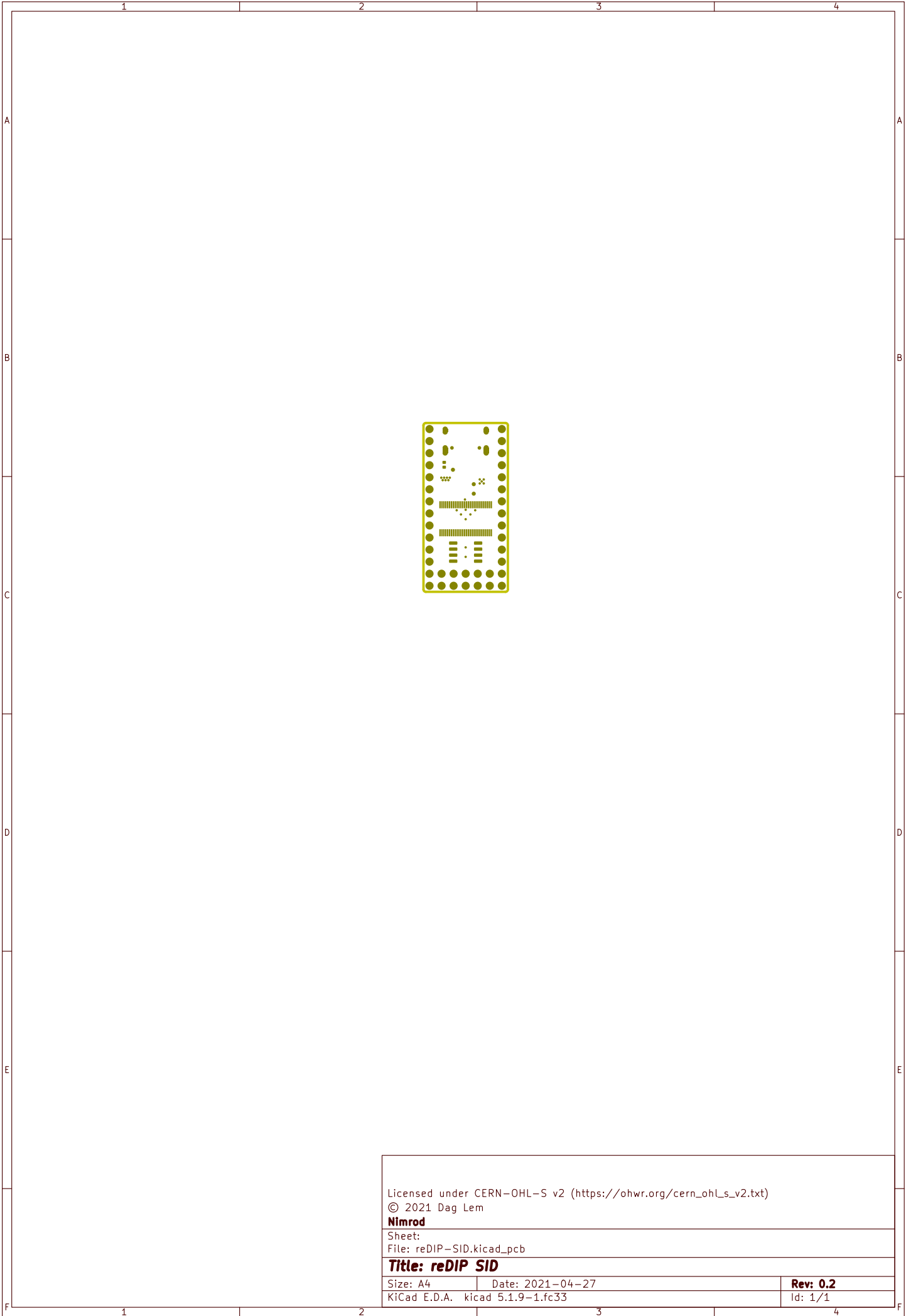
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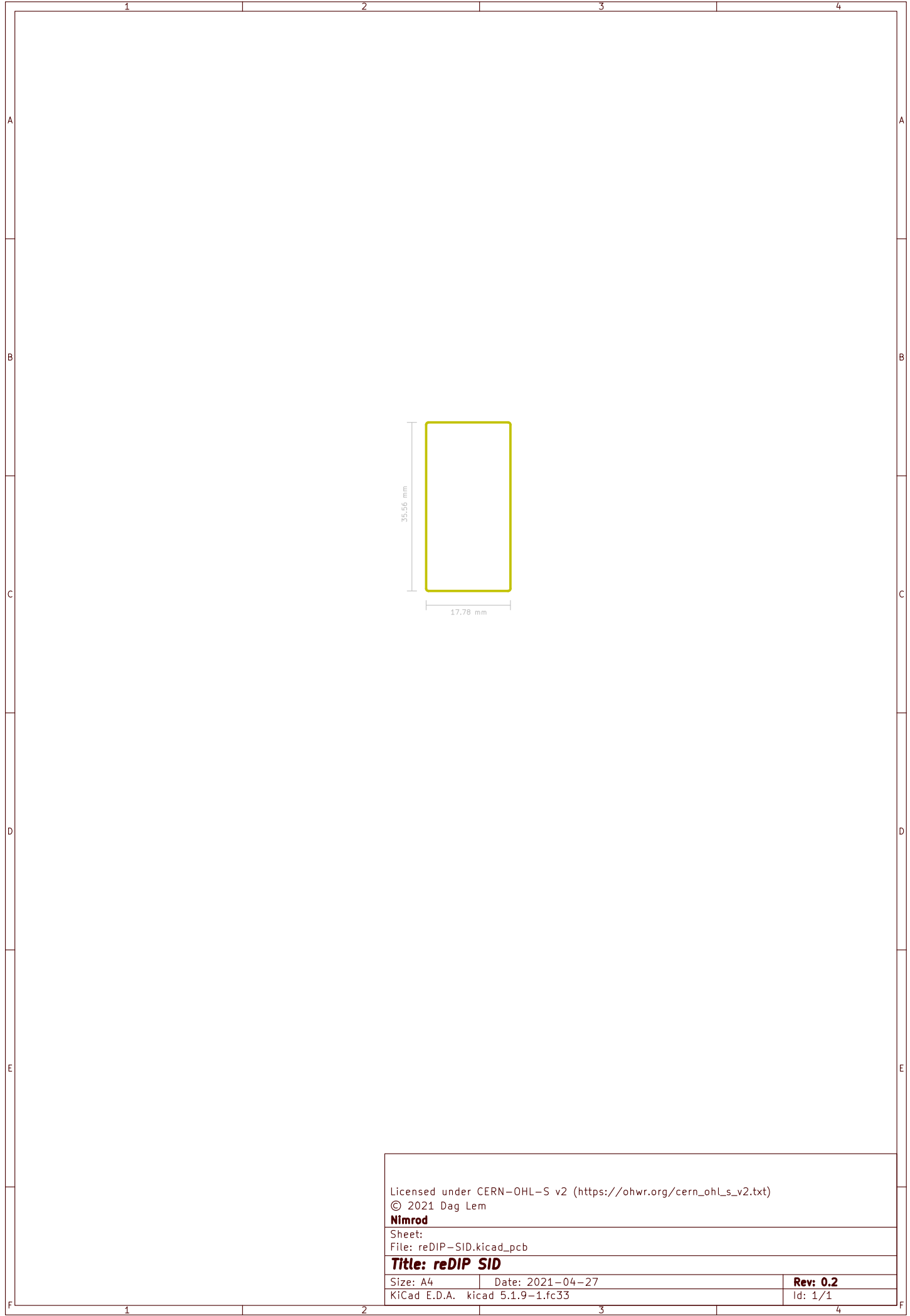
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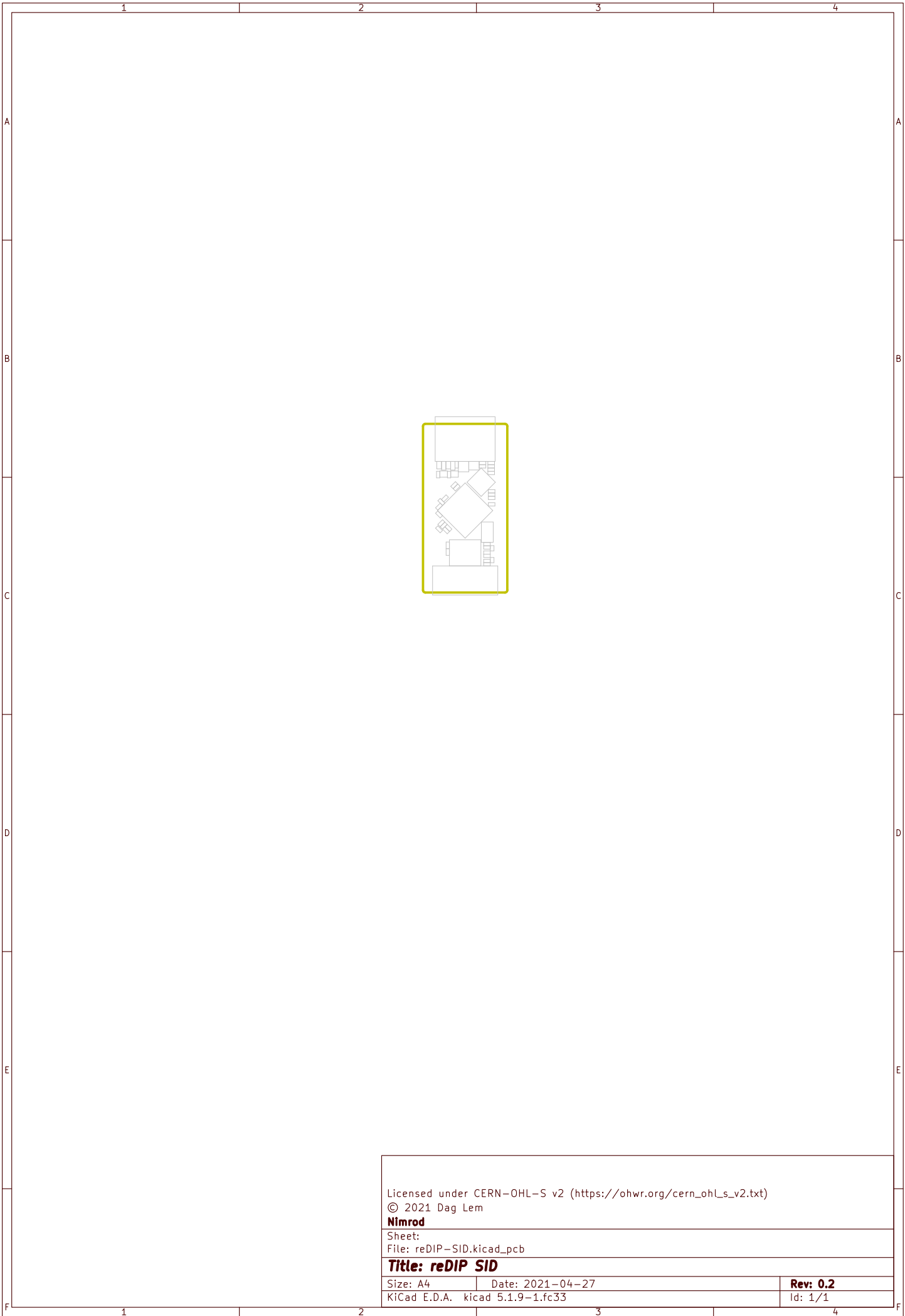
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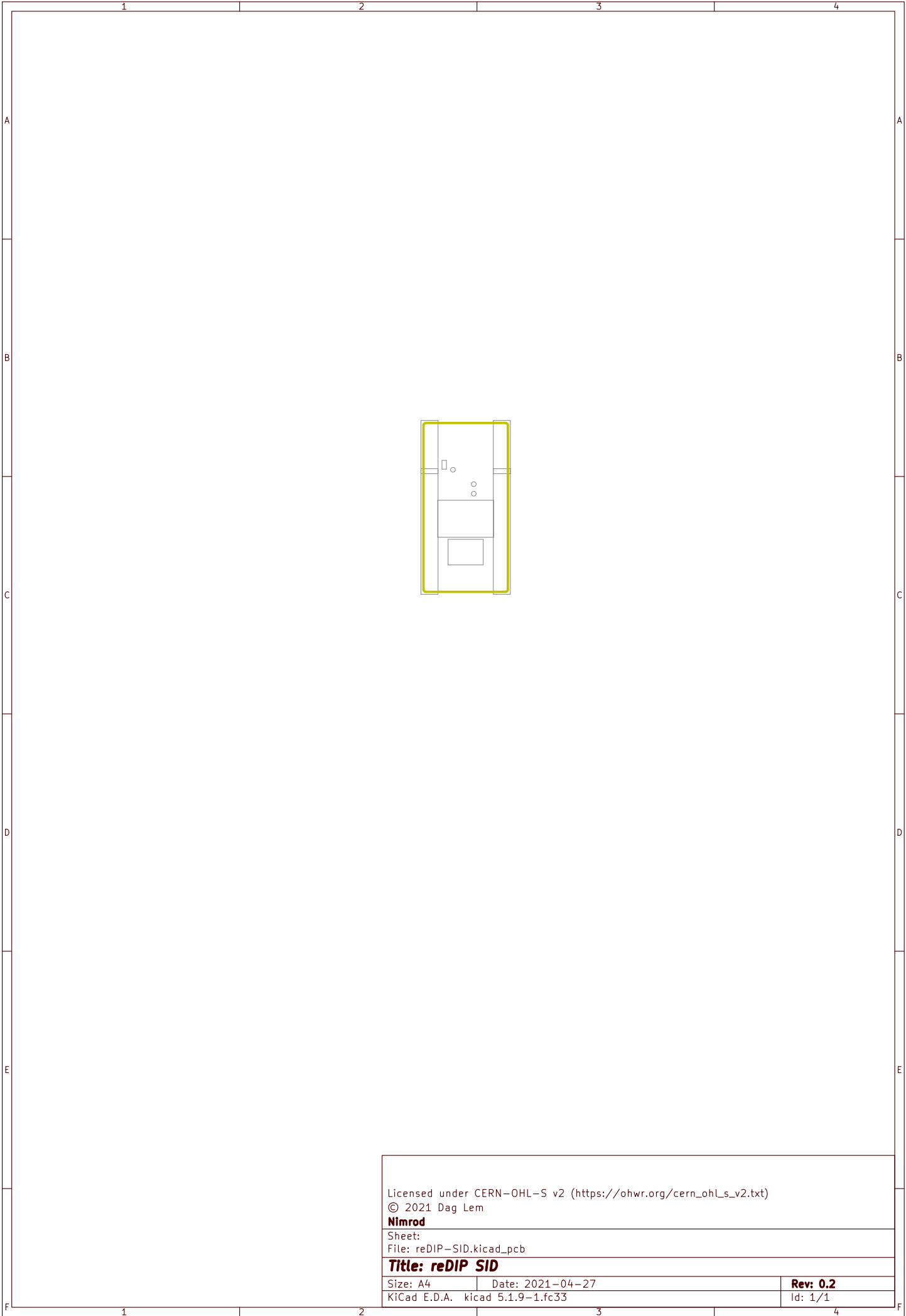
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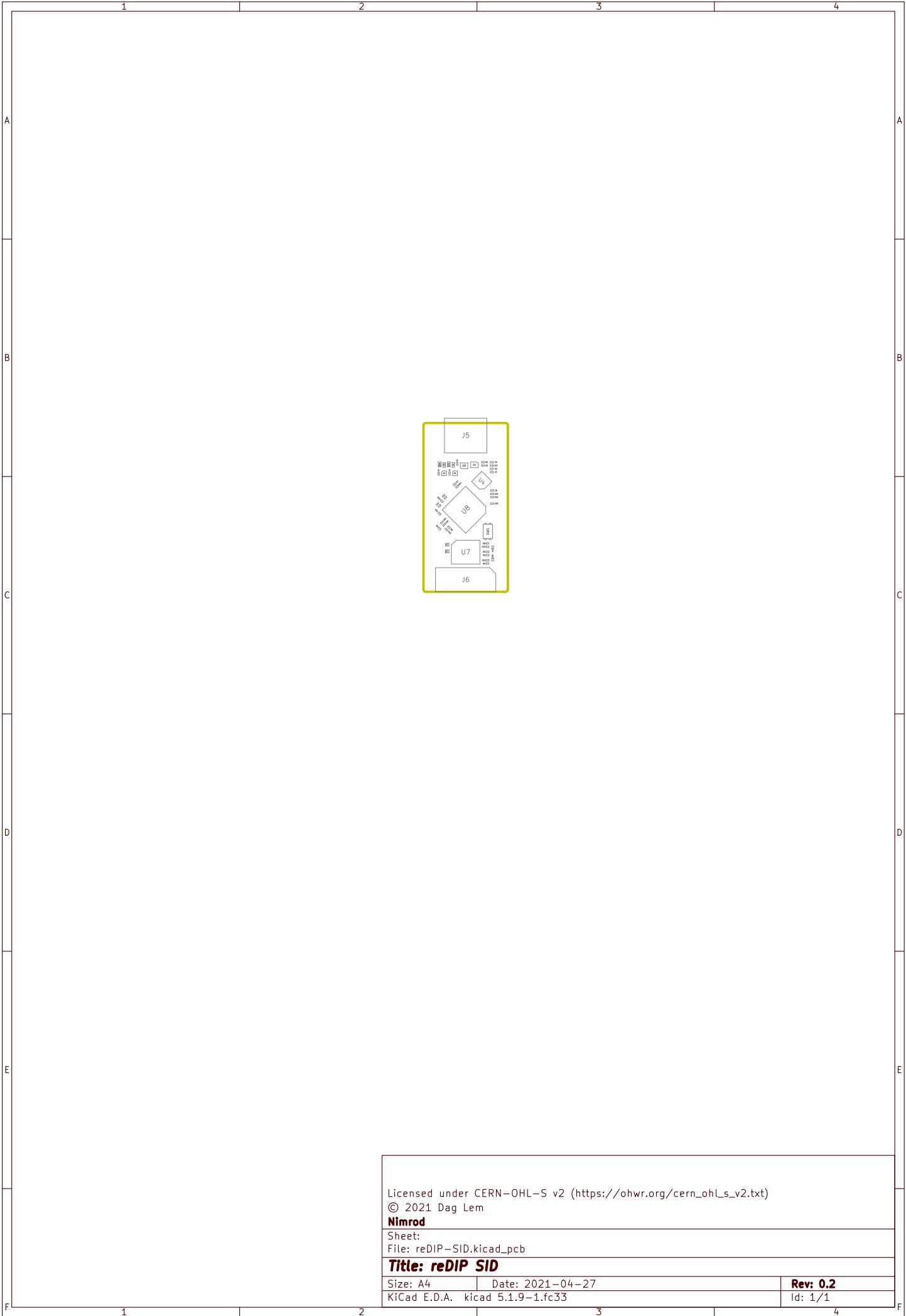
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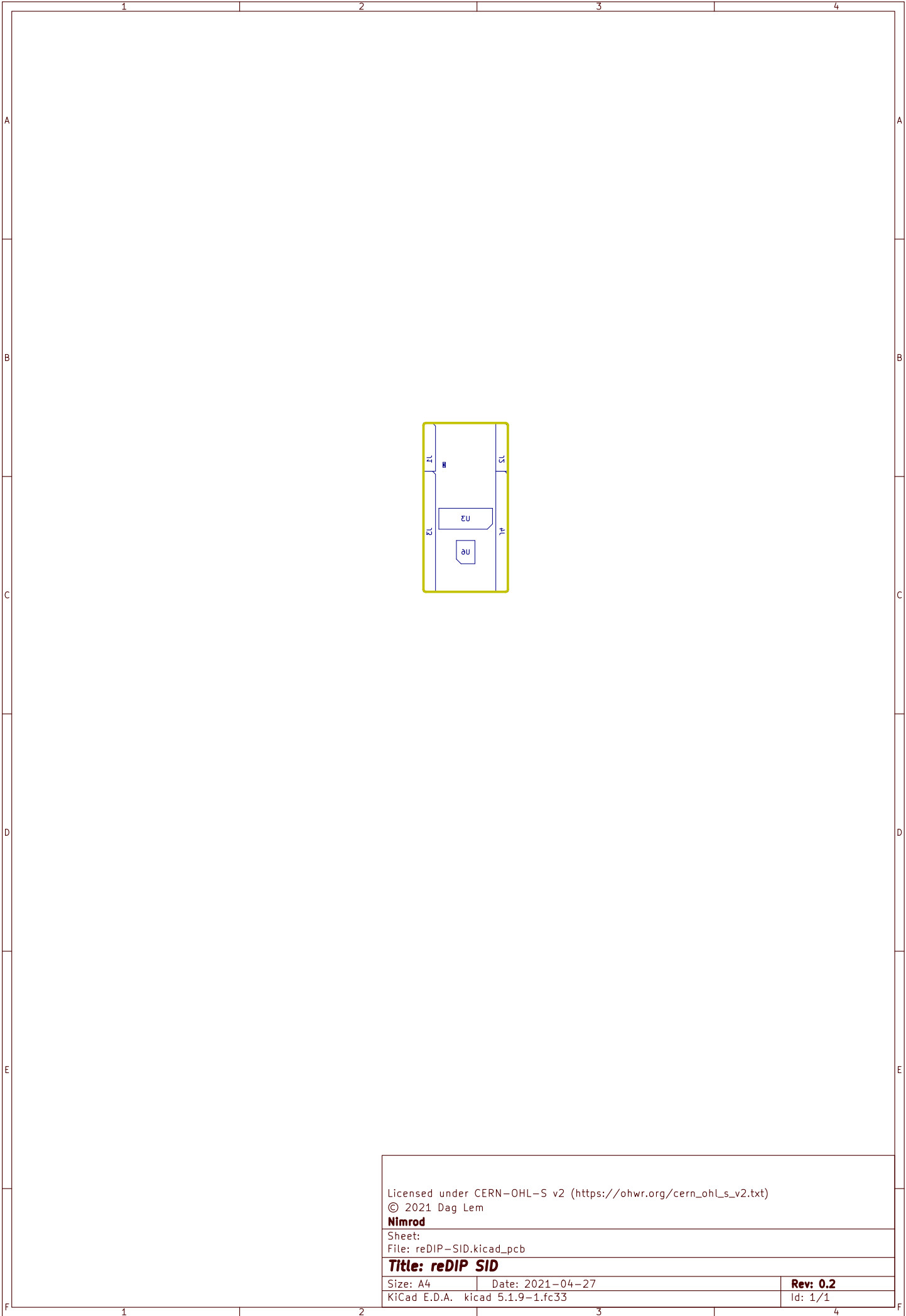
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