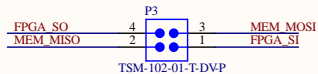
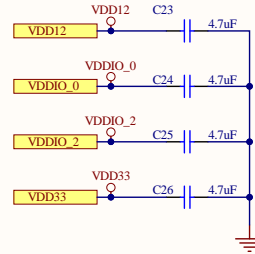
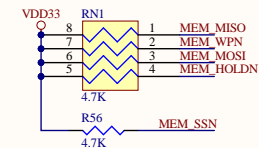
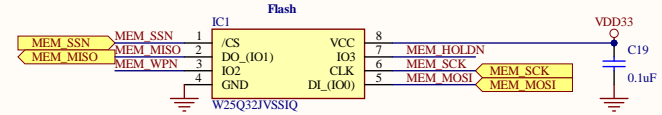
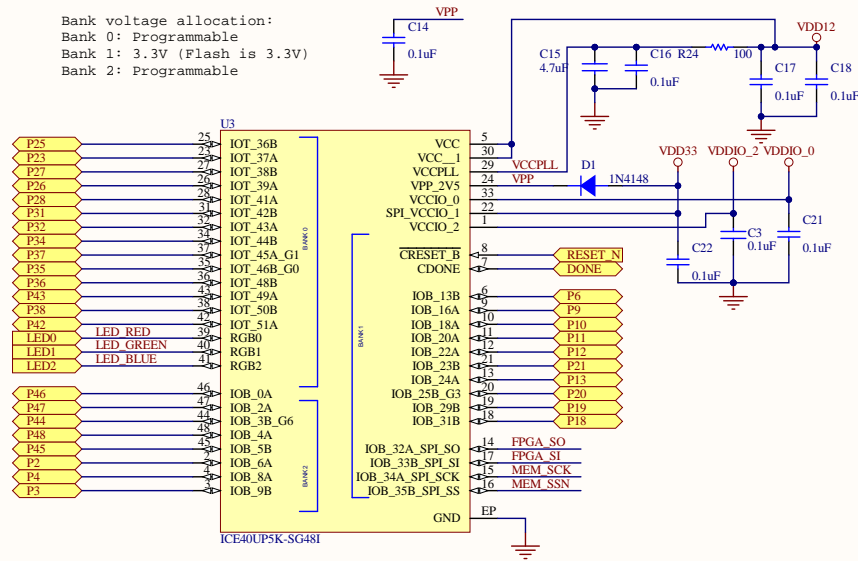


Bank voltage allocation:  
 Bank 0: Programmable  
 Bank 1: 3.3V (Flash is 3.3V)  
 Bank 2: Programmable



Note:  
 Short 1-2 and 3-4 to program Flash  
 Short 1-3 and 2-4 to program FPGA RAM

|       |                                   |           |
|-------|-----------------------------------|-----------|
| Title |                                   |           |
| FPGA  |                                   |           |
| Size  | Number                            | Revision  |
| B     |                                   |           |
| Date: | 9/03/2020                         | Sheet of  |
| File: | G:\My Drive\ICE40UP5K base design | Drawn By: |