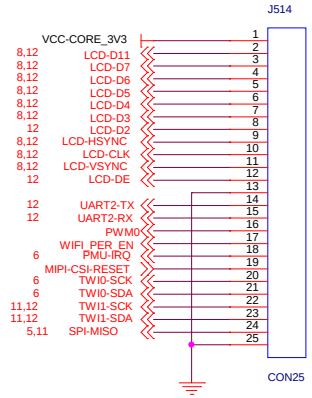
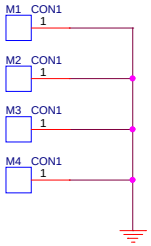
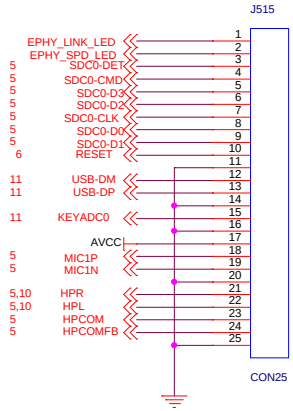
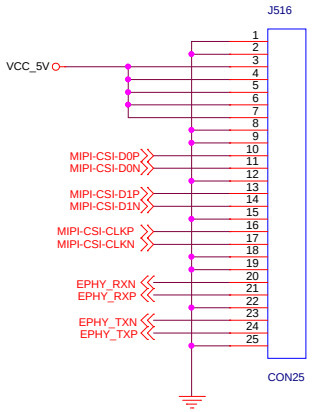
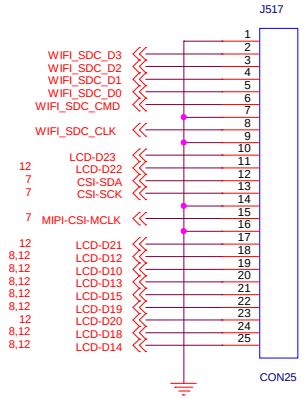
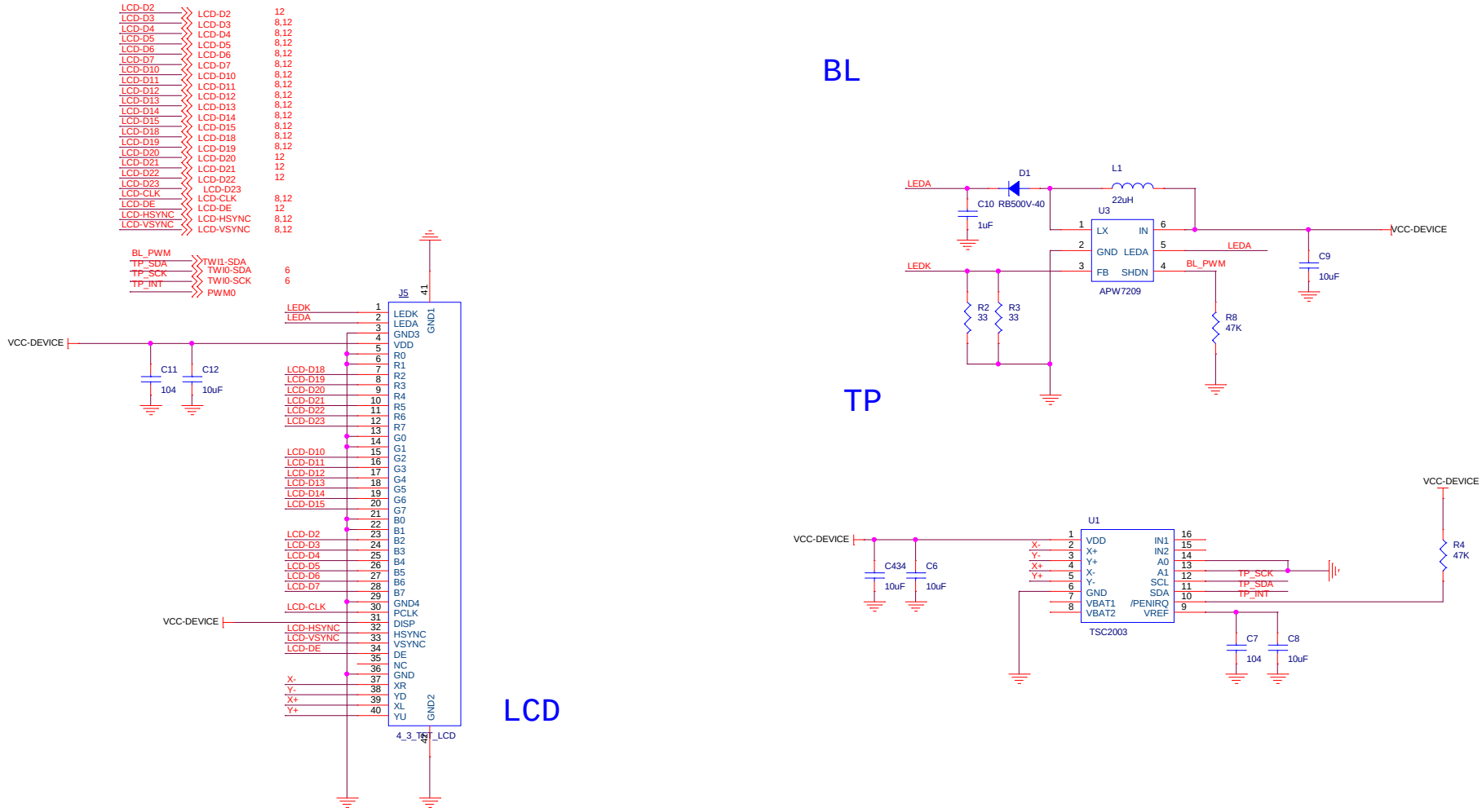

J514
VCC-CORE_3V3
8,12 LCD-D11
8,12 LCD-D7
8,12 LCD-D6
8,12 LCD-D5
8,12 LCD-D4
8,12 LCD-D3
12 LCD-D2
8,12 LCD-HSYNC
8,12 LCD-CLK
8,12 LCD-VSYNC
12 LCD-DE
12 UART2-TX
12 UART2-RX
PWM0
WIFI_PER_EN
6 PMU-IRQ
MIPI-CSI-RESET
6 TWI0-SCK
6 TWI0-SDA
11,12 TWI1-SCK
11,12 TWI1-SDA
5,11 SPI-MISO
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
CON25

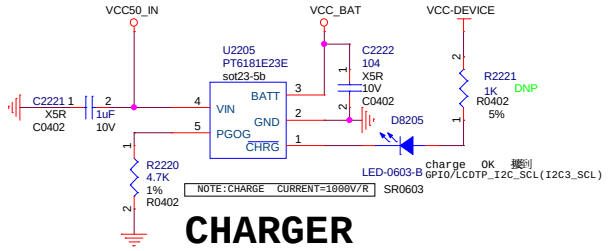

M1 CON1
1
M2 CON1
1
M3 CON1
1
M4 CON1
1


J515
EPHY_LINK_LED
EPHY_SPD_LED
5 SDC0-DET
5 SDC0-CMD
5 SDC0-D3
5 SDC0-D2
5 SDC0-CLK
5 SDC0-D0
5 SDC0-D1
6 RESET
11 USB-DM
11 USB-DP
11 KEYADC0
AVCC
5 MIC1P
5 MIC1N
5,10 HPR
5,10 HPL
5 HPCOM
5 HPCOMFB
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
CON25

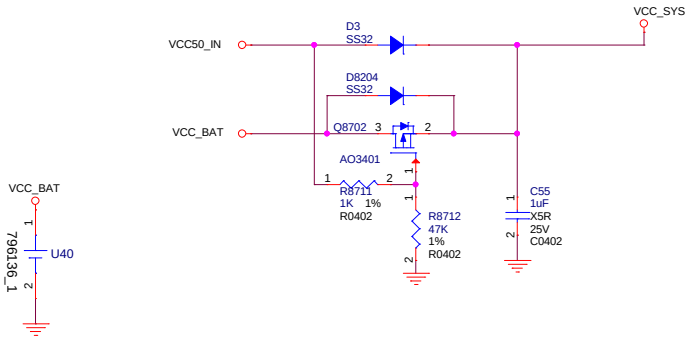

J516
VCC_5V
MIPI-CSI-D0P
MIPI-CSI-D0N
MIPI-CSI-D1P
MIPI-CSI-D1N
MIPI-CSI-CLKP
MIPI-CSI-CLKN
EPHY_RXN
EPHY_RXP
EPHY_TXN
EPHY_TXP
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
CON25


J517
WIFI_SDC_D3
WIFI_SDC_D2
WIFI_SDC_D1
WIFI_SDC_D0
WIFI_SDC_CMD
WIFI_SDC_CLK
LCD-D23
12 LCD-D22
7 CSI-SDA
7 CSI-SCK
7 MIPI-CSI-MCLK
12 LCD-D21
8,12 LCD-D12
8,12 LCD-D10
8,12 LCD-D13
8,12 LCD-D15
8,12 LCD-D19
12 LCD-D20
8,12 LCD-D18
8,12 LCD-D14
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25
CON25


LCD-D2 LCD-D2 12
LCD-D3 LCD-D3 8,12
LCD-D4 LCD-D4 8,12
LCD-D5 LCD-D5 8,12
LCD-D6 LCD-D6 8,12
LCD-D7 LCD-D7 8,12
LCD-D10 LCD-D10 8,12
LCD-D11 LCD-D11 8,12
LCD-D12 LCD-D12 8,12
LCD-D13 LCD-D13 8,12
LCD-D14 LCD-D14 8,12
LCD-D15 LCD-D15 8,12
LCD-D18 LCD-D18 8,12
LCD-D19 LCD-D19 8,12
LCD-D20 LCD-D20 12
LCD-D21 LCD-D21 12
LCD-D22 LCD-D22 12
LCD-D23 LCD-D23
LCD-CLK LCD-CLK 8,12
LCD-DE LCD-DE 12
LCD-HSYNC LCD-HSYNC 8,12
LCD-VSYNC LCD-VSYNC 8,12
BL_PWM
TP_SDA TWI1-SDA
TP_SCK TWI0-SDA 6
TP_INT TWI0-SCK 6
PWM0
J5
4_3_TFT_LCD
LEDK
LEDA
GND1
GND2
GND3
VDD
R0 R1 R2 R3 R4 R5 R6 R7
G0 G1 G2 G3 G4 G5 G6 G7
B0 B1 B2 B3 B4 B5 B6 B7
GND4
PCLK
DISP
HSYNC
VSYNC
DE
NC
GND
XR
YD
XL
YU
X-
Y-
X+
Y+
VCC-DEVICE
C11 104
C12 10uF
BL
TP
LCD
D1 RB500V-40
L1 22uH
C10 1uF
U3
LX IN
GND LEDA
FB SHDN
APW7209
R2 33
R3 33
R8 47K
C9 10uF
U1
VDD
X- Y- X+ Y+
GND
VBAT1 VBAT2
IN1 IN2 A0 A1
SCL SDA
/PENIRQ VREF
TSC2003
C434 10uF
C6 10uF
C7 104
C8 10uF
R4 47K


VCC50_IN
VCC_BAT
VCC-DEVICE
U2205
PT6181E23E
sot23-5b
VIN
BATT
GND
PGOG
CHRG
C2221
X5R
C0402
1uF
10V
C2222
104
X5R
10V
C0402
R2221
1K
R0402
5%
DNP
D8205
LED-0603-B
R2220
4.7K
1%
R0402
NOTE:CHARGE CURRENT=1000V/R
SR0603
charge OK 指示
GPIO/LCDTP_I2C_SCL(I2C3_SCL)

CHARGER

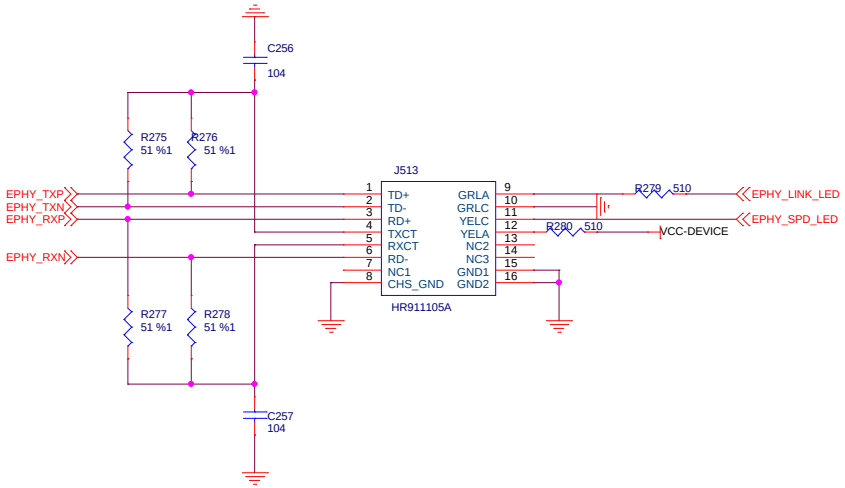

VCC_SYS
VCC50_IN
D3
SS32
D8204
SS32
VCC_BAT
Q8702
AO3401
R8711
1K 1%
R0402
R8712
47K
1%
R0402
C55
1uF
X5R
25V
C0402
VCC_BAT
U40
796136_1

REVISION HISTORY

Schematics Index:

P01: REVISION HISTORY
P02: BLOCK
P03: POWER TREE
P04: GPIO ASSIGNMENT
P05: CPU
P06: POWER
P07: CAMERA-MIPI
P08: RGB LCD 2.7
P09: NOR NANDFlash/TF Card
P10: SPEAKER/MICROPHONE/HEADPHONE
P11: USB/KEY/GSENSOR
P12: OPTION:RGB LCD 4.3 /CTP/GPS

Revision	Description	Date	Drawn	Checked
Ver 0.1	Initial	2015-02-04	HW	
Ver 1.1	changelist.txt	2015-08-17	WJW	



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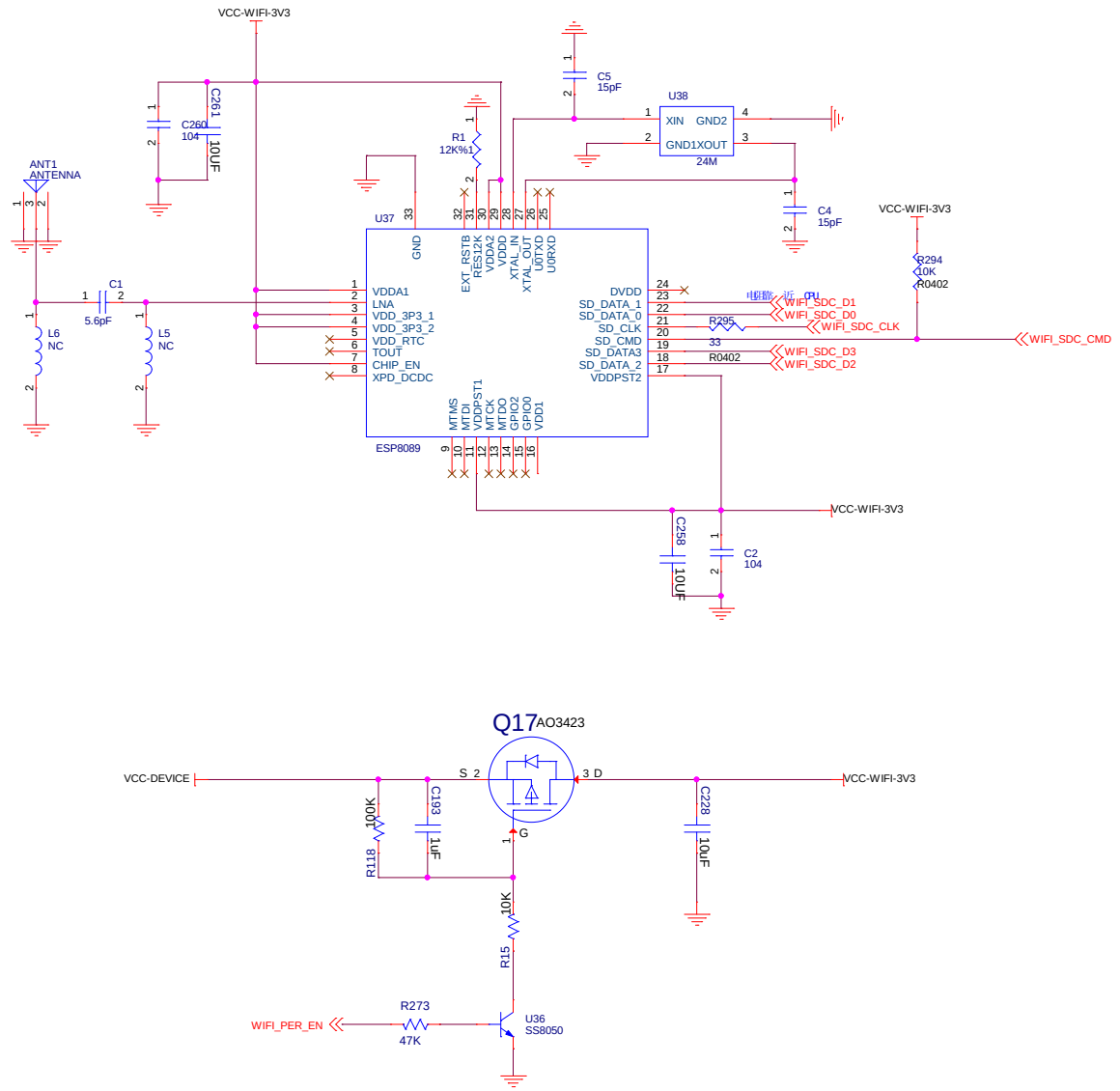
Size A3	Document Number <Doc>	Rev <RevCode>
Date: Sunday, May 20, 2018	Sheet 1 of 1	



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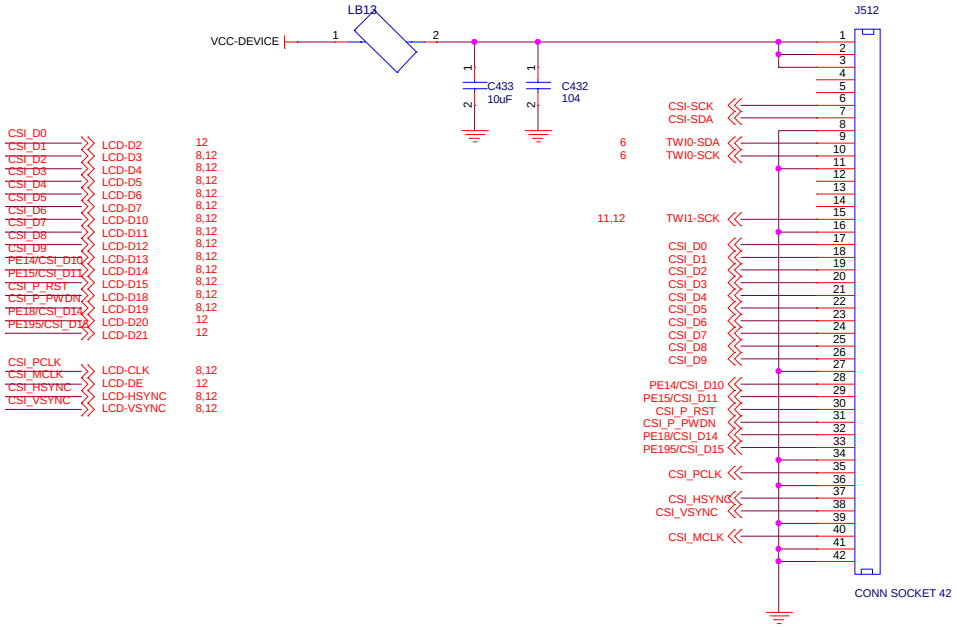
Size A3	Document Number <Doc>	Rev <RevCode>
Date: Sunday, May 20, 2018	Sheet 1 of 1	



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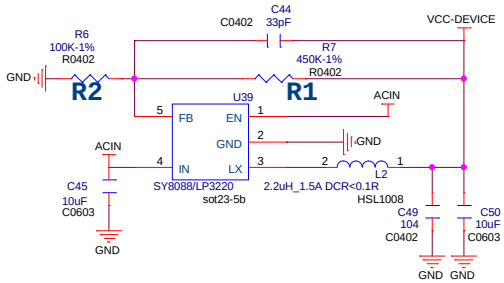
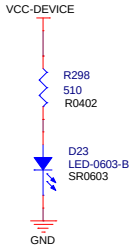
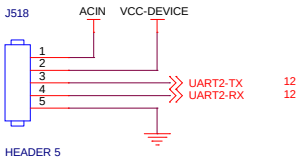
Date: Monday, May 21, 2018	Sheet 1 of 1		







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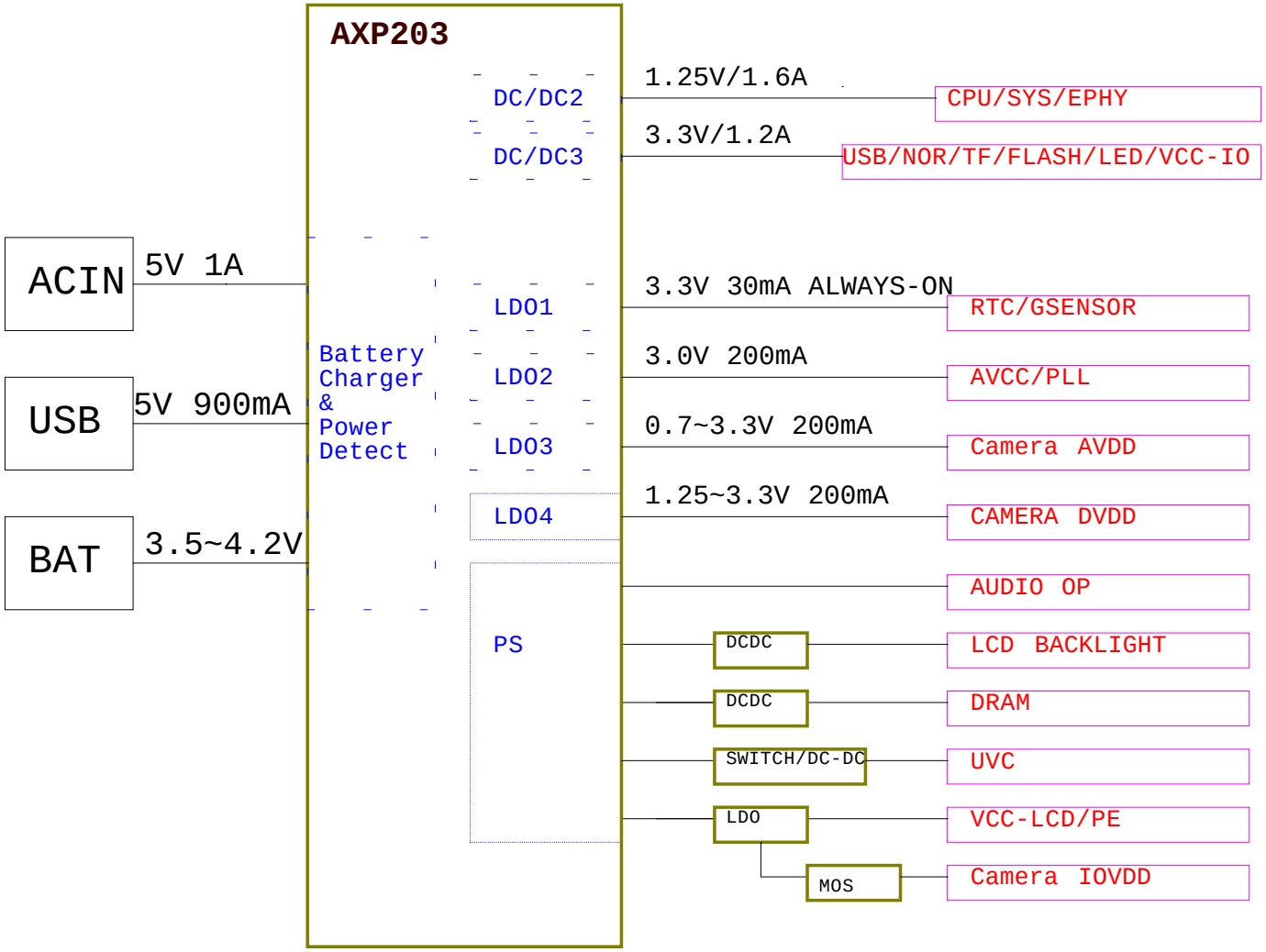
Size A3	Document Number <Doc>	Rev <RevCode>
Date: Monday, May 21, 2018	Sheet 1 of 1	

POWER TREE



AllWinner Technology Co.,Ltd		
Design Name: V3S_STD_CDR_V1_0		
Size: A3	Page Name: POWER TREE	Rev
Date: Monday, August 17, 2015	Sheet 3 of 12	

GPIO ASSIGNMENT

PIN	Define	CFG	Function
PB0	CTP-WAKE	1	CTP
PB1	CTP-INT	0	
PB2	LCD-BL-EN	1	LCD
PB3	LCD-PWR-EN	1	
PB4	PWM0	2	
PB5	PA-SHDN	1	AUDIO
PB6	TWI0-SCK	2	PMU
PB7	TWI0-SDA	2	
PB8	TWI1_SCK	2	G-SENSOR
PB9	TWI1_SDA	2	

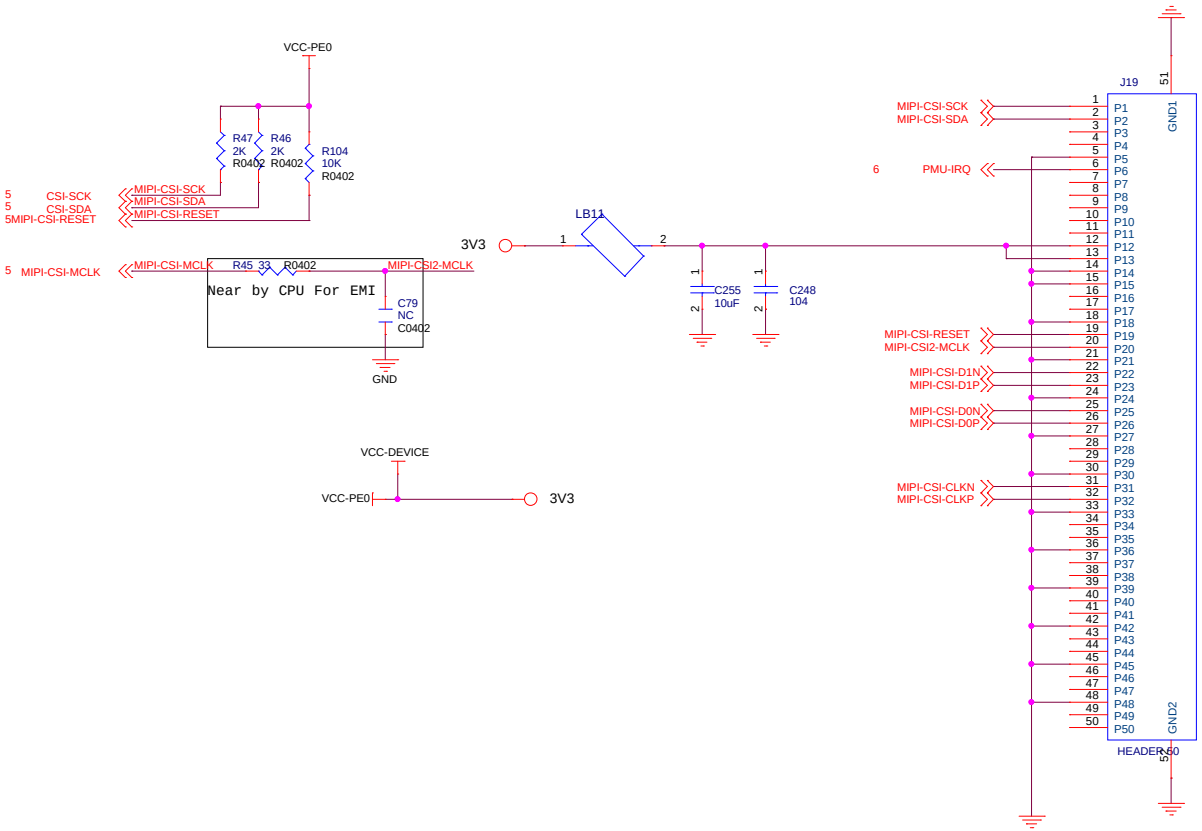
PIN	Define	CFG	Function
PC0	SPI0_MISO	3	NOR/ NAND
PC1	SPI0_CLK	3	
PC2	SPI0_CS	3	
PC3	SPI0_MOSI	3	

PIN	Define	CFG	Function
PE0	LCD_CLK	3	LCD
PE1	LCD_DE	3	
PE2	LCD_HSYNC	3	
PE3	LCD_VSYNC	3	
PE4	LCD_D2	3	
PE5	LCD_D3	3	
PE6	LCD_D4	3	
PE7	LCD_D5	3	
PE8	LCD_D6	3	
PE9	LCD_D7	3	
PE10	LCD_D8	3	
PE11	LCD_D11	3	
PE12	LCD_D12	3	
PE13	LCD_D13	3	
PE14	LCD_D14	3	
PE15	LCD_D15	3	
PE16	LCD_D18	3	
PE17	LCD_D19	3	
PE18	LCD_D20	3	
PE19	LCD_D21	3	
PE20	CSI-MIPI-MCLK	2	MIPI
PE21	CSI-SDA	2	
PE22	CSI-SCK	2	
PE23	LCD_D22	3	LCD
PE24	LCD_D23	3	

PIN	Define	CFG	Function
PF0	SDC0-D1	2	TF CARD
PF1	SDC0-D0	2	
PF2	SDC0-CLK	2	
PF3	SDC0-CMD	2	
PF4	SDC0-D3	2	
PF5	SDC0-D2	2	
PF6	SDC0-DET	0	

PIN	Define	CFG	Function
PG0	MIPI-CSI-RESET	1	GPIO
PG1	MIPI-CSI-PWDN	1	
PG2	PG2	0	
PG3	USB-DRVVBUS	1	
PG4	USB-ID	0	
PG5	PMU-IRQ	0	

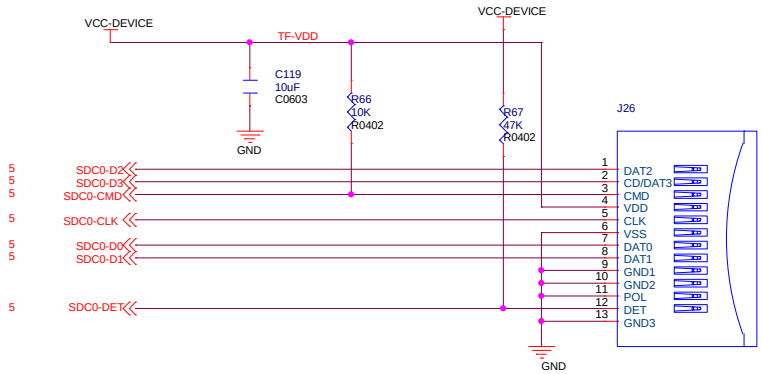
CAMERA



AllWinner Technology Co.,Ltd		
Design Name: V3S_STD_CDR_V1_0		
Size: A3	Page Name: CAMERA-MIPI	Rev
Date: Sunday, May 20, 2018	Sheet 7 of 12	

SD CARD

CARD0



AllWinner Technology Co.,Ltd	
Design Name	V3S_STD_CDR_V1_0
Size A3 / Page Name	MEMORY / Rev
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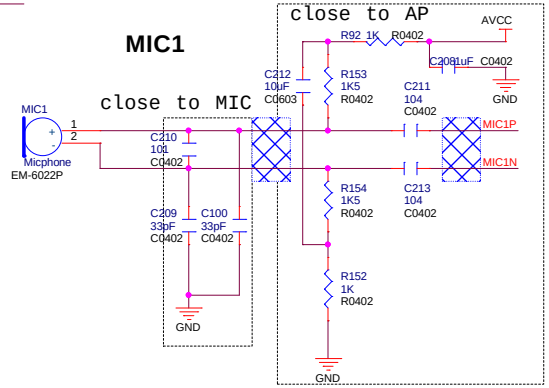
AUDIO

Analog Microphone (Main MIC)

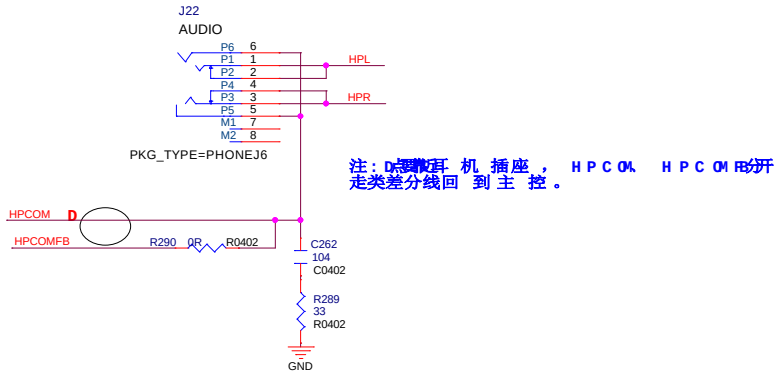
5 MIC1P
5 MIC1N



Differential pairs

OPTION:Head Phone

5 MIC-HBIAS
5,10 HPR
5,10 HPL
5 HPCOM
5 HPCOMFB



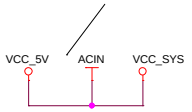
注：D:耳机插座，HPCOM、HPCOMFB分开走类差分线回到主控。

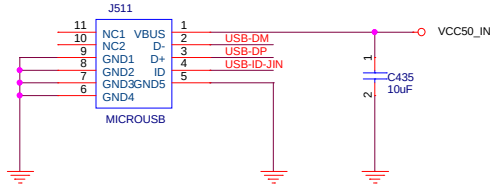
AllWinner Technology Co.,Ltd		
Design Name: V3S_STD_CDR_V1_0		
Size: A3	Page Name: Speaker/Microphone/LED	Rev
Date: Thursday, April 12, 2018	Sheet 10 of 12	

USB/WIFI/KEY/GSENSOR

USB OTG/UVC

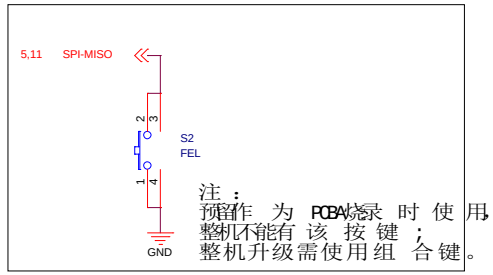
注：
USB-ID用作OTG功能时检测，用来区别HOST还是DEVICE接入；倒车检测功能时USB摄像头上实现，通过USB上报。

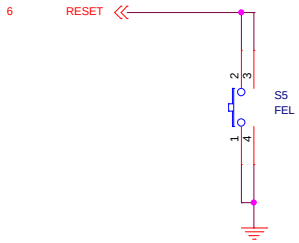


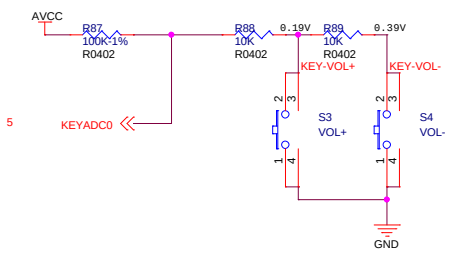


KEY

注：每隔0.2V一个按键







AllWinner Technology Co.,Ltd		
Design Name: V3S_STD_CDR_V1_0		
Size: A3	Page Name: USB/WIFI/KEY/GSENSOR	Rev
Date: Sunday, May 20, 2018	Sheet 11 of 12	