mcu_3v3

J1

pcie_3v3aux

bsb_tx3_p
bsb_tx3_n

bsb_tx2_p
bsb_tx2_n

bsb_tx1_p
bsb_tx1_n
bsb_tx0_p
bsb_tx0_n

jtag0_tdi
jtag0_tdo
jtag0_tms
jtag0_tck

jtag2_tdi
jtag2_tdo
jtag2_tms
jtag2_tck

pmbus_ctrl
pmbus_clk

pcie_smclk
pcie_perstn
ipmi_icsp_miso
ipmi_icsp_sck

2V5 1V8

bsb_rx3_p
bsb_rx3_n

bsb_rx0_p
bsb_rx0_n

bsb_rx1_p
bsb_rx1_n
bsb_rx2_p
bsb_rx2_n

jtag1_tdi
jtag1_tdo
jtag1_tms
jtag1_tck

jtag3_tdi
jtag3_tdo
jtag3_tms
jtag3_tck

pmbus_alert
pmbus_data

pcie_smdat
ipmi_gpio
ipmi_icsp_mosi
ipmi_icsp_resetn

1V8 2V5

MEC1-140-02-S-D-A

1V8
R1
2R7
C1
10uF 10v

J2
Pinstrip P2.54mm
jtag3_tdi
jtag3_tdo
jtag3_tms
jtag3_tck

2V5
R2
2R7
C2
10uF 10v

J3
Pinstrip P2.54mm
jtag0_tdi
jtag0_tdo
jtag0_tms_in
jtag0_tck_in

2V5
R3
2R7
C3
10uF 10v

J4
Pinstrip P2.54mm
jtag1_tdi
jtag1_tdo
jtag1_tms_in
jtag1_tck_in

2V5
R4
2R7
C4
10uF 10v

J5
Pinstrip P2.54mm
jtag2_tdi
jtag2_tdo
jtag2_tms_in
jtag2_tck_in

pcie_3v3aux
mcu_3v3
R5
2R7
R6
2R7

J10
pcie_smclk
pcie_smdat
pcie_perstn
ipmi_gpio

C5
10uF 10v
C6
10uF 10v

Pinstrip P2.54mm

J6
73251-2200
bsb_tx3_p

J7
73251-2200
bsb_tx3_n

J8
73251-2200
bsb_tx2_p

J9
73251-2200
bsb_tx2_n

J11
73251-2200
bsb_tx1_p

J12
73251-2200
bsb_tx1_n

J13
73251-2200
bsb_tx0_p

J14
73251-2200
bsb_tx0_n

J18
73251-2200
bsb_rx3_p

J19
73251-2200
bsb_rx3_n

J20
73251-2200
bsb_rx0_p

J21
73251-2200
bsb_rx0_n

J22
73251-2200
bsb_rx1_p

J23
73251-2200
bsb_rx1_n

J24
73251-2200
bsb_rx2_p

J25
73251-2200
bsb_rx2_n

2V5

C7
1uF

2V5

U1
1OE
1A1
2Y4
1A2
2Y3
1A3
2Y2
1A4
2Y1
GND
VCC
2OE
1Y1
2A4
1Y2
2A3
1Y3
2A2
1Y4
2A1
SN74LVC244ADWR

jtag0_tms_in
jtag0_tck
jtag0_tms_in
jtag0_tck
jtag0_tms_in
jtag0_tck
jtag0_tms_in
jtag0_tck

jtag0_tms
jtag0_tck_in
jtag0_tms
jtag0_tck_in
jtag0_tms
jtag0_tck_in
jtag0_tms
jtag0_tck_in
jtag0_tms
jtag0_tck_in

jtag0_tms_in   jtag0_tck_in

R7
DNP

R8
DNP

jtag0_tms   jtag0_tck

J15
STRIP P2.54mm 5X2

pmbus_ctrl
pmbus_clk

pmbus_alert
pmbus_data

1V8

R9
2R7

C9
10uF 10v

2V5

C8
1uF

2V5

U2
1OE
1A1
2Y4
1A2
2Y3
1A3
2Y2
1A4
2Y1
GND
VCC
2OE
1Y1
2A4
1Y2
2A3
1Y3
2A2
1Y4
2A1
SN74LVC244ADWR

jtag1_tms_in
jtag1_tck
jtag1_tms_in
jtag1_tck
jtag1_tms_in
jtag1_tck
jtag1_tms_in
jtag1_tck

jtag1_tms
jtag1_tck_in
jtag1_tms
jtag1_tck_in
jtag1_tms
jtag1_tck_in
jtag1_tms
jtag1_tck_in

jtag1_tms_in   jtag1_tck_in

R10
DNP

R11
DNP

jtag1_tms   jtag1_tck

J16

jtag3_tms
jtag3_tck
jtag3_tdo
jtag3_tdi

87832-1420

mcu_3v3

R12
2R7

C10
10uF 10v

J17

ipmi_icsp_mosi

ipmi_icsp_miso
ipmi_icsp_sck
ipmi_icsp_resetn

HEADER 3X2

2V5

C11
1uF

2V5

U3
1OE
1A1
2Y4
1A2
2Y3
1A3
2Y2
1A4
2Y1
GND
VCC
2OE
1Y1
2A4
1Y2
2A3
1Y3
2A2
1Y4
2A1
SN74LVC244ADWR

jtag2_tms_in
jtag2_tck
jtag2_tms_in
jtag2_tck
jtag2_tms_in
jtag2_tck
jtag2_tms_in
jtag2_tck

jtag2_tms
jtag2_tck_in
jtag2_tms
jtag2_tck_in
jtag2_tms
jtag2_tck_in
jtag2_tms
jtag2_tck_in
jtag2_tms
jtag2_tck_in

jtag2_tms_in   jtag2_tck_in

R13
DNP

R14
DNP

jtag2_tms   jtag2_tck

IFLEX board v.1.00.00, In2H2 / Nearist Inc.

Title
Breakout and service card

Size
A3

Document Number
Designed: L. Minati, Checked: R. Brown, M. Fandrich, M. McCormick

Rev
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Date: Wednesday, May 31, 2017   Sheet   2   of   2