CL485/CLG485 Packages—XC7Z012S and XC7Z015

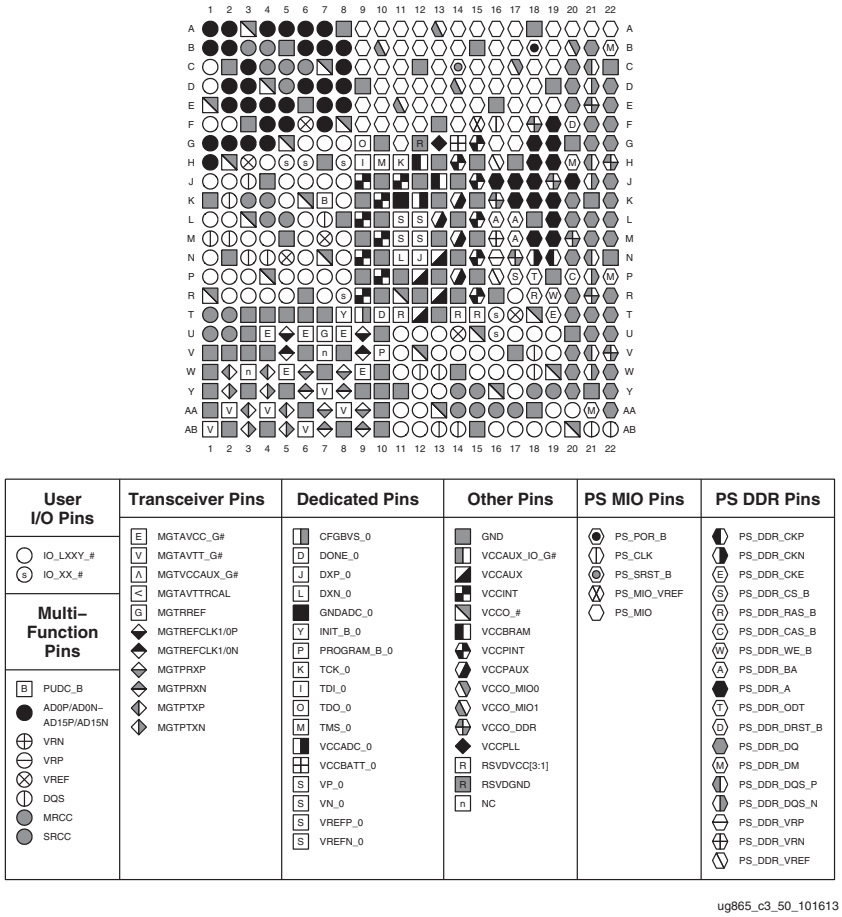

Figure 3-9: **CL485/CLG485 Packages—XC7Z012S and XC7Z015 Pinout Diagram**

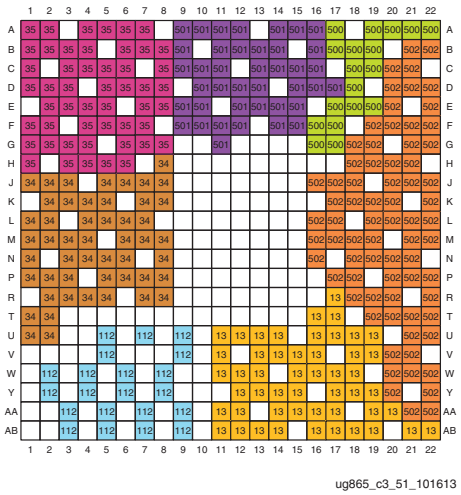

Figure 3-10: **CL485/CLG485 Packages—XC7Z012S and XC7Z015 I/O Banks**

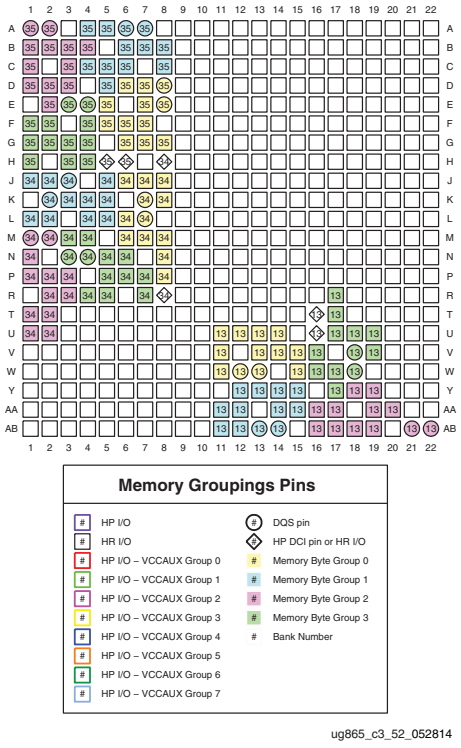

Figure 3-11: **CL485/CLG485 Packages—XC7Z012S and XC7Z015 Memory Groupings**

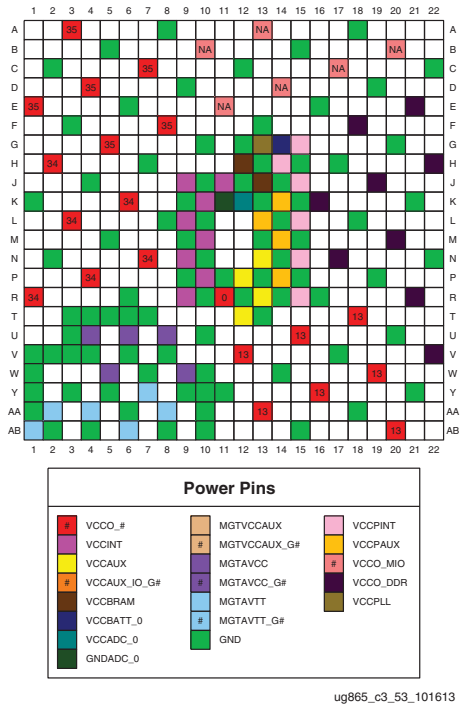

Figure 3-12: **CL485/CLG485 Packages—XC7Z012S and XC7Z015 Power and GND Placement**