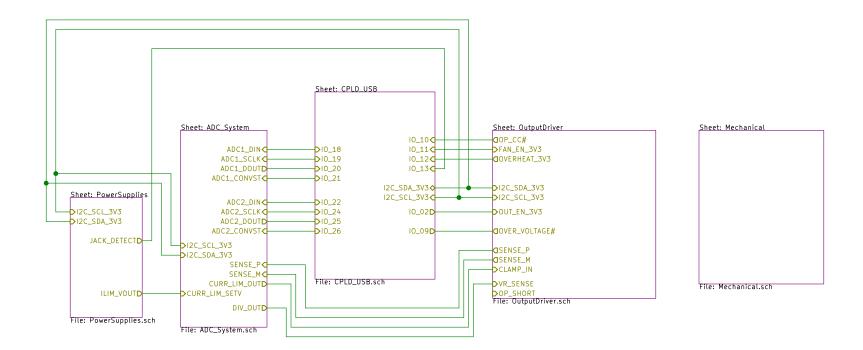


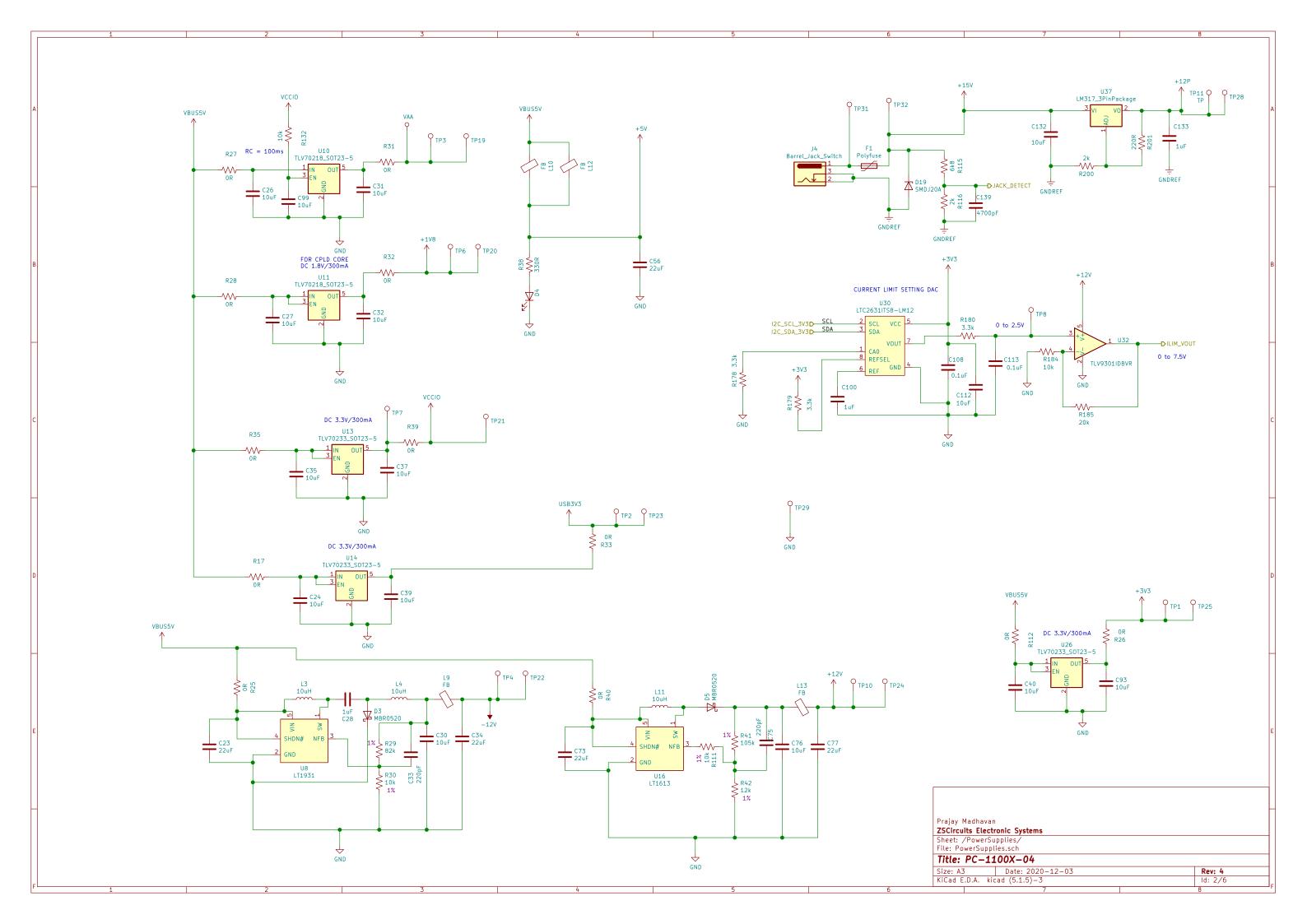
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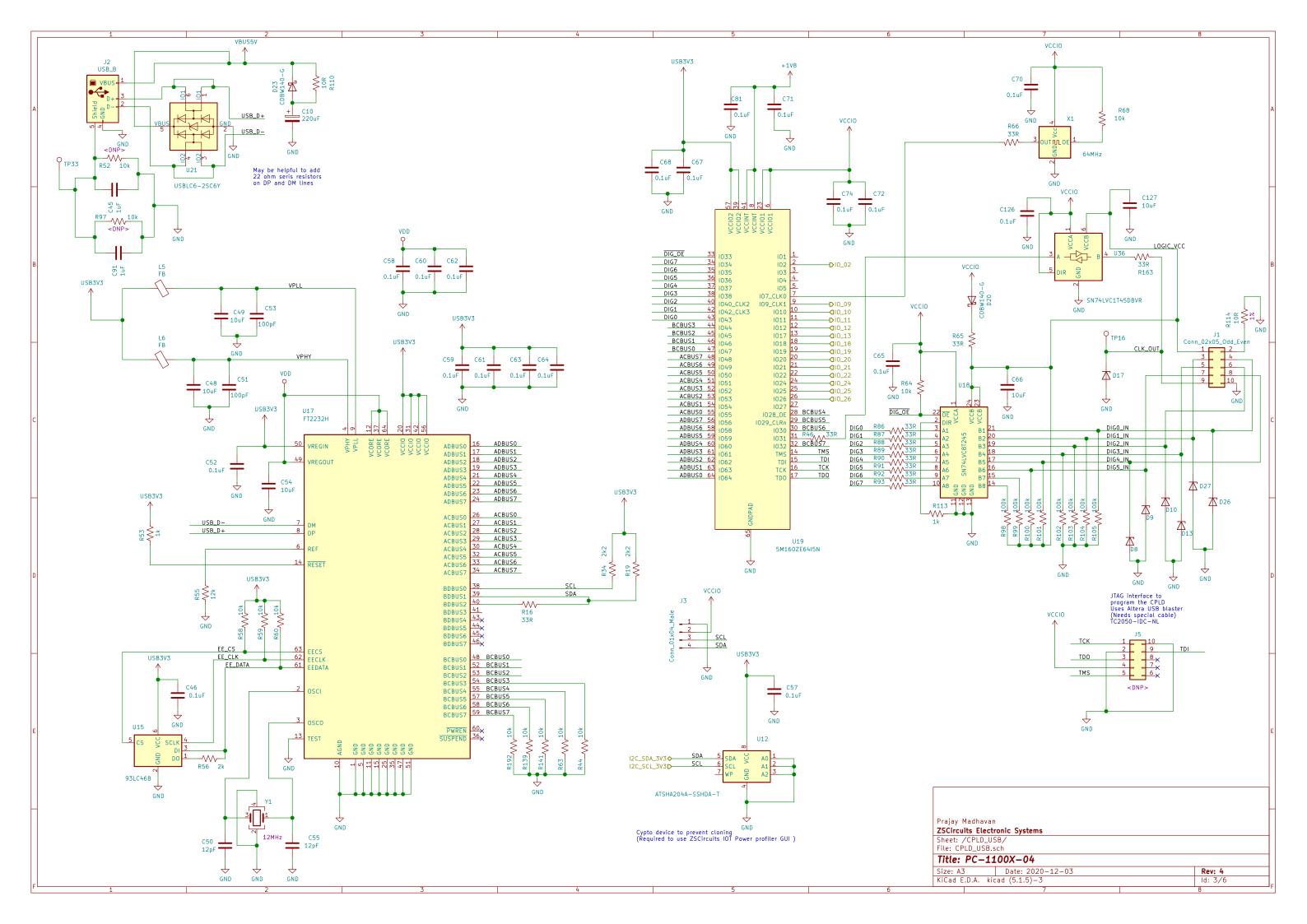
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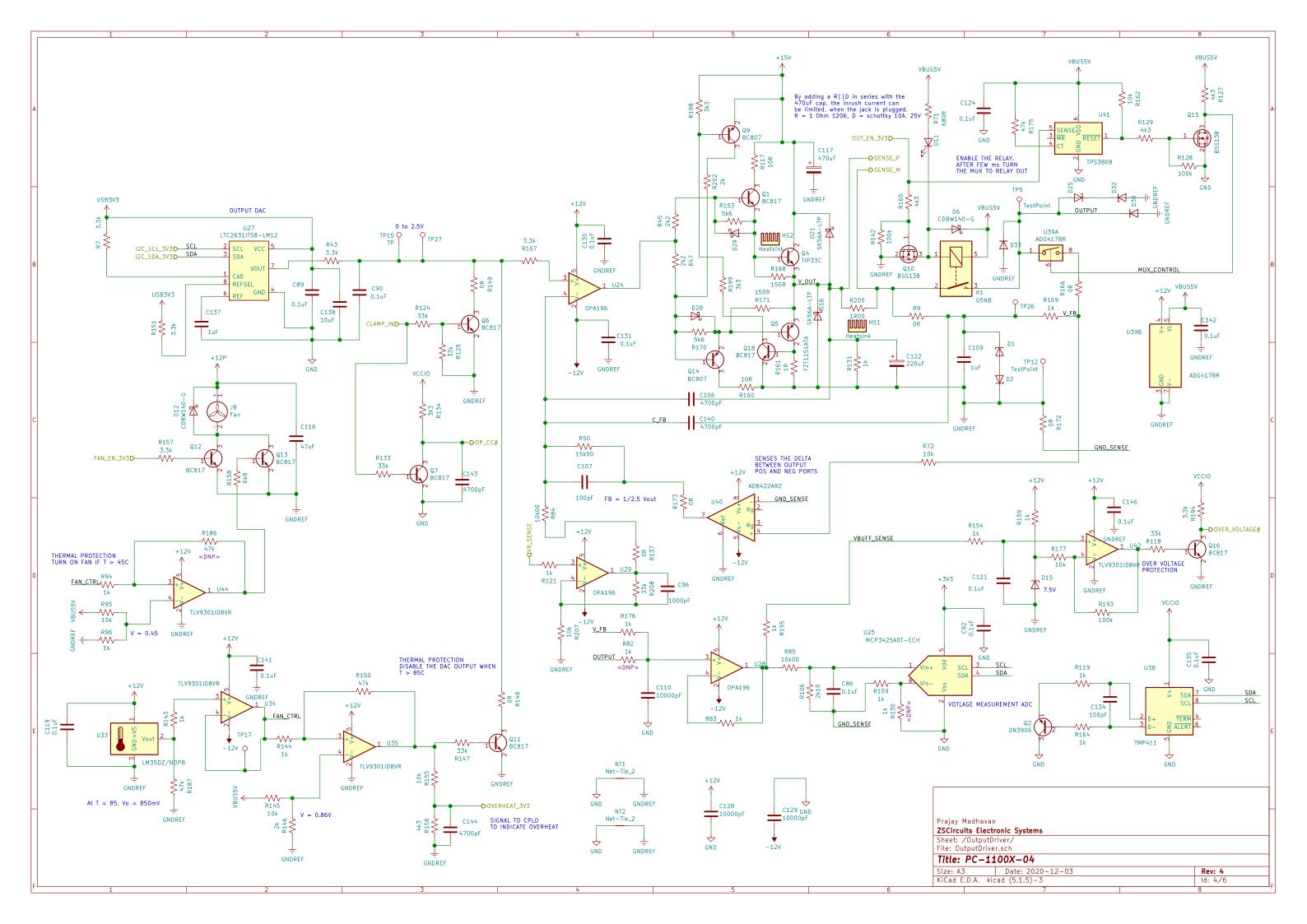
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SCHEMATICS FOR THE PCB FOR ZS-1100-A









	1	2	3	4	5
Α		P10 FAN 12V BLDC 30x30x10mm P14 Button head Bolt M3x16 (4 Qty) P15 Nut M3 (4 Qty) P16 Molex cable 2-pin	P11 SSD1306 I2C OLED 0.96" MODULE WHITE P9 Button head bolt M2x6 (4 Qty) P12 Nut M2 (8 Qty) P13 Molex cable 4 pin	P2 Aluminium Case P17 Bumpon 12x5mm (Qty = 4) P7 Banana Jack RED P8 Banana Jack Black	P3 CABLE USB A MALE - B MALE 1M BLK P1 DC Power Adapter 15V, 65W P4 Banana to Crocodile clips Black 0.5m P5 Banana to Crocodile clips Red 0.5m
В		P6 Button head Bolt M3x8 (2 Qty) P18 Nut M3 (2 Qty)	P19 441-R311-GRX	7. Removed CIU/	Rev3 for the Analog Mux is inverted now c caps at output (47uF). For stability esistor to 330 Ohms dd sensing of the Voltage measurement ADC. stability for Low current measurement ent limiting A turn ON, demanded by SAR ADC e 12C bus. Can be used to count the warm-up time 14631 with OPA180/OPA197 for better stability pe protection
C		FD1 FD2 FIDUCIAL FD4 FIDUCIAL	LG Made ZS L LG2 RoH ZS LO	15. Added protection 14. Added reverve VB 15. Added diode prote 16. Changed the TO2: 17. Replaced LM321 18. Added reverse vo 19. Removed buzzer 20. Removed RTC and 21. Reduced the I2C Based on Coupon bose 22. Added back the o S 23. Remove the Drive	for high reverse current. Limit to U.5A E protection for driver transistors in darlington ection for high gain AD8422 20-4 sense resistor with T0220-2 , 1 Ohm with TLV9301 from critical path due to offset tlage protection diodes at output d replaced the CPLD with 160 logic blocks. Timer is inside CPLD now pull—up to 2.2k from 2.7k ard === utput short circuit protection r_EM/Dis circuit as it could cause oscillations
		H3 MountingHole H5 MountingHe	ole		
, D		H4 MountingHole H2 MountingH	ole	Prajay Madhavan ZSCircuits Electronic Syste Sheet: /Mechanical/ File: Mechanical.sch Title: PC-1100X-04 Size: A4 Date: 20 KiCad E.D.A. kicad (5.1.5)	4 020-12-03 Rev: 4

