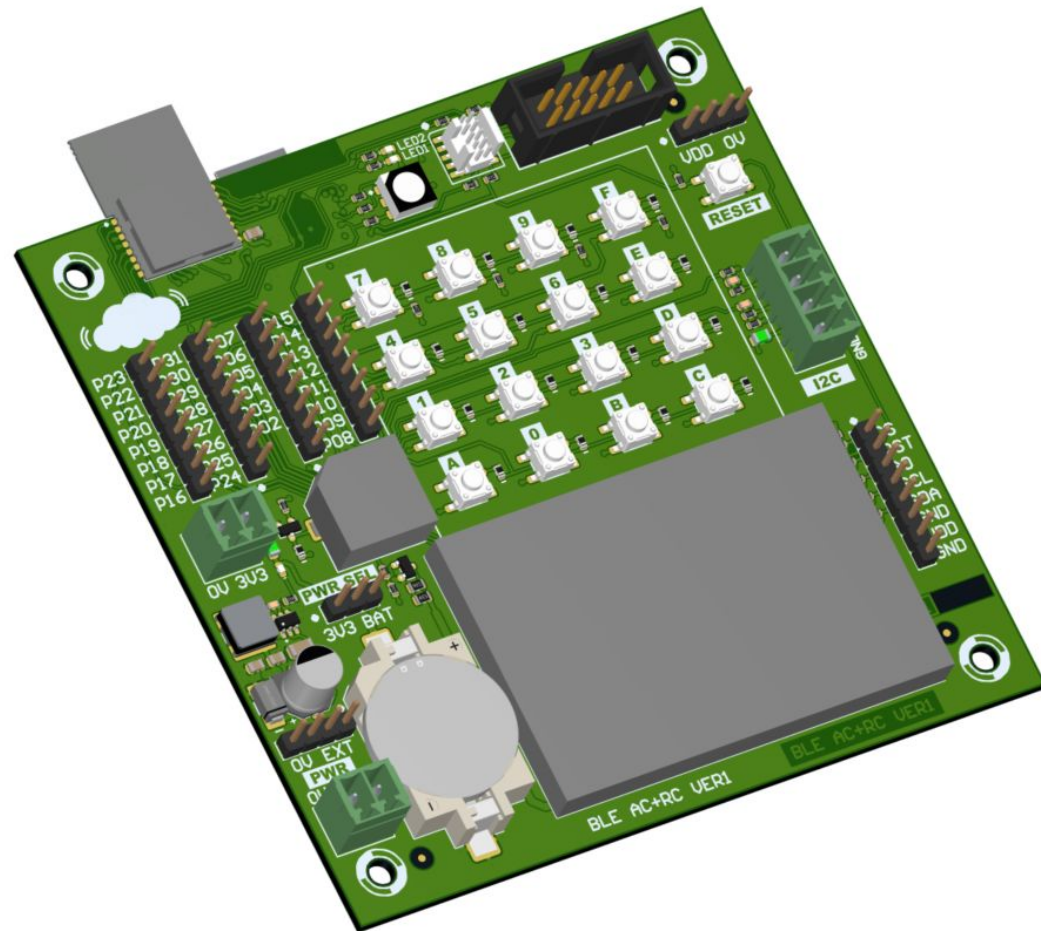


AC+RC Unit (BLE 5.0)

Project: AC+RC Unit
Version: VER1
Revision: REV1
Project State: Released
Variant: BLE_FANSTEL
Print date: 2020-01-13

TOP VIEW



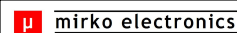
| Page | Index |
|------|-----------------|
| --- | ----- |
| 01 | Cover page |
| 02 | Top schematic |
| 03 | BLE + KEY + LED |
| 04 | LCD + PWR |

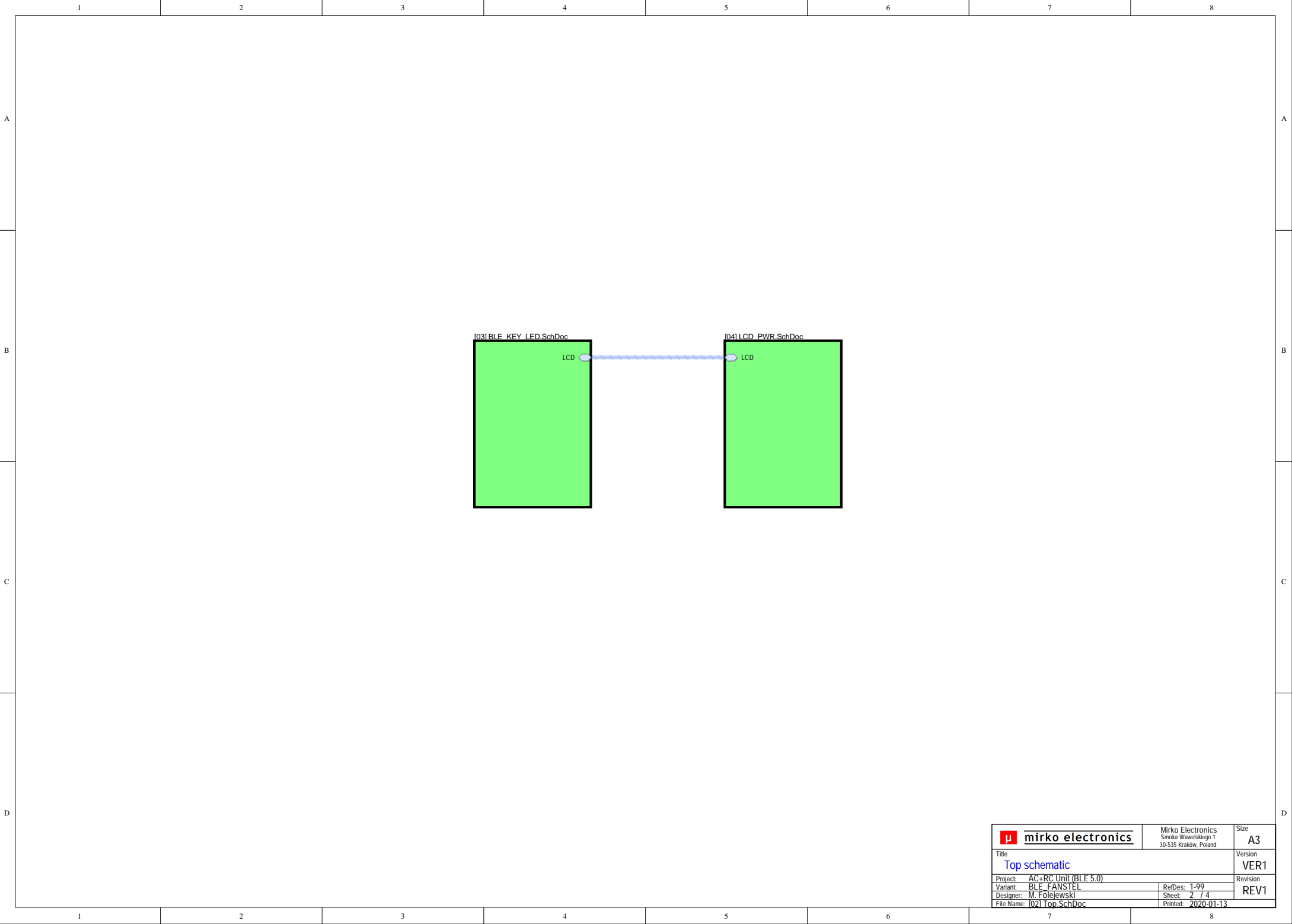
[02] Top_SchDoc

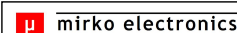
7.1.3 GPIO pins located near the radio

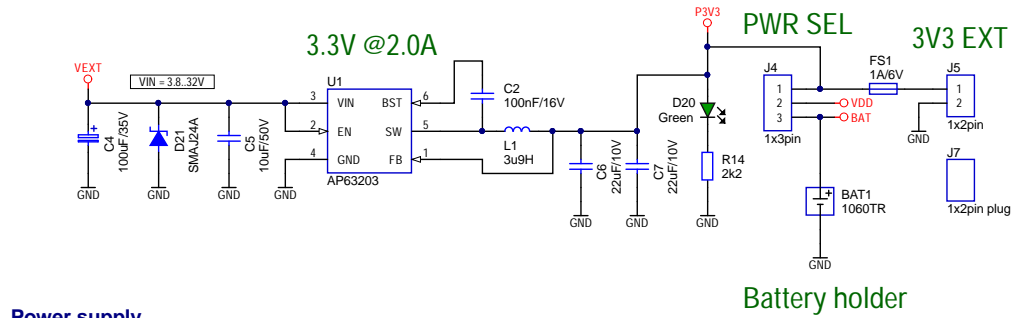
Radio performance parameters, such as sensitivity, may be affected by high frequency digital I/O with large sink/source current close to the radio power supply and antenna pins.

| GPIO | QFN48 pin | QFN32 pin | Recommended usage |
|-------|-----------|-----------|------------------------------------|
| P0.25 | 37 | 26 | Low drive, low frequency I/O only. |
| P0.26 | 38 | | |
| P0.27 | 39 | | |
| P0.28 | 40 | 27 | |
| P0.29 | 41 | | |

| | | | |
|---|--|---|------------------|
|  | | Mirko Electronics Smoka Wawelskiego 1 30-535 Kraków, Poland | Size A3 |
| Title AC+RC Unit (BLE 5.0) Board | | Version VER1 | Revision REV1 |
| Project AC+RC Unit (BLE 5.0) | | RefDes: - | |
| Variant BLE_FANSTEL | | Sheet: 1 / 4 | |
| Designer: M. Folejewski | | Printed: 2020-01-13 | |
| File Name: [01] Cover_page.SchDoc | | | |

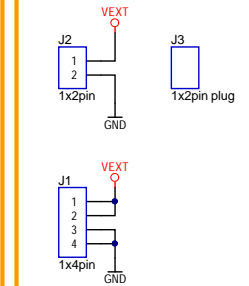


| | | | | |
|---|--|---|--|-------------------------|
|  | | Mirko Electronics Smoka Wawelskiego 1 30-535 Kraków, Poland | | Size A3 |
| Title Top schematic | | | | Version VER1 |
| Project: AC+RC Unit (BLE 5.0) | | RefDes: 1-99 | | Revision REV1 |
| Variant: BLE_FANSTEL | | Sheet: 2 / 4 | | |
| Designer: M. Folejewski | | Printed: 2020-01-13 | | |
| File Name: I02 Top.SchDoc | | | | |

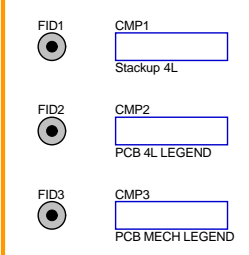


Power supply

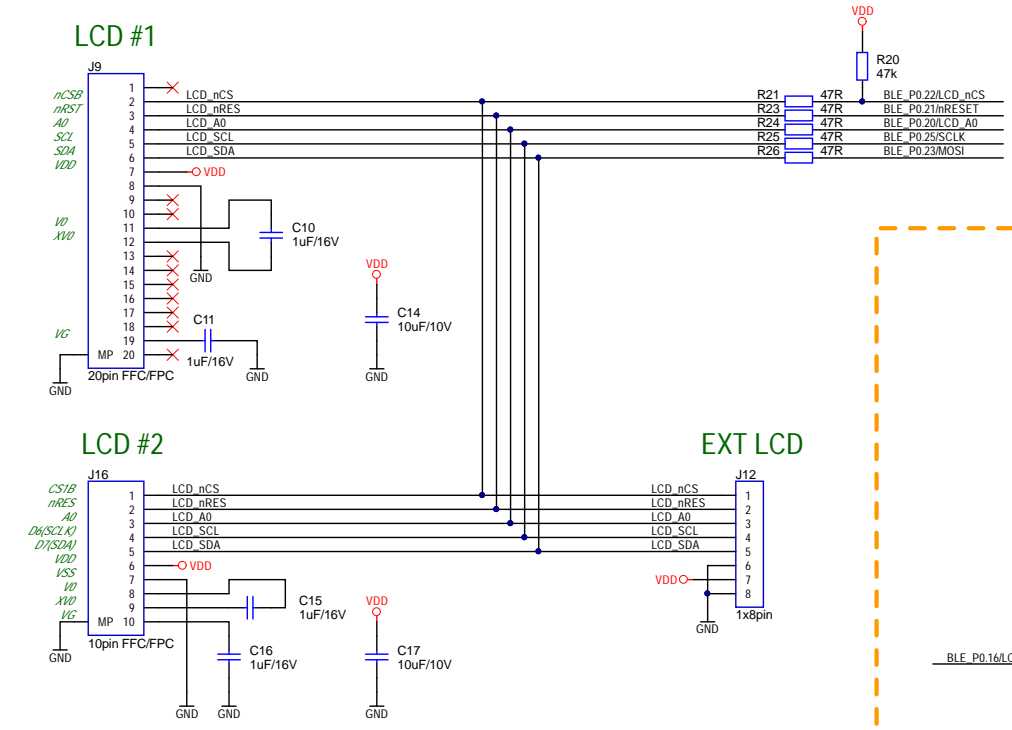
Battery holder



Power connector

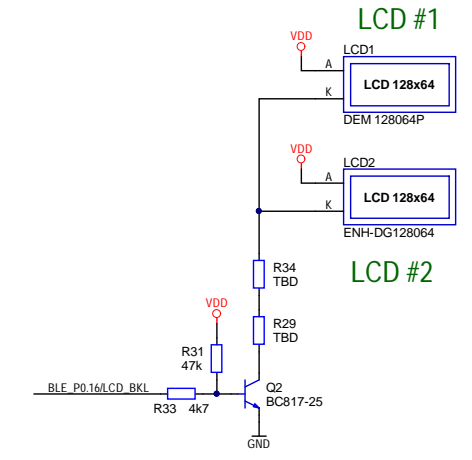
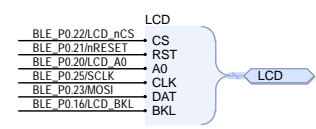


PCB marking



LCD display module (128x64)

EXT LCD



LCD backlight