

Channel width mod: non-ideal at sat.

$$I_D = \frac{1}{2} k' \frac{W}{L} (V_{GS} - V_T)^2 \quad \text{pinch-off}$$

$$= \frac{1}{2} k' \frac{W}{L} (V_{GS} - V_T)^2 \left(1 + \frac{\Delta L}{L}\right)$$

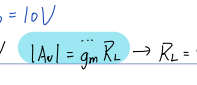
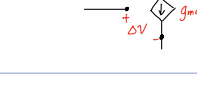
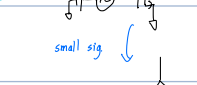
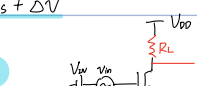
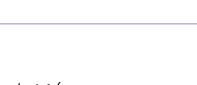
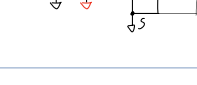
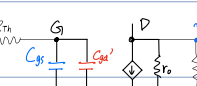
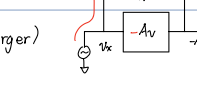
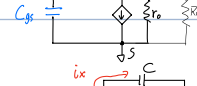
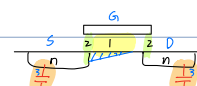
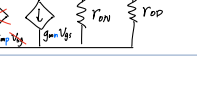
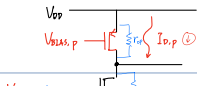
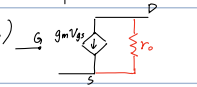
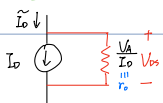
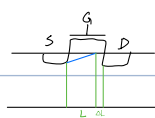
$$= \frac{1}{2} k' \frac{W}{L} (V_{GS} - V_T)^2 \left(1 + \lambda V_{GS}\right) \quad \frac{1}{\lambda} \approx V_A \quad (\text{Early voltage})$$

$$= I_D + \frac{I_D}{V_A} V_{GS} \rightarrow \text{lin!}$$

non-ideal I source (large sig)

$$ss \frac{\partial I_D}{\partial V_{GS}} \bigg|_{bias} = \frac{I_D}{V_A} = \frac{1}{r_o}$$

$r_o \sim 100k$ (want ∞)



$$I_D = I_S \left(e^{\frac{V_D}{n\phi_t}} - 1 \right) \quad \text{Diode}$$

$$\phi = \frac{kT}{q} = \frac{1.38 \times 10^{-23} \text{ J/K} \cdot 300 \text{ K}}{1.6 \times 10^{-19} \text{ C}} = 25.875 \text{ mV}$$

$$V_D = n\phi_t \ln \left(\frac{I_D}{I_S} + 1 \right)$$

$$V_{D1} - V_{D2} = n \frac{kT}{q} \ln \frac{I_{S2} I_{D1}}{I_{S1} I_{D2}}$$

$$V_D = V_{D0} - 2m(T - T_0)$$

$$C = \frac{I_L T_P}{V_{RIP}}$$

$$\alpha = \frac{I_C}{I_E} = \frac{\beta}{1 + \beta} \quad \beta = \frac{I_C}{I_B} = \frac{\alpha}{1 - \alpha}$$

$$V_{OUT} = V_{CC} - \frac{R_2}{R_1} \beta (V_{D0} - 0.75) - \frac{R_1}{R_2} \beta \Delta V$$

$$\alpha I_E = \beta I_B \quad \text{PNP}$$

$$r_e = \frac{\phi_t}{I_E}$$

$$r_e = \frac{\phi_t}{I_E}$$

$$g_m = \frac{I_C}{\phi_t}, \quad r_\pi = \frac{\beta}{g_m}$$

$$MOS \quad N \quad \frac{1}{\sqrt{f}} \quad \frac{1}{\sqrt{f}} \quad \frac{1}{\sqrt{f}}$$

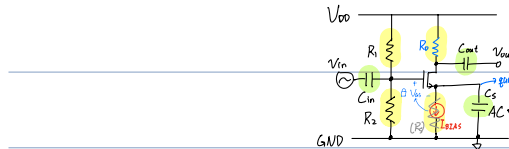
$$P \quad \frac{1}{\sqrt{f}} \quad \frac{1}{\sqrt{f}} \quad \frac{1}{\sqrt{f}}$$

App. Current source load (more gain w/ r_o)

$V_{in,n} \rightarrow$ contradicting $\Delta i \rightarrow$ flows thru r_o (huge) \rightarrow huge ΔV

$$A_v = -g_{m,n} (r_{on} \parallel r_{op})$$

$$|A_v| = k_n \frac{W}{L} V_{D0,n} \frac{V_A}{2I_D} = \frac{V_A}{V_{D0,n}}$$



Parasitic Cap 1. gate 2. overlap 3. pin-jet cap

- Triode $C_{gs} = C_{sd} = \frac{1}{2} W L C_{ox}$

(tie S, D) E. op amp compensation

- Sat. pinched off at D side

$$C_{gs} = \frac{2}{3} W L C_{ox} \quad C_{gd} \text{ small, pinched}$$

High freq. Turn bias $\rightarrow V_S = \frac{R_1 R_2}{R_1 + R_2} V_{in}$

$R_{th} = R_1 \parallel R_2 \parallel R_S$

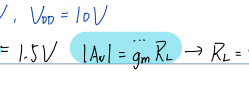
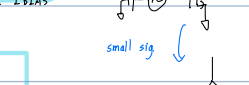
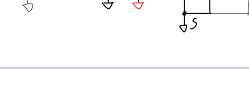
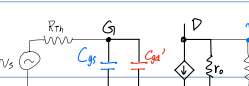
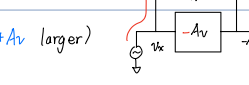
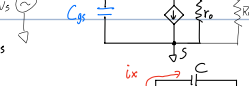
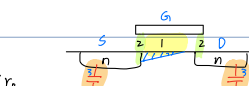
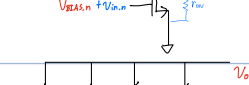
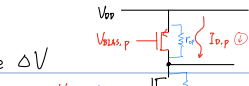
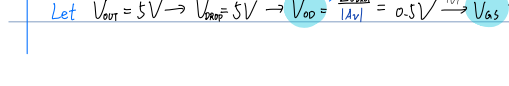
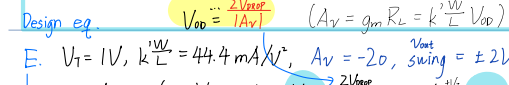
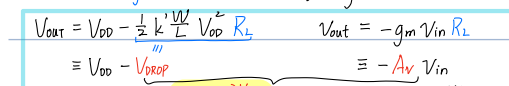
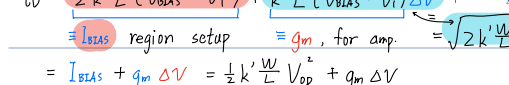
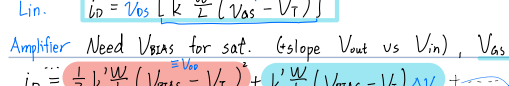
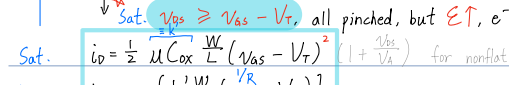
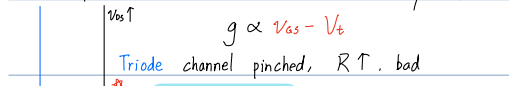
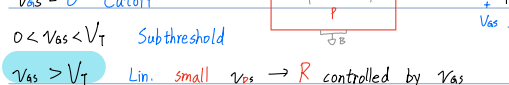
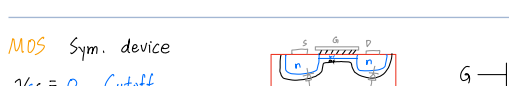
Miller effect $i_x = j\omega C (1 + A_v) V_x$ (cap appears $1 + A_v$ larger)

A_v can be made w/ opamp + pot \rightarrow varCap

$$C_{gd} \rightarrow C_{gd} (1 + g_m R_D \parallel R_L \parallel R_S) \rightarrow \text{to gnd}$$

$$f_{corner} = \frac{1}{2\pi R_{th} C_{gs} + C_{gd} (1 + g_m (R_D \parallel R_L \parallel R_S))} \quad \text{LTPF}$$

$$2p_{lz} \rightarrow 1p$$



$$\text{active } V_{GS} - V_T > 0$$

$$V_{SG} - |V_{TP}| > 0$$

$$\text{sat } V_{DS} \geq V_{GS} - V_T$$

$$V_{SD} \geq V_{SG} - |V_{TP}|$$

$$I_D = \frac{1}{2} k' \frac{W}{L} (V_{GS} - V_T)^2$$

$$g_m = \sqrt{2 k' \frac{W}{L} I_{BIAS}}$$

$$w/ \text{load } |A_v| = g_m (R_D \parallel R_L)$$

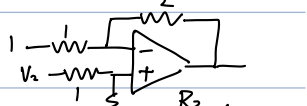
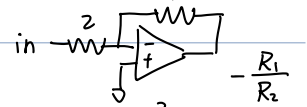
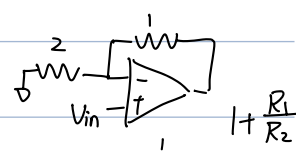
Cap open vs. gnd

inverting! amplify!

$$V_{os} \pm$$

$$\omega, f (2\pi)$$

DC path for I_{bias}



V_S, V_E may not be 0

$$V_P = \sqrt{2} V_{rms}$$

N, P (up, down?)