Virtual Ideal Functionality Framework High-Level Design Overview

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Features

Example

Scheduling

Lurrent Status

VIFF Features

- API for writing secure multi-party computations
 - ► Easy to use: a + b + c instead of add(add(a, b), c)
 - Simple if you already know Python
 - A custom language could be added
- ► Efficient asynchronous design
 - Automatic parallelism
 - ► Single-threaded (no locks!)
- ▶ Simple architecture
 - ► Small code base (2,500 lines)
 - ► Few layers of abstraction...
 - ...but hopefully sufficiently many

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- ▶ Players P_1 , P_2 , and P_3
- ightharpoonup Sharing x, y, and z
- ▶ Opens the result *r*





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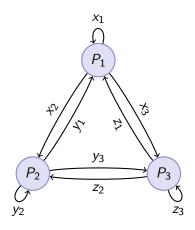
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$$x_1, y_1, z_1$$

$$P_1$$

- ▶ Players P_1 , P_2 , and P_3
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 x_3, y_3, z_3

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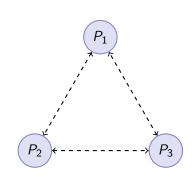
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- ▶ Players P_1 , P_2 , and P_3
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- ▶ Compute $r = (x + y) \cdot z$
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 r_1 P_1

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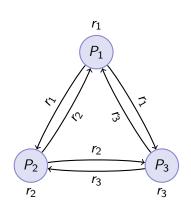
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 P_1

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import sys
from viff.field import GF
from viff.config import load_config
from viff.runtime import Runtime
from viff.util import dprint

$$\begin{split} & \mathsf{Z31} = \mathsf{GF}(31) \\ & \mathsf{my_id}, \ \mathsf{conf} = \mathsf{load_config}(\mathsf{sys.argv}[1]) \\ & \mathsf{my_input} = \mathsf{Z31}(\mathsf{int}(\mathsf{sys.argv}[2])) \end{split}$$

 $\begin{aligned} \text{rt} &= \text{Runtime}(\text{conf}, \ \text{my_id}, \ 1) \\ \text{x, y, z} &= \text{rt.shamir_share}(\text{my_input}) \\ \text{result} &= (\text{x} + \text{y}) * \text{z} \end{aligned}$

opened_result = rt.open(result)
dprint("Result: %s", opened_result)
rt.wait for(opened result)

Import standard and VIFF modules

► Load command line config

- Create Runtime, do calculation
- Open and print result

Example VIFF Program

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Import standard and VIFF modules

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Example Configuration File

```
[Player 1]
  host = camel09
  port = 7100
[Player 2]
  host = camel17
  port = 7200
[Player 3]
  host = camel19
  port = 7300
  [[prss keys]]
    1.3 = 0 \times 47D5B9B367DAFC9267EDF518AD5B5F396299L
    2.3 = 0 \times 7 EBC55E5CF1D014D081EA428B5F35FD12C64L
  [[prss dealer keys]]
    [[[Dealer 1]]]
      1.3 = 0 \times 903039893A06800A0FA7175CED8CC17B873BL
      2.3 = 0 \times 11C91354237193397589A1D1C6455F87CB79L
    # More PRSS keys...
# End of config
```

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Feature

Example

Scheduling

Current Statu

Asynchronous Design

- ▶ Entire tree is scheduled as once
- Operations wait on their operands
- ► Results are sent when ready
- Result is a form of "greedy scheduling"
- ► Implicit synchronization, no rounds

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Greedy Scheduling

Advantages:

- At least as efficient as round-based scheduling
- No cost when adding primitives (modular design)
- Automatic parallelism

Disadvantages:

▶ Not yet proven secure...

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What has been Implemented?

- Shamir sharing
- ► PRSS
- Opening
- Addition, multiplication, exclusive-or
- ► Classic SCET comparison

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Assumptions

Current primitives assume:

- ► Fixed number of players
- Passive and static adversary
- ▶ Threshold adversary structure, t < n/2

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Conclusion

- ▶ VIFF offers a light-weight design for doing MPC
- Asynchronous design gives automatic parallelism

Installation instructions, source code and more:

▶ http://viff.dk/

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Questions?

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