

C1 controls rise time: $1\text{nF} = 100\text{ }\mu\text{s}$
 R2 controls current: $I_{\text{LED}} = 100\text{mV}/R2$
 Maximum current is 1.5A
 +5V logic supply (may be between 4.5 and 40 V)
 $(V_{\text{CC}} - V_{\text{LED_DROP}}) * (I_{\text{LED}}) < 3\text{W}$
 If constraints on power dissipation cannot be met,
 substitute STSC2A, which has a power limit of 10W

Consider placing a ferrite bead between vcc and C3+LED
 Consider placing a ferrite bead between +5V and C2+ic power input

