Memory Map for combined output of
ex_app_led_blink when built with **ex_boot_uart**

Address			
0x000000			goto EZBL_Reset()
		Interrupt Vector Table	&IGT Trap ...
			&IGT Interrupt 0
			&Dispatch__U1RXInterrupt
			&Dispatch__U1TXInterrupt
			&IGT Interrupt ...
			&IGT Default Interrupt ...

Erase Page 0 · ex_boot_uart.merge.s/·gld · ex_boot_uart

Bootloader Code
EZBL_Reset()
__reset()
EZBL_WriteROMChecked()
EZBL_EraseAll()
EZBL_ReadPackedFlash()
UART_Reset()
Dispatch__U1RXInterrupt()
Dispatch__U1TXInterrupt()
_UxRXInterrupt() ISR
_UxTXInterrupt() ISR
...

0x000200

0x000400

Erase Page 1 to *n*

0xFF Filler to end of Erase Page
(prevents compiler from placing
application code here)

0x001800

EZBL_appBootloadState

__SP_init

goto __reset

Interrupt Goto Table (IGT)
goto Trap 0
goto Trap ...
goto _PWM1Interrupt()
goto _U1RXInterrupt()
goto Interrupt 2
goto Interrupt ...
goto Default Interrupt

Erase Page *n+1* to *m* · ex_app_led_blink

Application Code
__reset()
CRT
main()
InitializeBoard()
_PWM1Interrupt() ISR
_U1RXInterrupt() ISR
_U1TXInterrupt() ISR
...

0x0557F0 · **Flash Configuration Words**

Execution path for interrupts undefined in bootloader

Legend

— Reset Path →

— Shared Interrupt Path →

Not Erasable

Bootloader

Application