EC1834 201.1

Seite 1 / 201.1

File: EC1834_201.1.kicad_sch

EC1834_201.3 RAM

Seite 3 — RAM

O{MEMR MEMW MDPOE MDPEE}
OMD[0.15]
O{MEM[0..7] WE RAS0 RAS1 CAS1 CASH}

File: EC1834_201.3.kicad_sch

R52 — KWH 3936,
Widerstandsarray mit gemeinsamen
Pin 8 getauscht gegen RN mit gemeinsamen Pin 1!

{AENBRD AEN MEMR PB3 IOCHCK BHE DRQ0 DACK0BRD DACK0 DACK0 INTA IOR IOW XIOR XIOW XMEMR MEMW MEMW NMIADRCS TIMERCS PPICS NIMREGCS IRQ0 IRQ1 OSC OSC1 PCLK CLK86 MEMADRSEL NMI NPNPI RESET RESETDRV}O

SFW2O

SFW0

EC1834 201.2

Seite 2 / 201.2

Power

Seite 4
Stromversorgung

File: Power.kicad_sch

SFW1O   OSFW1

MD[0.15]O

{MEM[0..8] RAS0 RAS1 CAS1 CASH WE}

SFW2O   OSFW2

{AENBRD AEN MEMR PB3 IOCHCK BHE DRQ0 DACK0BRD DACK0 DACK0 INTA IOR IOW XIOR XIOW XMEMR MEMW MEMW NMIADRCS TIMERCS PPICS NIMREGCS IRQ0 IRQ1 OSC OSC1 PCLK CLK86 MEMADRSEL NMI NPNPI RESET RESETDRV MDPOE}O

File: EC1834_201.2.kicad_sch

Bus

Seite 5 — BUS

O{DACK0 DACK1 DACK2 DACK3 ALE RESETDRV CLK IOR IOW MEMR MEMW AEN IOCHRDY IOCHCK MASTER MEMCS16 DRQ0 DRQ1 DRQ2 DRQ3 TC IRQ3 IRQ4 IRQ5 IRQ6 IRQ7 IRQ10 IRQ11 IRQ14 IRQ15 OSC}

O{A[0..19] VA0 BHE}

File: Bus.kicad_sch

EC1834 Hauptplatine 201. — X1 Bus Connector auf Seite "BUS"

WDx – Drahtbrücken

Durch WD3 deaktiviert

Durch WD2 deaktiviert

Mario Gögel 2022
Sheet: /EC1834 201.1/
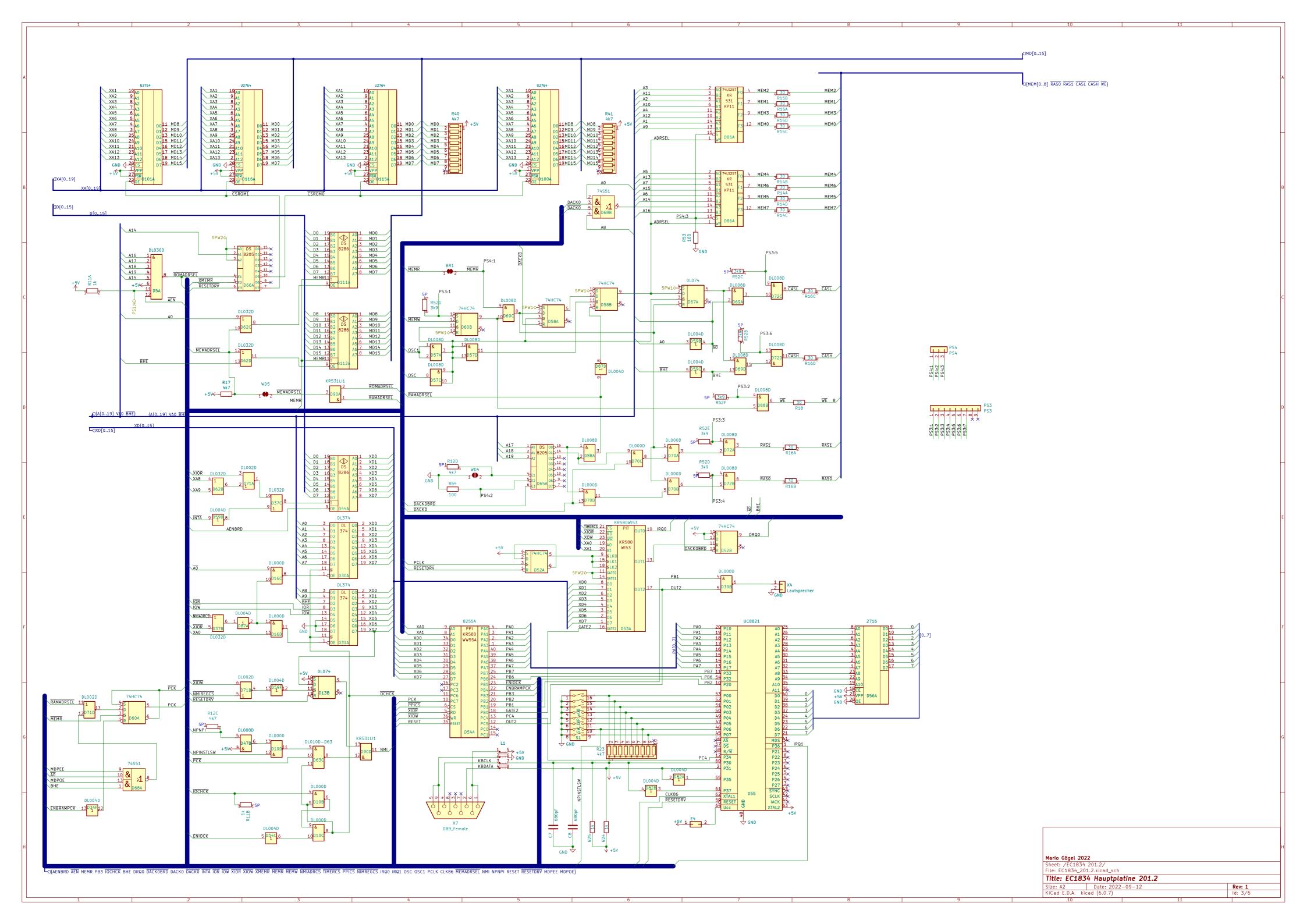File: EC1834_201.1.kicad_sch
Title: EC1834 Hauptplatine 201.
Size: A2   Date: 2022-09-12   Rev:
KiCad E.D.A. kicad (6.0.7)   Id: 2/6
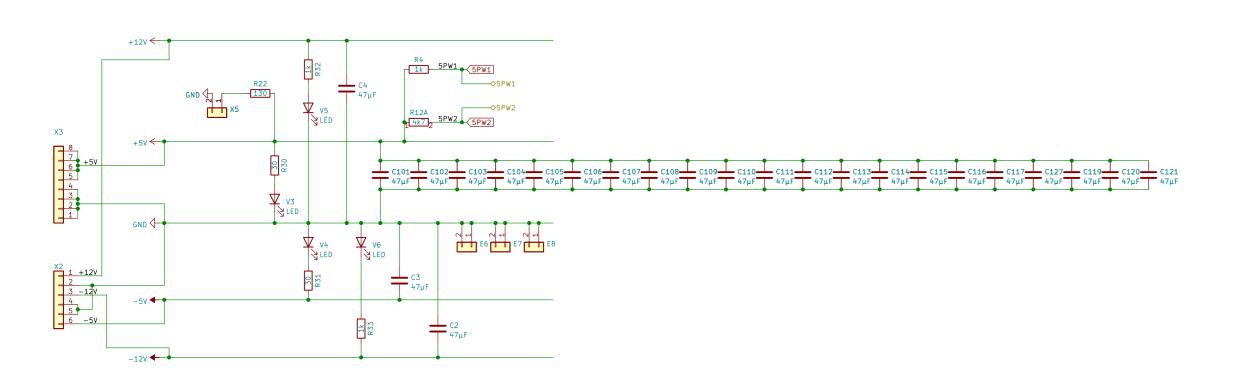
EC1834 Hauptplatine 201.2

Title: EC1834 Hauptplatine 201.2

Mario Gögel 2022
Sheet: /EC1834 201.2/
File: EC1834_201.2.kicad_sch

Size: A2   Date: 2022-09-12   Rev: 1
KiCad E.D.A. kicad (6.0.7)   Id: 3/6

MD[0..15]

{MEM[0..7] WE RAS0 RAS1 CASL CASH}

{MEMR MEMR MDPOE MDPEE}

This page is a KiCad electronic schematic diagram consisting primarily of component arrays (decoupling capacitors, resistors, diodes, and ICs) and a power supply section. The readable text content is limited to the title block and scattered component labels.

D[0..15]
{A[0..19] VA0 BHE}

X1.2 BUS_EC1834_16Bit
X1.4 BUS_EC1834_16Bit
X1.6 BUS_EC1834_16Bit
X1.8 BUS_EC1834_16Bit
X1.1 BUS_EC1834_16Bit
X1.3 BUS_EC1834_16Bit
X1.5 BUS_EC1834_16Bit
X1.7 BUS_EC1834_16Bit