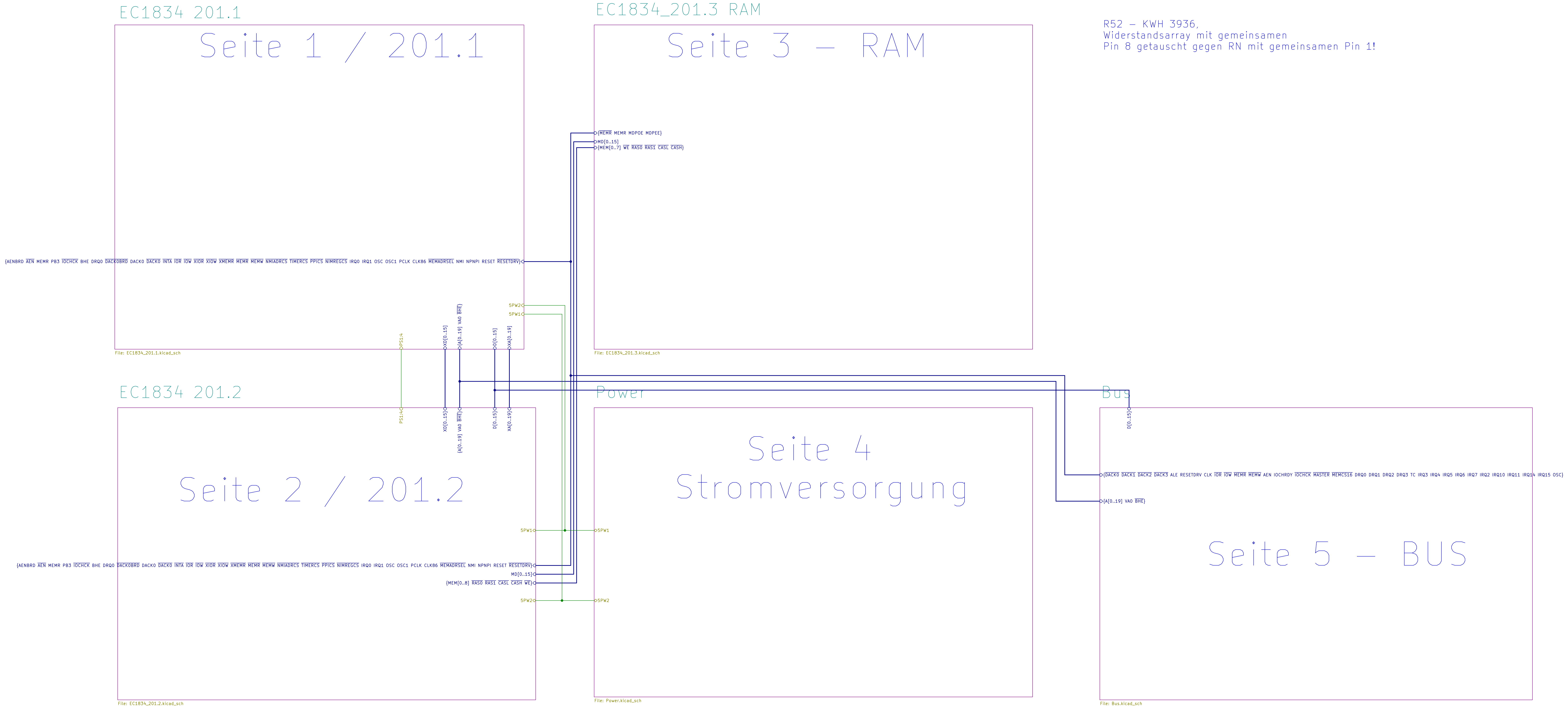
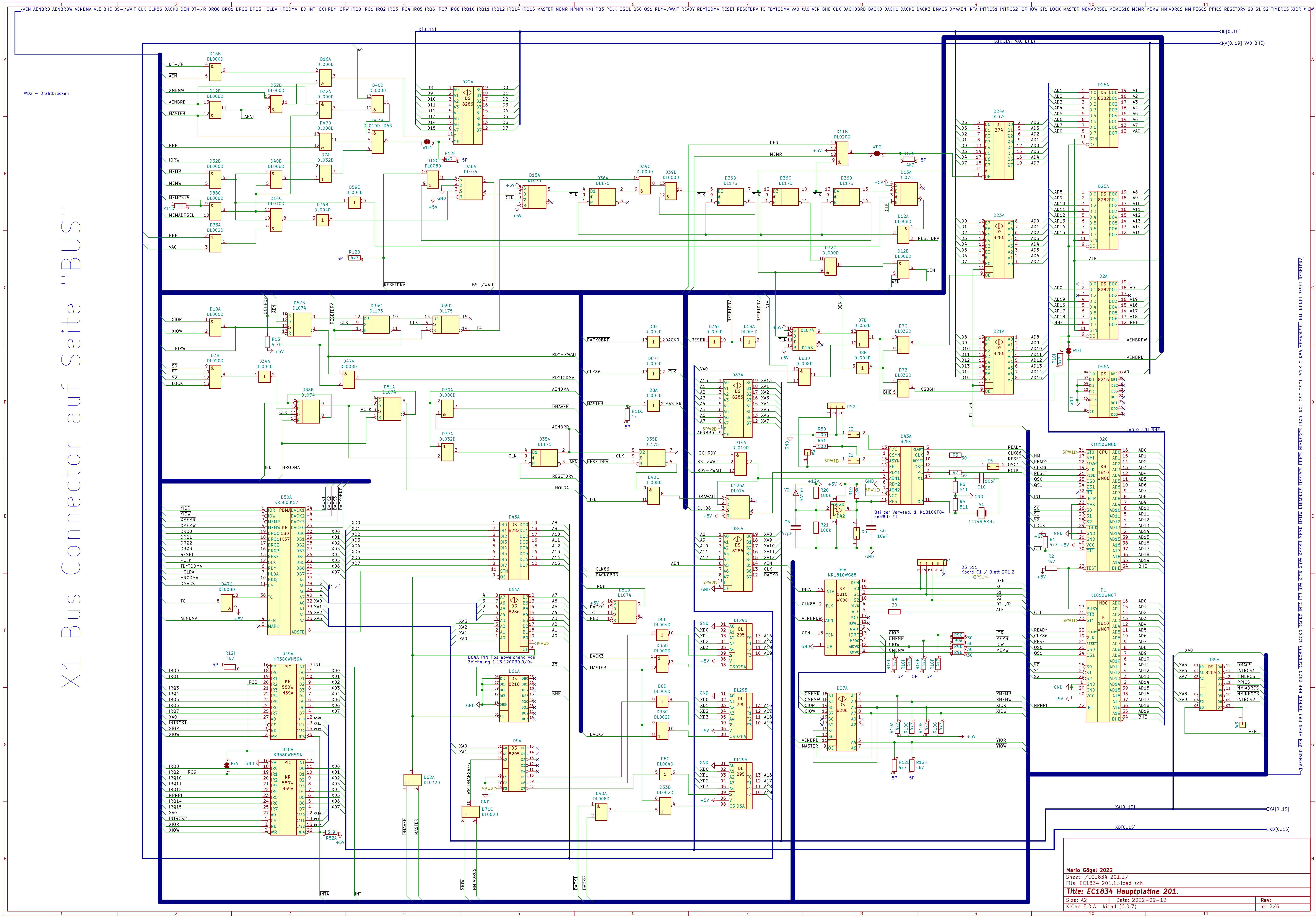

EC1834 201.1
Seite 1 / 201.1
{AENBRD AEN MEMR PB3 IOCHCK BHE DRQ0 DACK0BRD DACK0 DACK0 INTA IOR IOW XIOR XIOW XMEMR MEMR MEMW NMIADRCS TIMERCS PPICS NIMREGCS IRQ0 IRQ1 OSC OSC1 PCLK CLK86 MEMADRSEL NMI NPNPI RESET RESETDRV}
5PW2
5PW1
PS1:4
XD[0..15]
{A[0..19] VA0 BHE}
D[0..15]
XA[0..19]
File: EC1834_201.1.kicad_sch
EC1834_201.3 RAM
Seite 3 – RAM
{MEMR MEMR MDPOE MDPEE}
MD[0..15]
{MEM[0..7] WE RAS0 RAS1 CASL CASH}
File: EC1834_201.3.kicad_sch
EC1834 201.2
PS1:4
XD[0..15]
{A[0..19] VA0 BHE}
D[0..15]
XA[0..19]
Seite 2 / 201.2
5PW1
{AENBRD AEN MEMR PB3 IOCHCK BHE DRQ0 DACK0BRD DACK0 DACK0 INTA IOR IOW XIOR XIOW XMEMR MEMR MEMW NMIADRCS TIMERCS PPICS NIMREGCS IRQ0 IRQ1 OSC OSC1 PCLK CLK86 MEMADRSEL NMI NPNPI RESET RESETDRV}
MD[0..15]
{MEM[0..8] RAS0 RAS1 CASL CASH WE}
5PW2
File: EC1834_201.2.kicad_sch
Power
Seite 4
Stromversorgung
5PW1
5PW2
File: Power.kicad_sch
Bus
D[0..15]
{DACK0 DACK1 DACK2 DACK3 ALE RESETDRV CLK IOR IOW MEMR MEMW AEN IOCHRDY IOCHCK MASTER MEMCS16 DRQ0 DRQ1 DRQ2 DRQ3 TC IRQ3 IRQ4 IRQ5 IRQ6 IRQ7 IRQ2 IRQ10 IRQ11 IRQ14 IRQ15 OSC}
{A[0..19] VA0 BHE}
Seite 5 – BUS
File: Bus.kicad_sch

R52 – KWH 3936,
Widerstandsarray mit gemeinsamen
Pin 8 getauscht gegen RN mit gemeinsamen Pin 1!

Mario Gögel 2022
Sheet: /
File: EC1834–Board.kicad_sch
Title: EC1834 Hauptplatine
Size: A1 Date: 2022–09–12 **Rev: 1**
KiCad E.D.A. kicad (6.0.7) Id: 1/6

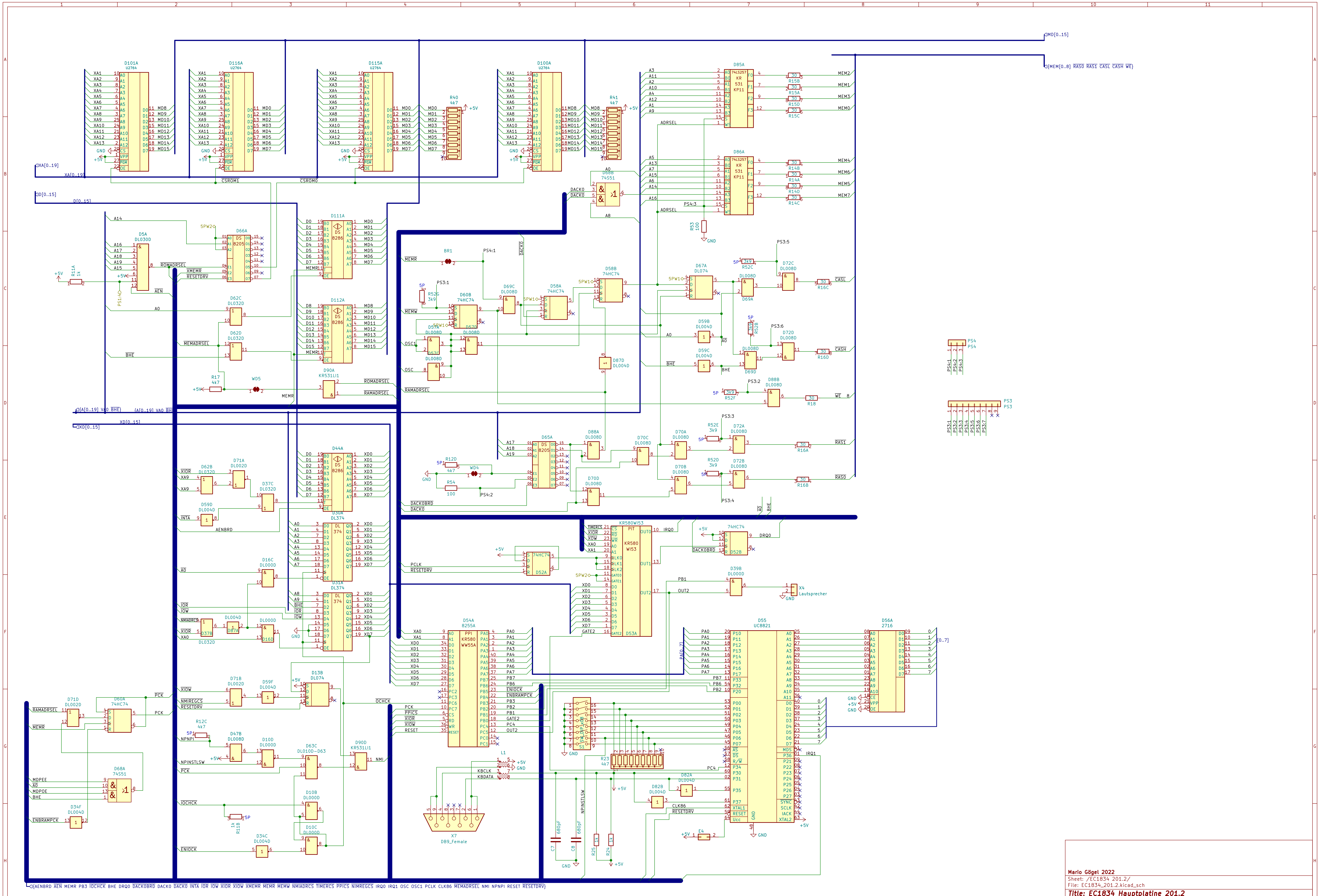

X1 Bus Connector auf Seite "BUS"
WDx - Drahtbrücken
D[0..15]
A[0..19] VAO BHE
AD[0..19] BHE
XA[0..19]
XD[0..15]
D20 K1810WM86
D1 K1810WM87
D43A 8284
D4A K1810WG88
D49A KR580WN59A
D48A KR580WN59A
D50A KR580IK57
D22A DS 8286
D23A DS 8286
D21A DS 8286
D24A DL 374
D26A DS 8282
D25A DS 8282
D2A DS 8282
D45A DS 8282
D64A DS 8286
D83A DS 8286
D84A DS 8286
D27A DS 8286
D89A DS 8205
D9A DS 8205
D61A DS 8216
D46A DS 8216
DL295
Bei der Verwend. d. K1810GF84 entfällt E1
D64A PIN Pos abweichend von Zeichnung 1.13.120030.0/04
D5 p11 Koord C1 / Blatt 201.2
14745,6kHz

Mario Gögel 2022

Sheet: /EC1834 201.1/
File: EC1834_201.1.kicad_sch

Title: EC1834 Hauptplatine 201.

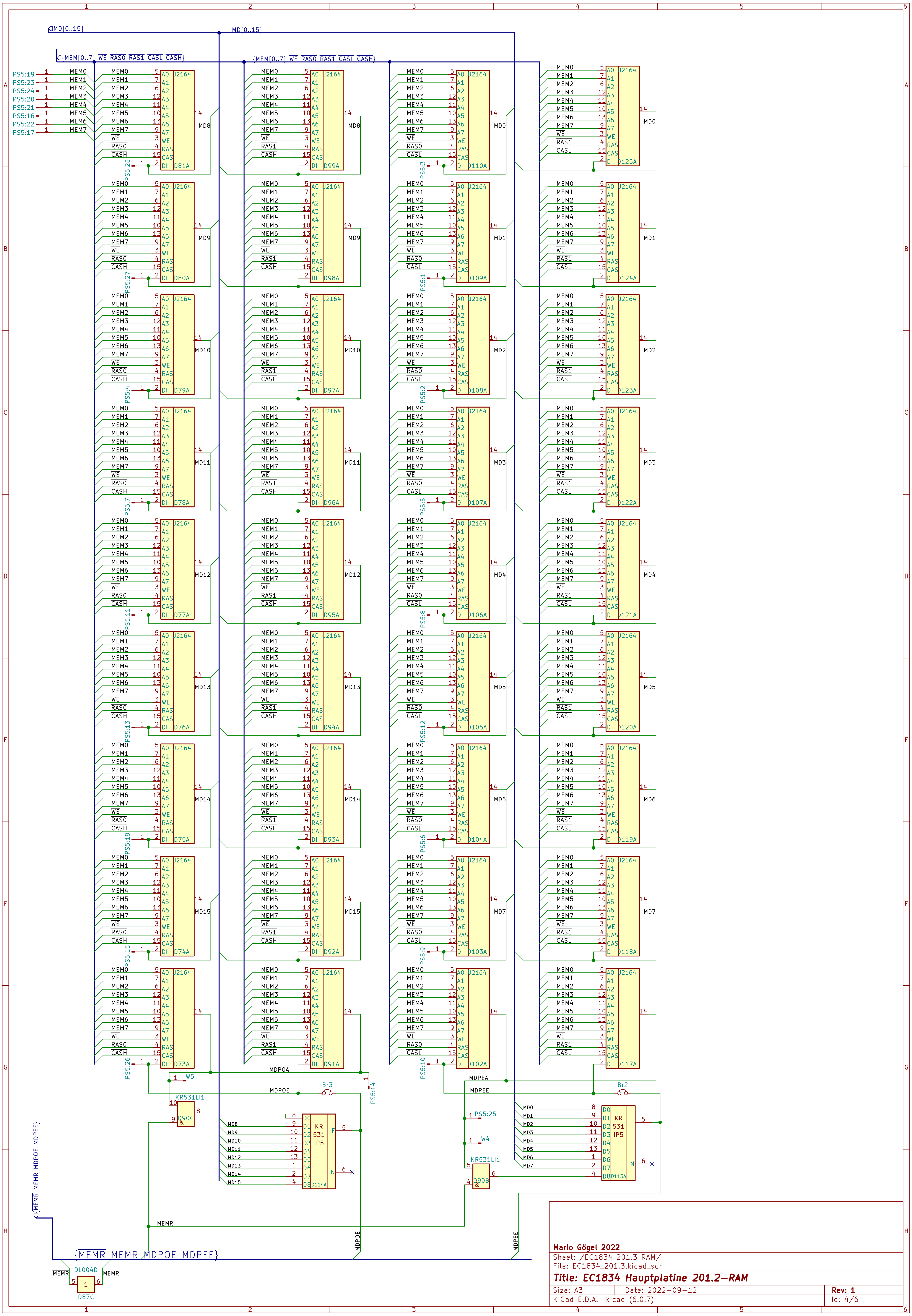
Size: A2 | Date: 2022-09-12 | **Rev:**
KiCad E.D.A. kicad (6.0.7) | Id: 2/6


D101A U2764
D116A U2764
D115A U2764
D100A U2764
R40 4k7
R41 4k7
D85A 74LS257 KR 531 KP11
D86A 74LS257 KR 531 KP11
MD[0..15]
MEM[0..8] RAS0 RAS1 CASL CASH WE
XA[0..19]
D[0..15]
CSROM1
CSROM0
D66A DS 8205
D5A DL030D
D111A DS 8286
D112A DS 8286
D90A KR531LI1
D44A DS 8286
D30A DL374
D31A DL374
D60B 74HC74
D58A 74HC74
D58B 74HC74
D65A DS 8205
D68B 74S51
D54A 8255A KR580 WW55A
D53A KR580WI53
D52A 74HC74
D52B 74HC74
D55 UC8821
D56A 2716
D13B DL074
D60A 74HC74
D68A 74S51
X7 DB9_Female
X4 Lautsprecher
PS3
PS4
A[0..19] VA0 BHE
XD[0..15]
AENBRD AEN MEMR PB3 IOCHCK BHE DRQ0 DACK0BRD DACK0 DACK0 INTA IOR IOW XIOR XIOW XMEMR MEMR MEMW NMIADRCS TIMERCS PPICS NMIREGCS IRQ0 IRQ1 OSC OSC1 PCLK CLK86 MEMADRSEL NMI NPNPI RESET RESETDRV

Mario Gögel 2022
Sheet: /EC1834 201.2/
File: EC1834_201.2.kicad_sch

Title: EC1834 Hauptplatine 201.2

Size: A2 Date: 2022-09-12 Rev: 1
KiCad E.D.A. kicad (6.0.7) Id: 3/6

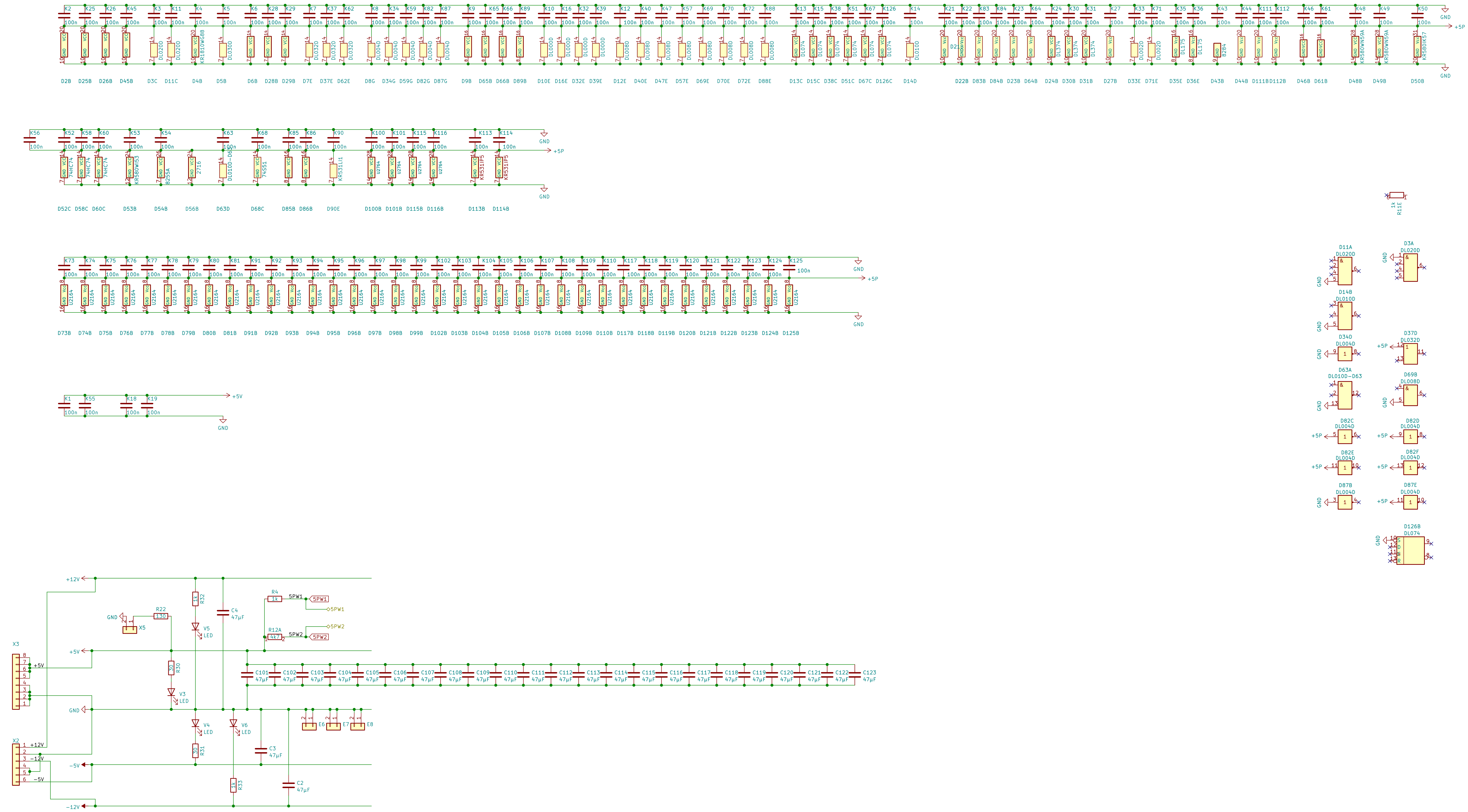

MD[0..15]
{MEM[0..7] WE RAS0 RAS1 CASL CASH}
PS5:19
PS5:23
PS5:24
PS5:20
PS5:21
PS5:16
PS5:22
PS5:17
MEM0
MEM1
MEM2
MEM3
MEM4
MEM5
MEM6
MEM7
WE
RAS0
RAS1
CASH
CASL
J2164
D81A
D80A
D79A
D78A
D77A
D76A
D75A
D74A
D73A
D99A
D98A
D97A
D96A
D95A
D94A
D93A
D92A
D91A
D110A
D109A
D108A
D107A
D106A
D105A
D104A
D103A
D102A
D125A
D124A
D123A
D122A
D121A
D120A
D119A
D118A
D117A
MD0
MD1
MD2
MD3
MD4
MD5
MD6
MD7
MD8
MD9
MD10
MD11
MD12
MD13
MD14
MD15
MDPOA
MDPOE
MDPEA
MDPEE
W5
W4
Br3
Br2
PS5:14
PS5:25
KR531LI1
D90C
D90B
KR 531 IP5
D114A
D113A
MEMR
{MEMR MEMR MDPOE MDPEE}
DL004D
D87C

Mario Gögel 2022

Sheet: /EC1834_201.3 RAM/
File: EC1834_201.3.kicad_sch

Title: EC1834 Hauptplatine 201.2–RAM

Size: A3 Date: 2022–09–12 **Rev: 1**
KiCad E.D.A. kicad (6.0.7) Id: 4/6

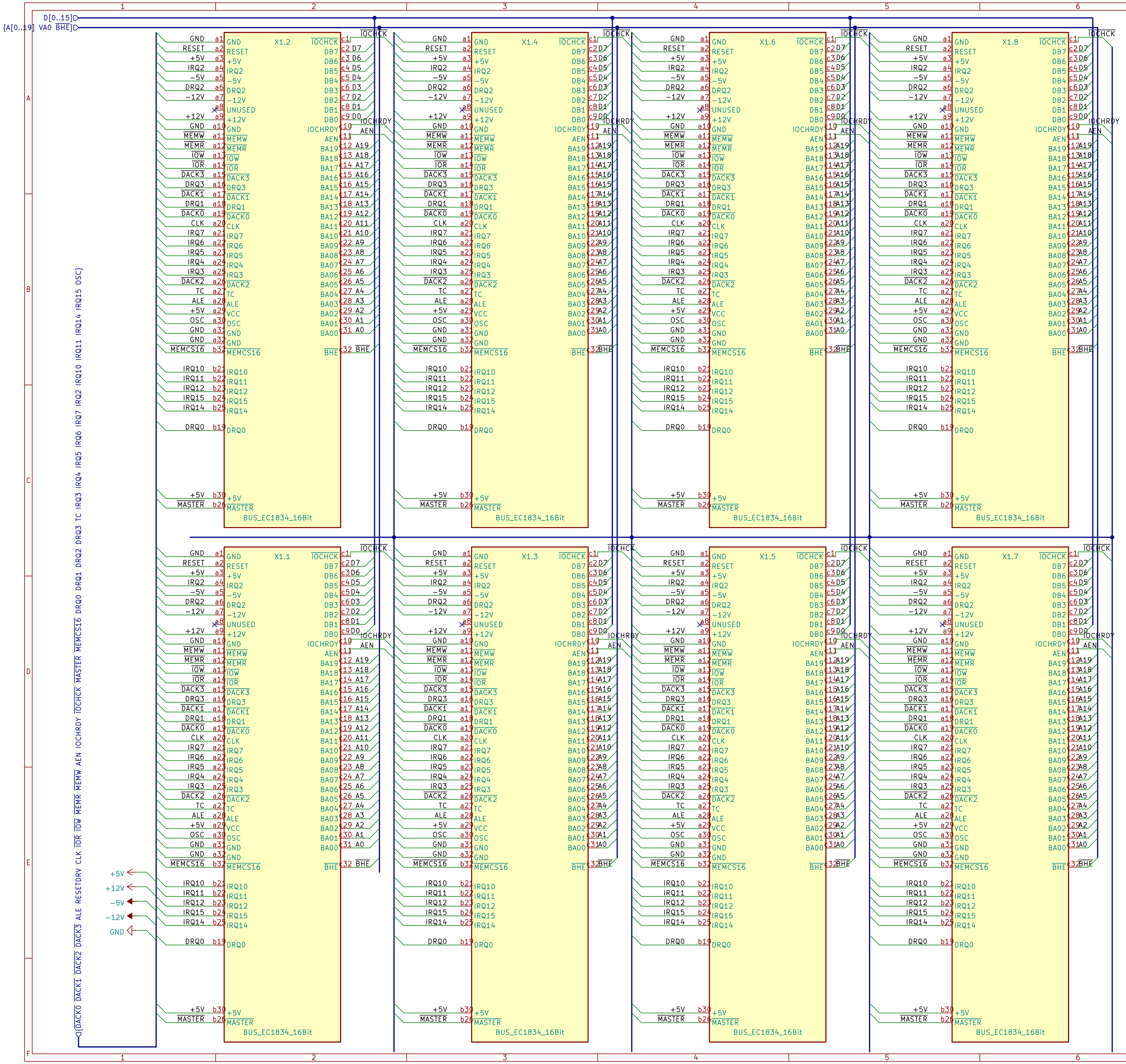

+5P
GND
+5V
+12V
-5V
-12V
X3
X2
X5
R22
130
R30
R31
R32
R33
R4
1k
R12A
4k7
5PW1
5PW2
V3
V4
V5
V6
LED
C2
C3
C4
47µF
E6
E7
E8
C101
C102
C103
C104
C105
C106
C107
C108
C109
C110
C111
C112
C113
C114
C115
C116
C117
C118
C119
C120
C121
C122
C123
K1
K55
K18
K19
100n
R11E
D11A
DL020D
D3A
D14B
DL010D
D34D
DL004D
D37D
DL032D
D63A
DL010D-D63
D69B
DL008D
D82C
D82D
D82E
D82F
D87B
D87E
D126B
DL074
U2164
74HC74
8255A
2716
74S51
U2764
KR531LI1
KR531IP5
KR580WI53
KR580WN59A
KR580IK57
KR1810WG88

Mario Gögel 2022

Sheet: /Power/
File: Power.kicad_sch

Title: EC1834 Hauptplatine 201.2—Power

Size: A2 | Date: 2022-09-12 | **Rev: 1**
KiCad E.D.A. kicad (6.0.7) | Id: 5/6


D[0..15]
{A[0..19] VA0 BHE}
{DACK0 DACK1 DACK2 DACK3 ALE RESETDRV CLK IOR IOW MEMR MEMW AEN IOCHRDY IOCHCK MASTER MEMCS16 DRQ0 DRQ1 DRQ2 DRQ3 TC IRQ3 IRQ4 IRQ5 IRQ6 IRQ7 IRQ2 IRQ10 IRQ11 IRQ14 IRQ15 OSC}
+5V
+12V
−5V
−12V
GND
X1.1
X1.2
X1.3
X1.4
X1.5
X1.6
X1.7
X1.8
BUS_EC1834_16Bit
GND
RESET
+5V
IRQ2
−5V
DRQ2
−12V
UNUSED
+12V
MEMW
MEMR
IOW
IOR
DACK3
DRQ3
DACK1
DRQ1
DACK0
CLK
IRQ7
IRQ6
IRQ5
IRQ4
IRQ3
DACK2
TC
ALE
VCC
OSC
MEMCS16
IRQ10
IRQ11
IRQ12
IRQ15
IRQ14
DRQ0
MASTER
IOCHCK
DB7
DB6
DB5
DB4
DB3
DB2
DB1
DB0
IOCHRDY
AEN
BA19
BA18
BA17
BA16
BA15
BA14
BA13
BA12
BA11
BA10
BA09
BA08
BA07
BA06
BA05
BA04
BA03
BA02
BA01
BA00
BHE
D7
D6
D5
D4
D3
D2
D1
D0
A19
A18
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0

Mario Gögel 2022
Sheet: /Bus/
File: Bus.kicad_sch

Title: EC1834 Hauptplatine 201.1-BUS

Size: A3 Date: 2022-09-12 **Rev: 1**
KiCad E.D.A. kicad (6.0.7) Id: 6/6