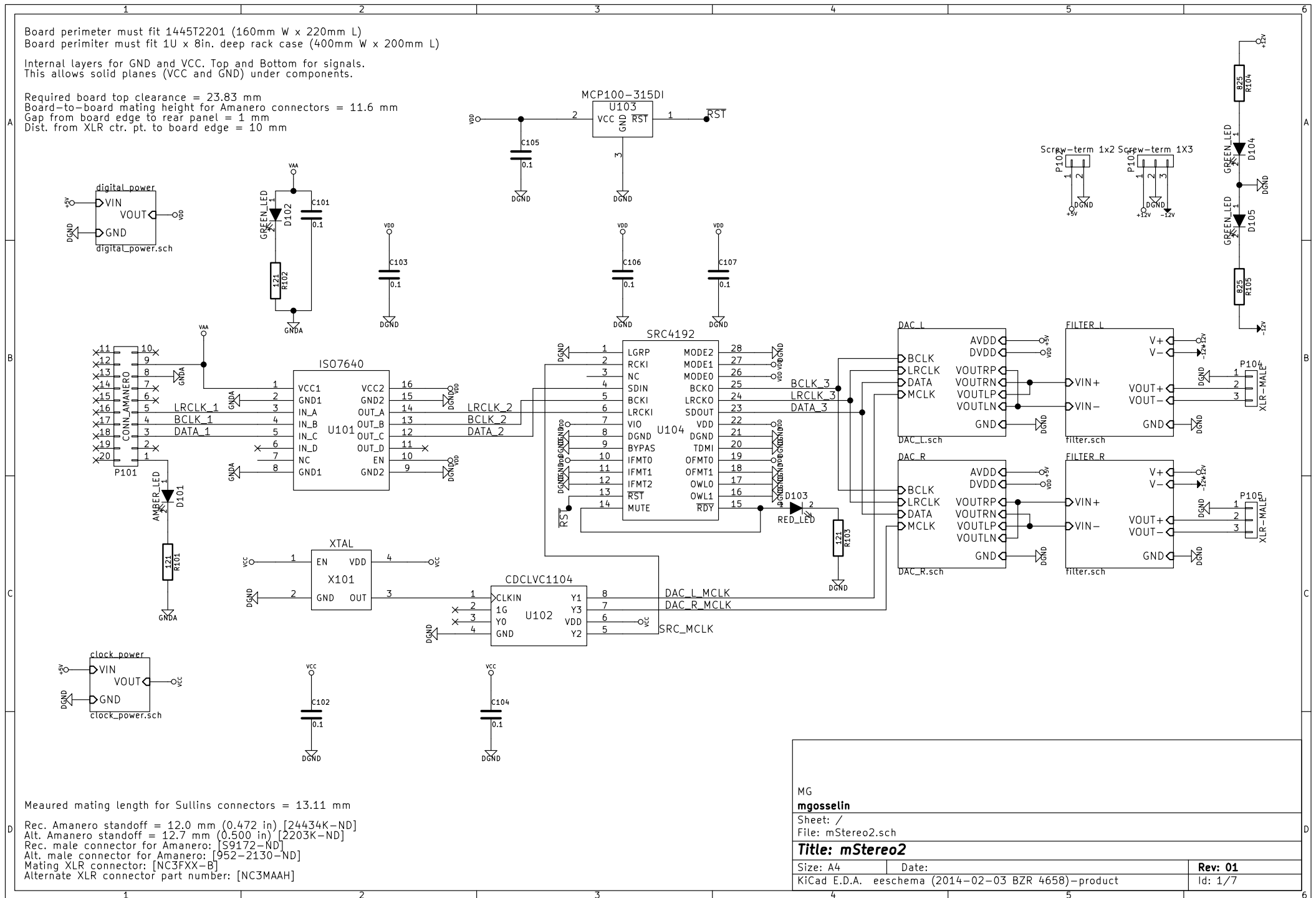
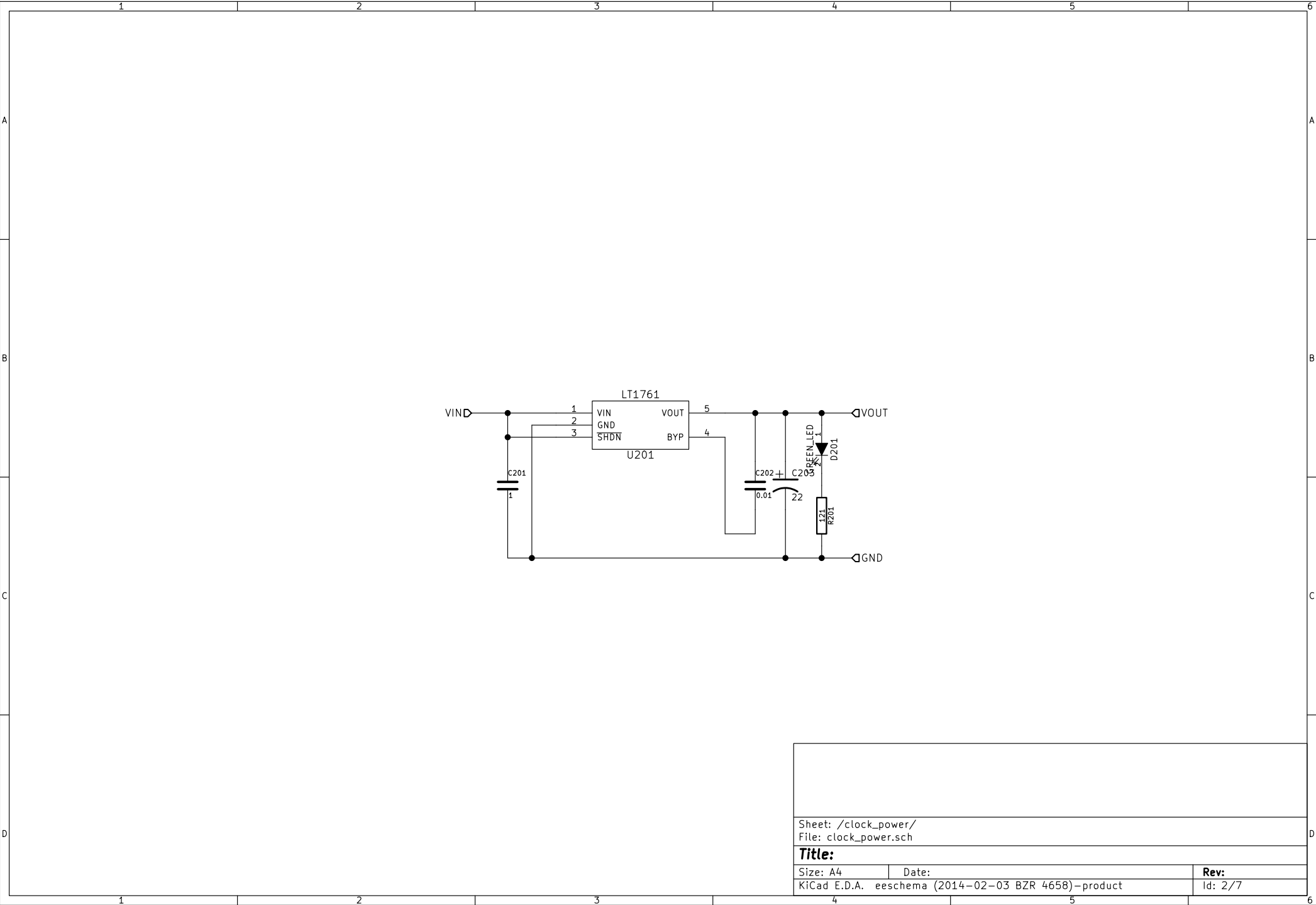


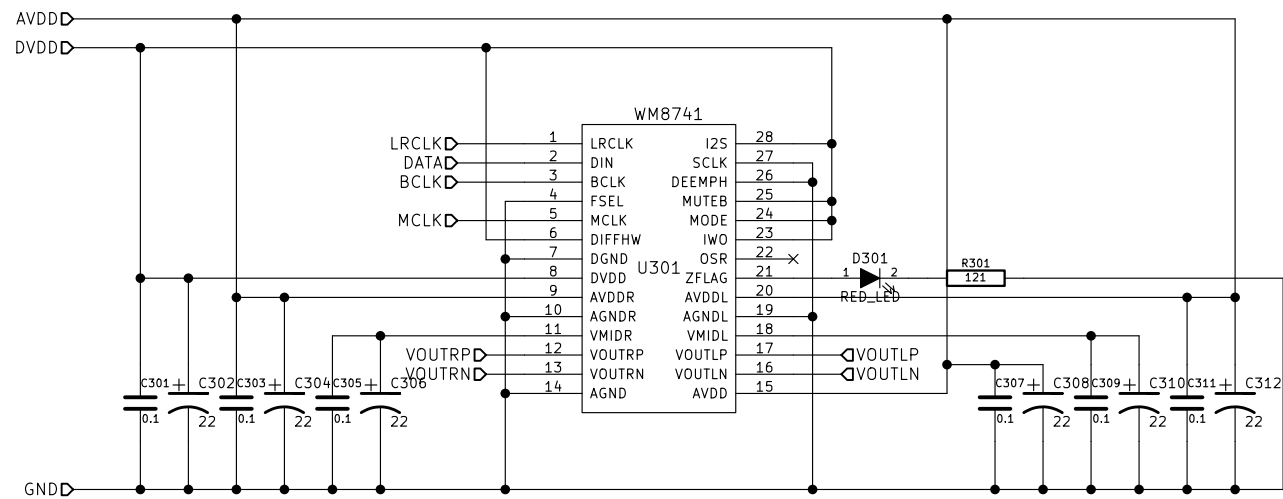
Board perimeter must fit 1445T2201 (160mm W x 220mm L)  
Board perimeter must fit 1U x 8in. deep rack case (400mm W x 200mm L)

Internal layers for GND and VCC. Top and Bottom for signals.  
This allows solid planes (VCC and GND) under components.

Required board top clearance = 23.83 mm  
Board-to-board mating height for Amanero connectors = 11.6 mm  
Gap from board edge to rear panel = 1 mm  
Dist. from XLR ctr. pt. to board edge = 10 mm







Sheet: /DAC\_R/  
File: DAC\_R.sch

**Title:**

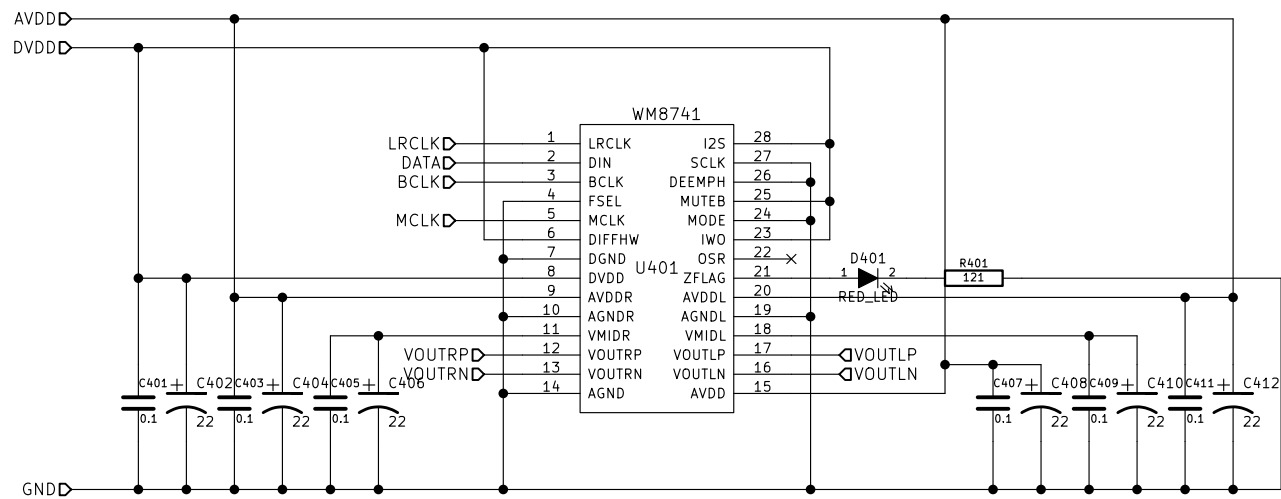
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Date:

KiCad E.D.A. eeschema (2014-02-03 BZR 4658)-product

Rev:

Id: 3/7



Sheet: /DAC\_L/  
File: DAC\_L.sch

**Title:**

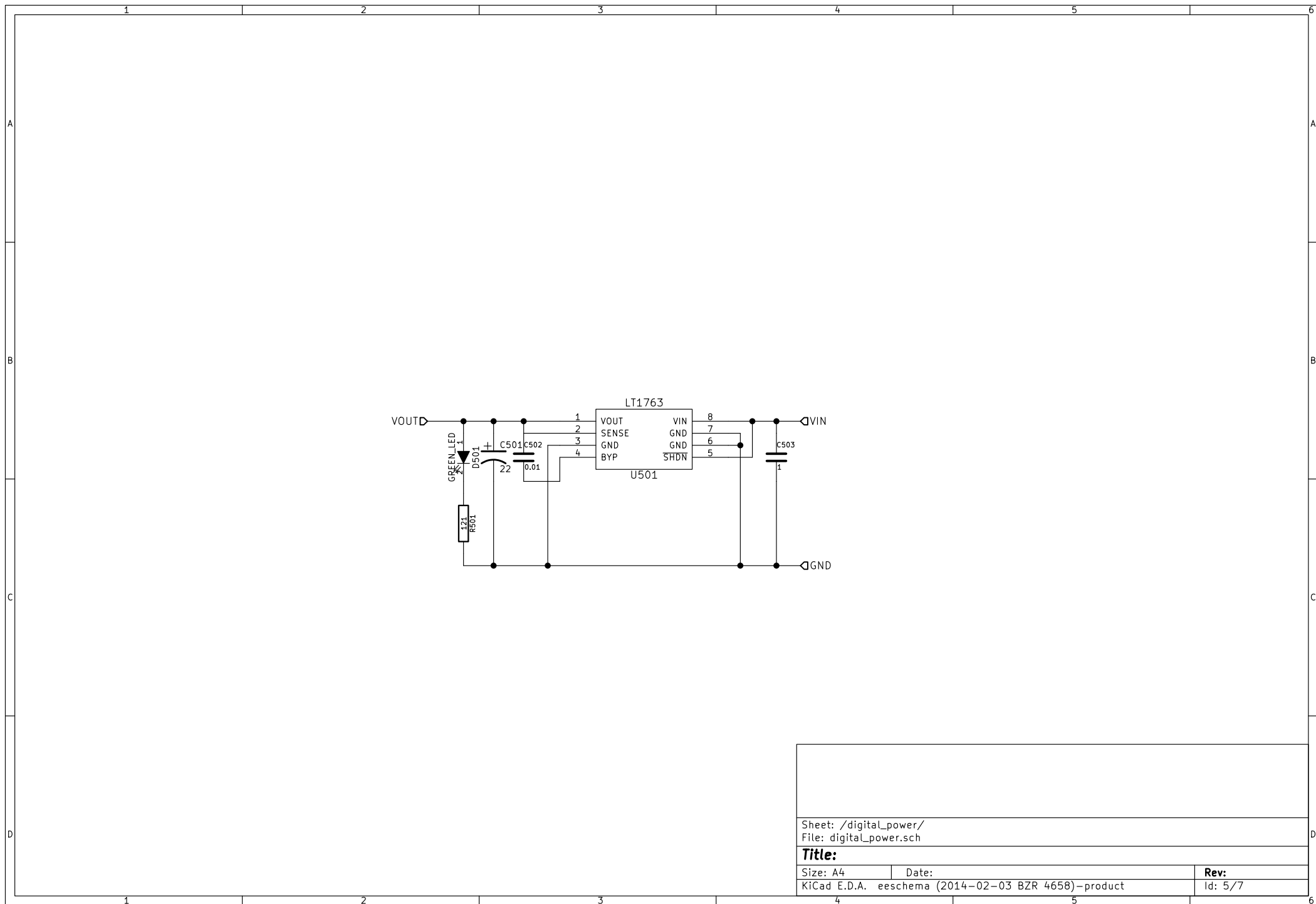
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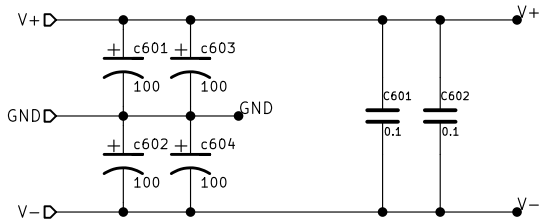
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KiCad E.D.A. eeschema (2014-02-03 BZR 4658)-product

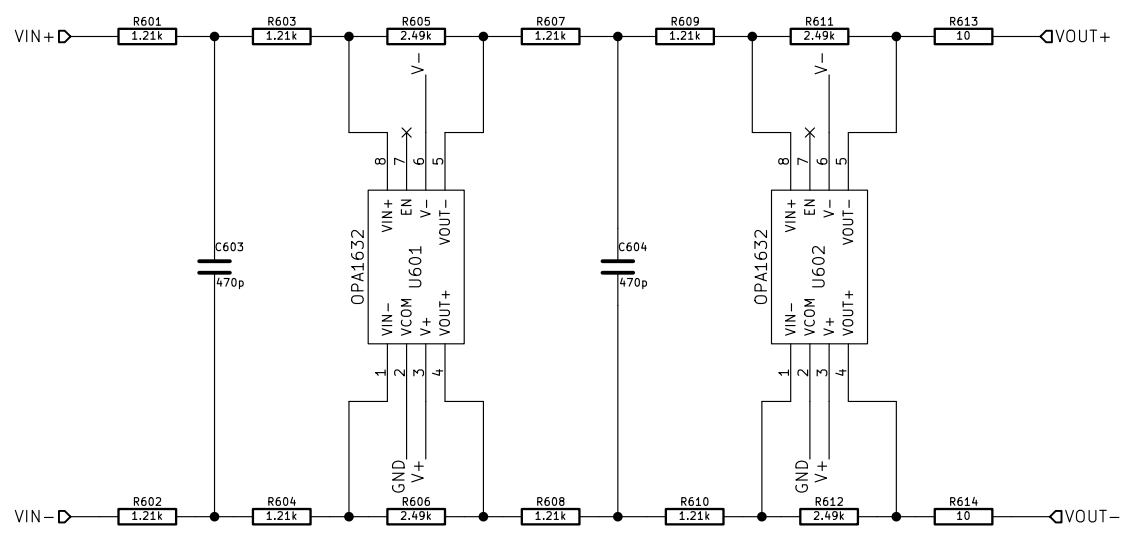
Rev:

Id: 4/7

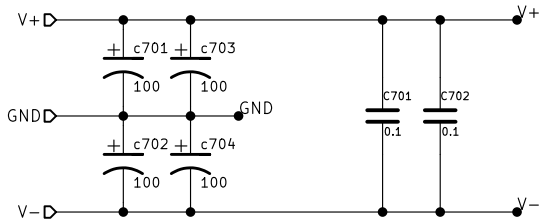




Gain =  $(1/2) \times 2 \times (1/2) \times 2 = 1$   
 $F_c = 1 / [2 \times \pi \times R \times C]$   
 $F_c = 1 / [2 \times \pi \times 1.21 \times 10^{-3} \times (2 \times 470) \times 10^{-12}] = 139 \text{ KHz}$



Sheet: /FILTER_L/ File: filter.sch		
<b>Title:</b>		
Size: A4	Date:	Rev:
KiCad E.D.A. eeschema (2014-02-03 BZR 4658)-product		Id: 6/7



Gain =  $(1/2) \times 2 \times (1/2) \times 2 = 1$   
Fc =  $1 / [2 \times \pi \times R \times C]$   
Fc =  $1 / [2 \times \pi \times 1.21 \times 10^{-3} \times (2 \times 470) \times 10^{-12}] = 139 \text{ KHz}$

