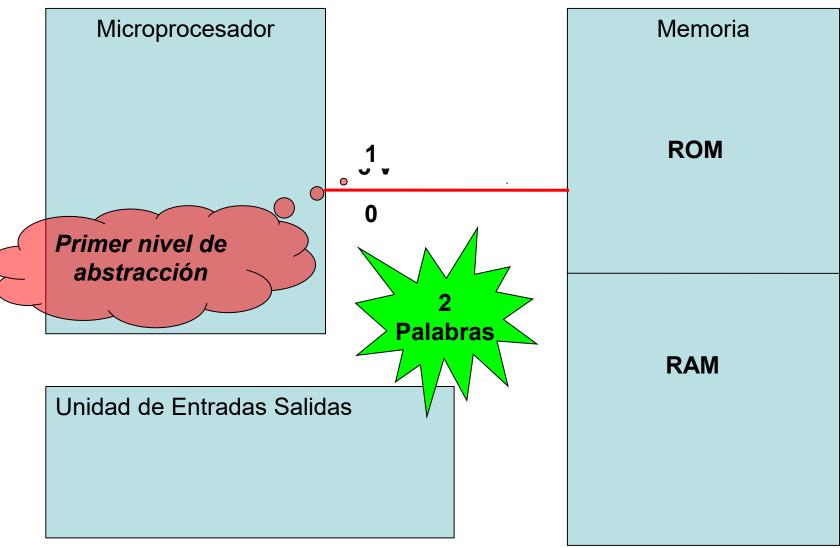
# Informática II

Análisis de un circuito Inteligente

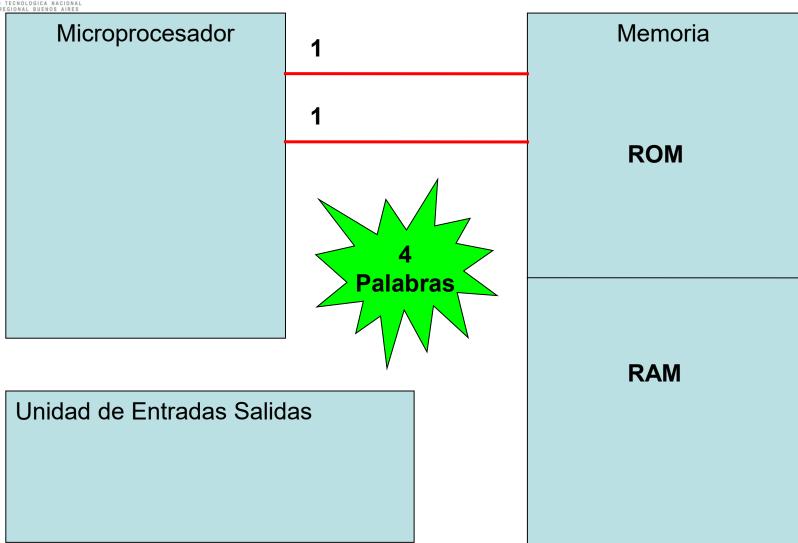


# **Diagrama Funcional**



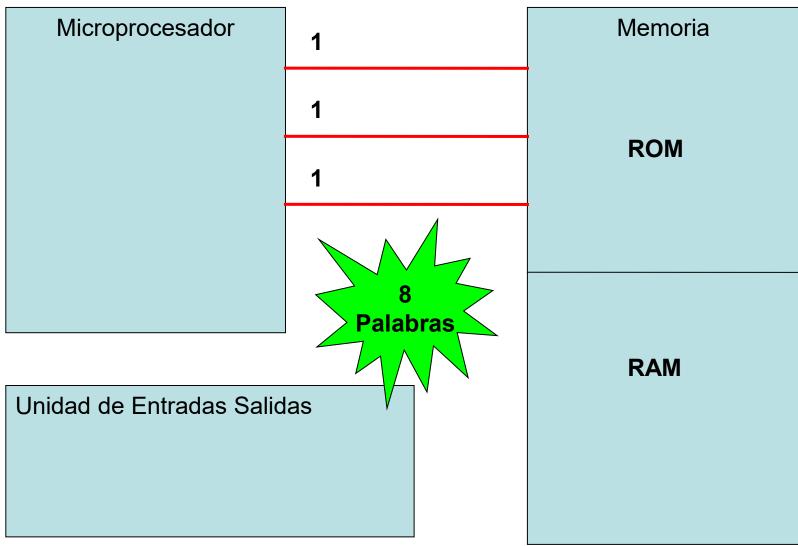


# **Dos cables para datos**



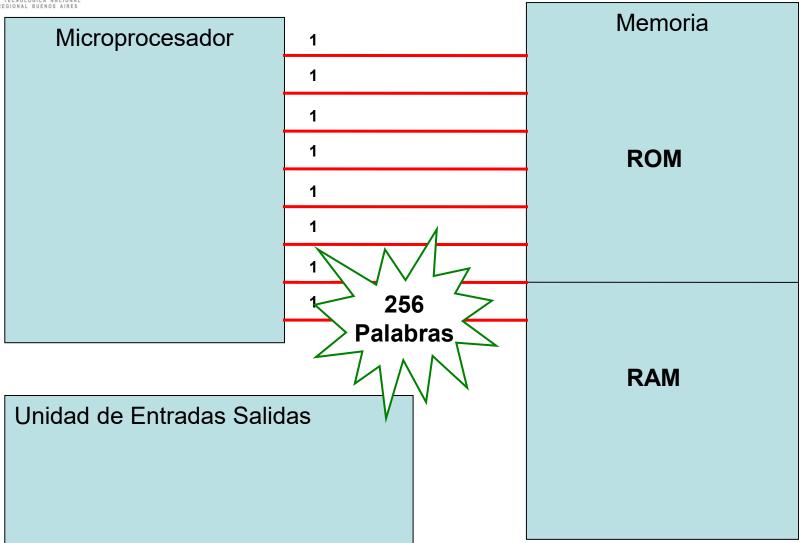


## Un cable mas .....





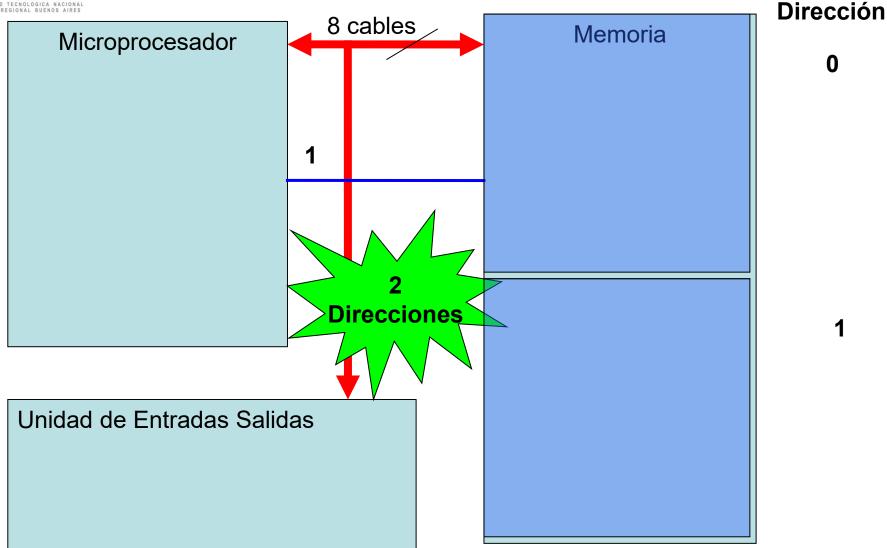
## **Una cantidad interesante...**



Adress bus

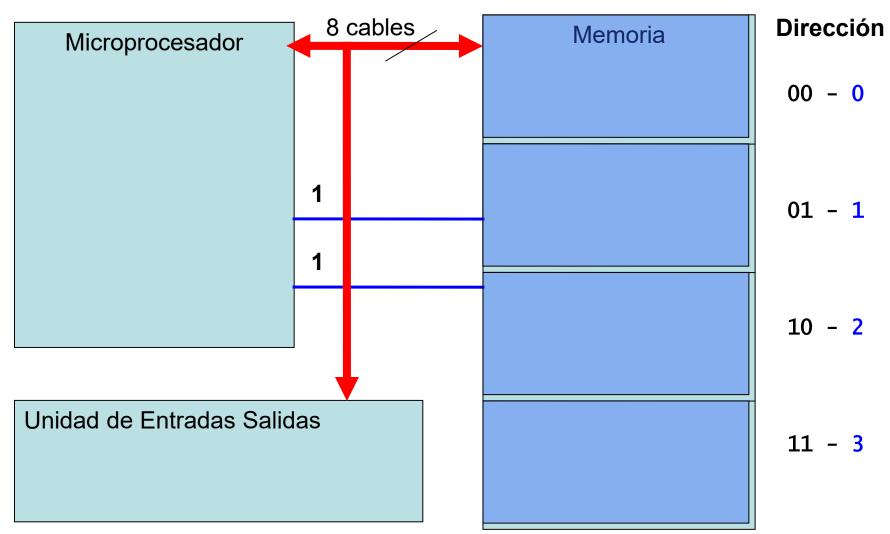


# ¿Como llego a los datos?



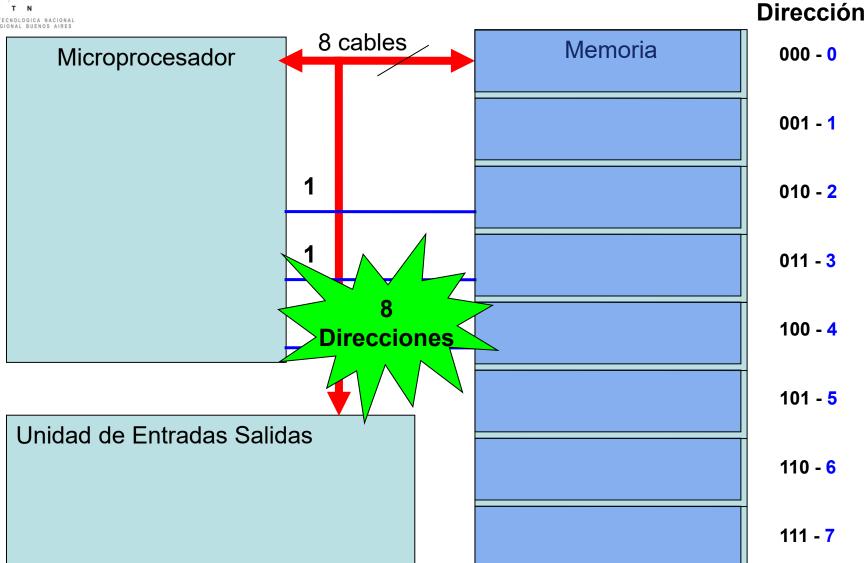


## Uno mas ...



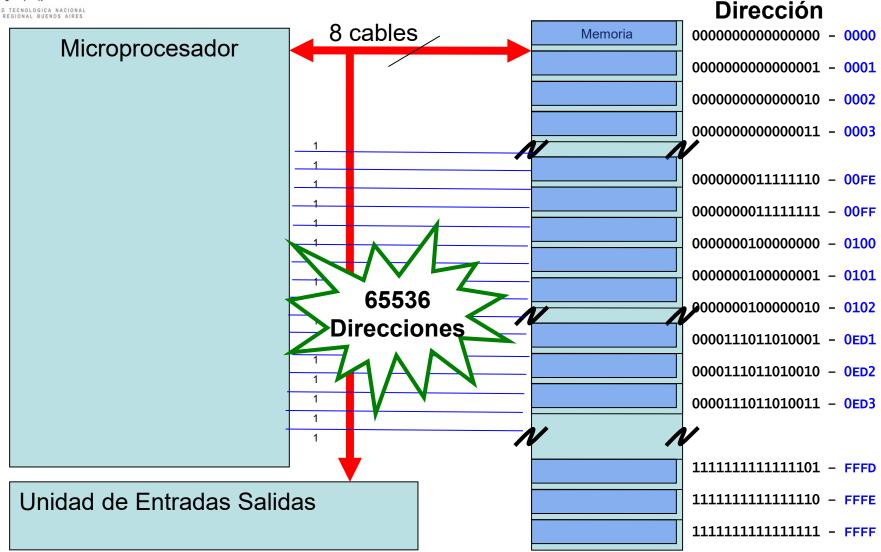


# ...y otro



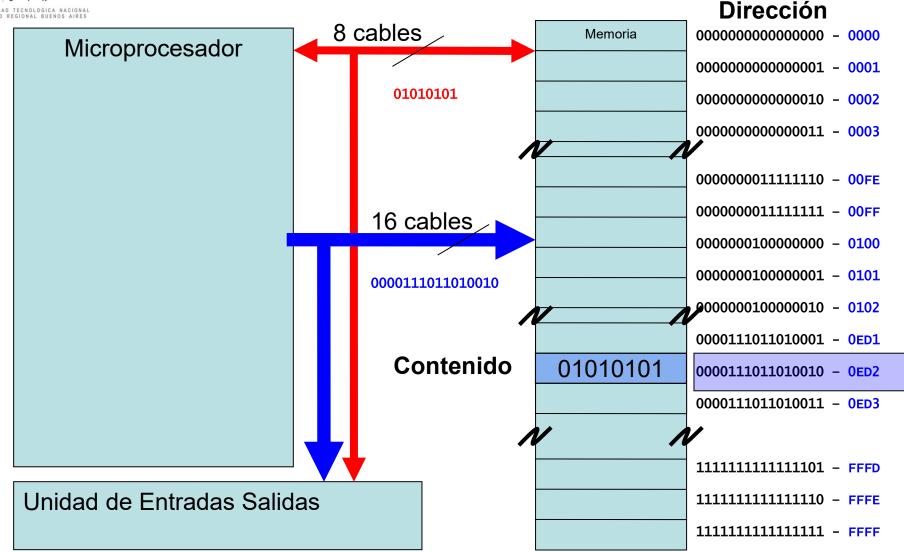


#### **Una cantidad interesante**





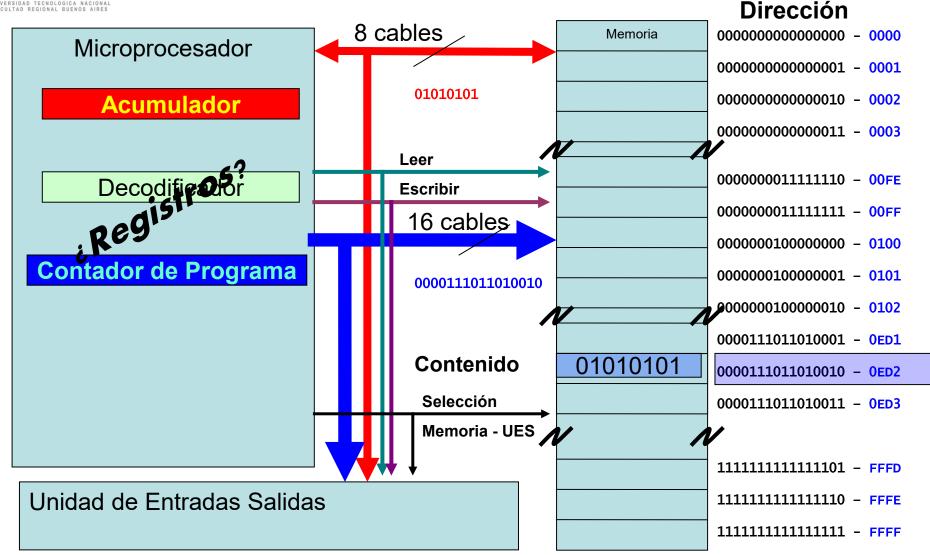
#### **Entonces** ....



Registros µP



### Un poco mas .....





## ¡Resumiendo!

