Synthetic and simplified view of the global class hierarchy **CPU** ∟ scheduler: Data Channels Scheduler is WorkloadSource mapped on bus pathes, i.e. _ getNextTransaction() sequences of BusMasters workloadList: – one for reading WorkloadSource list - one for writing getFirstMaster(trans.) belongs to endSchedule _getNextMaster(trans.) __timeSlice TMLTask is WorkloadSource currCommand: \(\schedule() TMLCommand ~ **BusMasters** ∟ getNextTransaction() task: accessGranted() **TMLTask** calls bus (list of) currTransaction: Bus **TMLTransaction** calls _schedule() rtime: runnable time _calcstartTimeLength() start time start: virtual length vlen: _calcLength() len: length - calcStartTimeLength updates cmd: command transaction's start and vlen chan: channel - calcLength updates pnties: penalties transaction's len _progress