Claim rejection under 35 USC 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –(a)(1) the claimed invention was patented, described in a printed publication, or in public use, on sale or otherwise available to the public before the effective filing date of the claimed invention.

Claims 1-19 are rejected under 35 U.S.C. 102(a)(1) as being anticipated by XXXXX et al (US )

Claim rejection under 35 USC 103

The following is a quotation of 35 U.S.C. 103 which forms the basis for all obviousness rejections set forth in this Office action:

A patent for a claimed invention may not be obtained, notwithstanding that the claimed invention is not identically disclosed as set forth in section 102 of this titleif the differences between the claimed invention and the prior art are such that the claimed invention as a whole would have been obvious before the effective filing date of the claimedinvention to a person having ordinary skill in the art to which the claimed invention pertains.Patentability shall not be negated by the manner in which the invention was made.

Claims 1-11 are rejected under 35 U.S.C. 103 as being unpatentable over XXXXXXX (US 20160142003) in view of XXXXXXX. (US ).

**Regarding claim 1**. A motor control device controlling a multiphase electric motor, the motor control device comprising:  
a controller configured to output a pulse width modulation control signal for controlling the multiphase electric motor; a driving signal generator configured to generate a switching element driving signal for driving the multiphase electric motor, according to the pulse width modulation control signal output from the controller; and a bridge circuit configured of a plurality of semiconductor switching elements configured to switch a current flowing through the multiphase electric motor, according to the switching element driving signal generated by the driving signal generator, wherein the controller is provided on a first board, wherein the driving signal generator and the bridge circuit are provided on a second board and are connected by wiring provided on the second board, wherein the first board and the second board are connected by a predetermined inter-board connecting line, and wherein the controller outputs the pulse width modulation control signal to the driving signal generator through the predetermined inter-board connecting line.   
   
**Regarding claim 2**. The motor control device according to claim 1,  
wherein the controller outputs a pulse width modulation control signal for each of the plurality of semiconductor switching elements, and wherein the predetermined inter-board connecting line has a signal line corresponding to each of the plurality of semiconductor switching elements.   
   
**Regarding claim 3**. The motor control device according to claim 1,  
wherein the pulse width modulation control signal output from the controller is a pulse width modulation duty value of each phase of the multiphase electric motor, and wherein the predetermined inter-board connecting line is a serial communication bus.   
   
**Regarding claim 4**. The motor control device according to claim 1 further comprising:  
a shunt resistor provided on a low potential side of the bridge circuit, the shunt resistor configured to measure a current flowing through each phase of the bridge circuit; and  
a current detector configured to detect a current according to a detection value detected by the shunt resistor,  
wherein the current detector includes a shunt resistor connecting terminal provided for each shunt resistor and connected to at least a high potential side of the shunt resistor, the current detector configured to output a current detection value corresponding to a voltage obtained from the shunt resistor connecting terminal to the predetermined inter-board connecting line, and  
wherein the controller receives the current detection value of each phase of the bridge circuit through the predetermined inter-board connecting line.