

Computer Organization and Architecture

Post-Lab

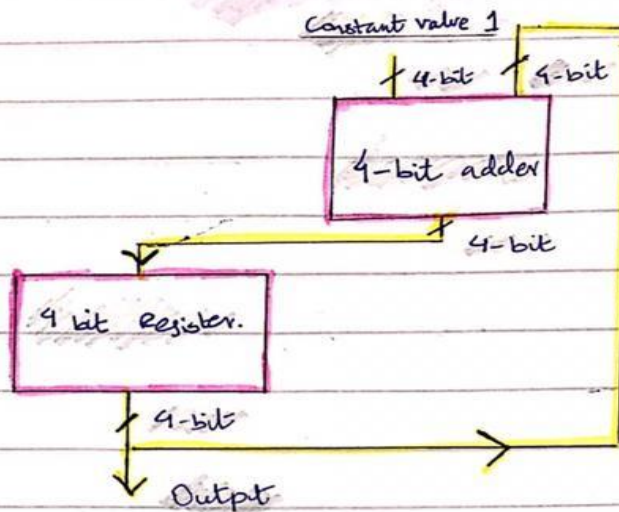
Lab 08



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Class	Computer Organization and Architecture CPE343(BCE-5B)
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Inlab: Task 1

Lab 07 - Post Lab.



Code:

```
module COUNTERfourBit (  
    input clk,  
    input rst,  
    output reg [3:0] out);
```

```
    always @ (posedge clk) begin
```

```
        if (!rst)
```

```
            out <= 0;
```

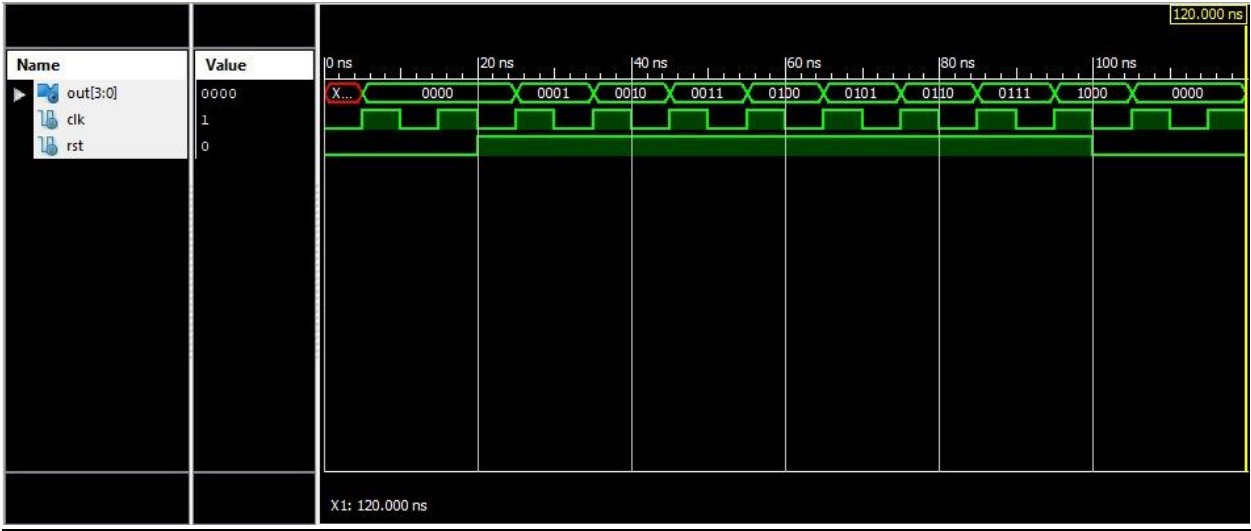
```
        else
```

```
            out <= out + 1;
```

```
    end
```

```
endmodule
```

Task1 Output Waveform:



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