

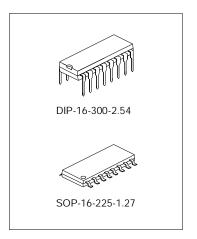
AM/FM RADIO RECEIVER

DESCRIPTION

The SA2003 is AM/FM radio IC (FM F/E+AM/FM IF) which is designed for AM/FM radios. Combining with the UTC7368 (Mono PW IC), a suitable AM/FM radio system is able to be constituted.

FEATURES

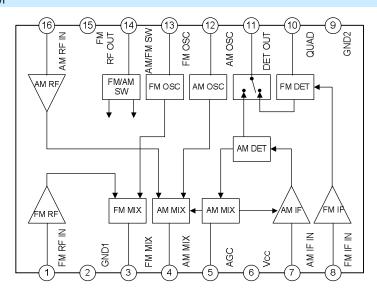
- * FM IFT, AM IFT and FM detector coil are not needed.
- * Pin compatible of TA8164P.
- * Operating supply voltage range: VCC(opr)=1.8~7V (Ta=25°C)



ORDERING INFORMATION

Device	Package
SA2003	DIP-16-300-2.54
SA2003S	SOP-16-225-1.27

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS (Tamb=25°C)

Characteris	tic	Symbol	Ratings	Unit	
Supply Voltage		Vcc	8	V	
B	DIP-16	55 (N 4)	750		
Power Dissipation	SOP-16	PD (Note)	350	mW	
Operating Temperature		Topr	-25~75	°C	
Storage Temperature		Tstg	-55~150	°C	

NOTE: Derated above Tamb=25°C in the proportion of 6mW/°C for SA2003 and of 2.8 mW/°C for SA2002S.

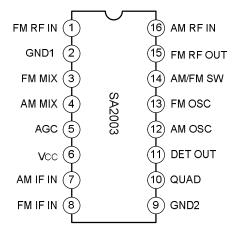
Rev: 1.3 2004.07.31



ELECTRICAL CHARACTERISTICS (Unless otherwise specified, $T_a=25^{\circ}C$, Vcc=3 V, F/E: f=98 MHz, fm=1 kHz; FM IF: f=10.7 MHz, $\Delta f=\pm22.5$ kHz, fm=1 kHz; AM: f=1MHz, MOD=30%, fm=1kHz)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
0	Supply Current		FM mode, Vin=0		10.5	16.5	4
Suppl			AM mode, Vin=0		5.0	8.0	mA
	Input Limiting Voltage	Vin(lim)	-3dB limiting point		12		dBμV EMF
F/E	Quiescent Sensitivity	QS	S/N=40dB		12		dBμV EMF
	Local OSC Voltage	Vosc	fosc =108 MHz	160	240	320	mVrms
	Local OSC Stop Voltage	Vstop(FM)	Vin=0		1.2		V
	Input Limiting Voltage	Vin(lim) IF	-3dB limiting point	42	47	52	dΒμV емғ
	Recovered Output Voltage	Vod	Vin = 80dBμV EMF	50	70	90	mVrms
FM IF	Signal to Noise Ratio	S/N	Vin = 80dBμV EMF		62		dB
	Total Harmonic Distortion	THD	Vin = 80dBμV EMF		0.4		%
	AM Rejection Ratio	AMR	Vin = 80dBμV EMF		33		dB
	Voltage Gain	GV	Vin = 27dBμV EMF	15	32	50	mVrms
	Recovered Output Voltage	Vod	Vin = 60dBμV EMF	35	60	85	mVrms
AM	Signal to Noise Ratio	S/N	Vin = 60dBμV EMF		43		dB
	Total Harmonic Distortion	THD	Vin = 60dBμV EMF		1.0		%
	Local OSC Stop Voltage	Vstop(AM)	Vin=0		1.6		V

PIN CONFIGURATION





PIN DESCRIPTION TERMINAL VOLTAGE: Typical DC voltage at Ta=25°C, VCC=3V and no signal with Test Circuit 1

Pin No.	Cymala al	Internal Circuit	Terminal \	/oltage (V)
PIN NO.	Symbol	Internal Circuit	AM	FM
1	FM RF IN	FM-RF OUT 15 15 Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q Q	0	0.7
2	GND1	-	0	0
3	FM MIX	AM/FM SW GND1	0.4	1.7
4	AM MIX	Vcc 6	0.6	0
5	AGC	IF AGC S RF AGC GND2 9	0	0
6	VCC	-	3.0	3.0
7	AM IF IN	Vcc 6 C C C C C C C C C C C C C C C C C C	3.0	3.0

(TO be continued)



(Continued)	1				
Din No	Cymala al	Internal Circuit	Terminal Voltage (V)		
Pin No.	Symbol	Internal Circuit	AM	FM	
8	FM IF IN	Vcc 6 8 8 GND2 9	3.0	3.0	
9	GND2	<u>-</u>	0	0	
10	QUAD	Vcc 6 10 GND2 9	2.5	2.2	
11	DET OUT	Vcc 6	1.4	1.1	
12	AM OSC	VCC 6 12 ALC GND12	3.0	3.0	
13	FM OSC	AM/FM SW 14 13 MIX GND1 2	0.9	3.0	

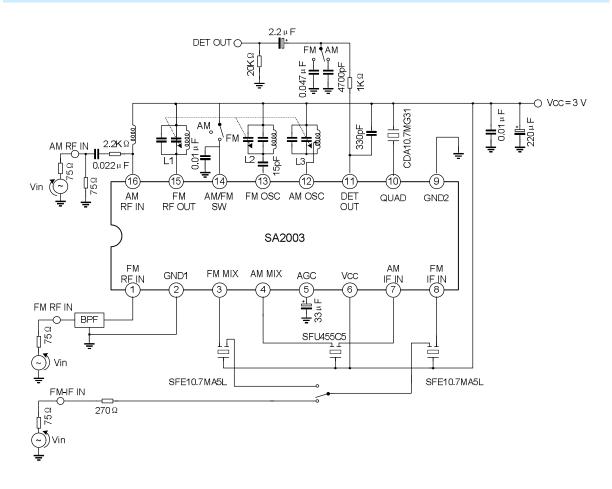
(TO be continued)



(Continued)

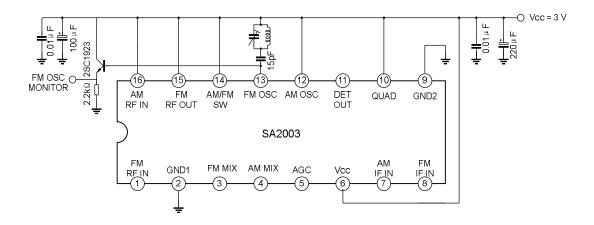
(Continued)	Console al	linta ma al Cinavit	Terminal Voltage (V)		
Pin No. Syn	Symbol	Internal Circuit	AM	FM	
14	AM/FM SW	Vcc 6	0.9	3.0	
15	FM RF OUT	Cf. PIN①	3.0	3.0	
16	AM RF IN	Vcc 6 16 4 GND1 2	3.0	3.0	

TEST CIRCUIT 1





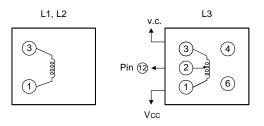
TEST CIRCUIT 2



COIL DATA

0 11 11	Test	L	Со				Turns				5.6
Coil No.	Freq.	(μH)	(pF)	Qo	1-2	2-3	1-3	1-4	4-6	Wire(mm)	Reference
L1 FM RF	100MHz			100				$2\frac{1}{4}$		0.5 UEW	(s)0258-000-
								4			021
L2 FM OSC	100MHz			100			$1\frac{3}{4}$			0.5 UEW	(s)0258-000-
							7				020
L3 AM OSC	796kHz	268		125	14	86				0.06 UEW	(s)2157-2239-
											213A

(s): Sumida electric co., Itd.





PACKAGE OUTLINE

