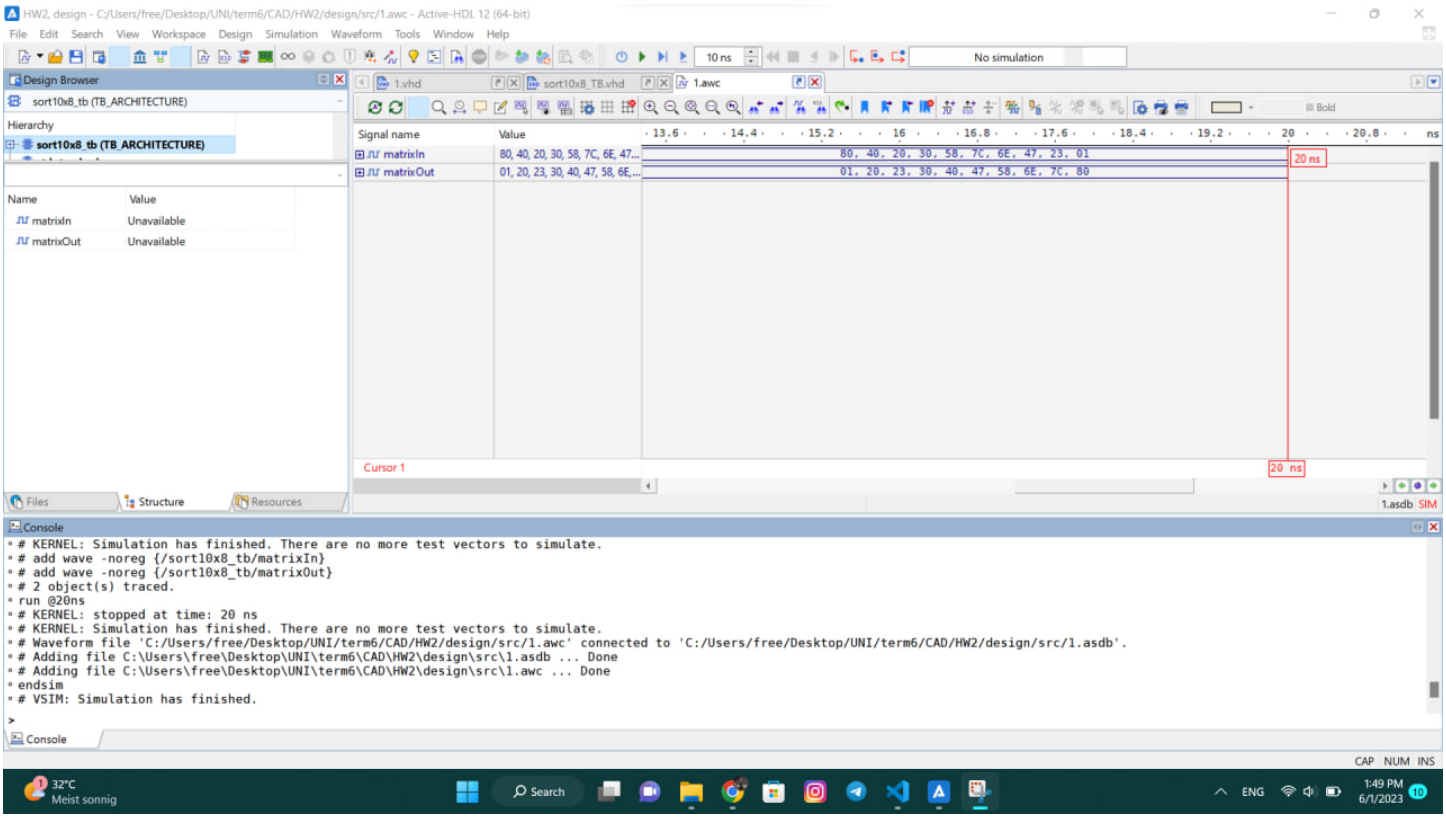
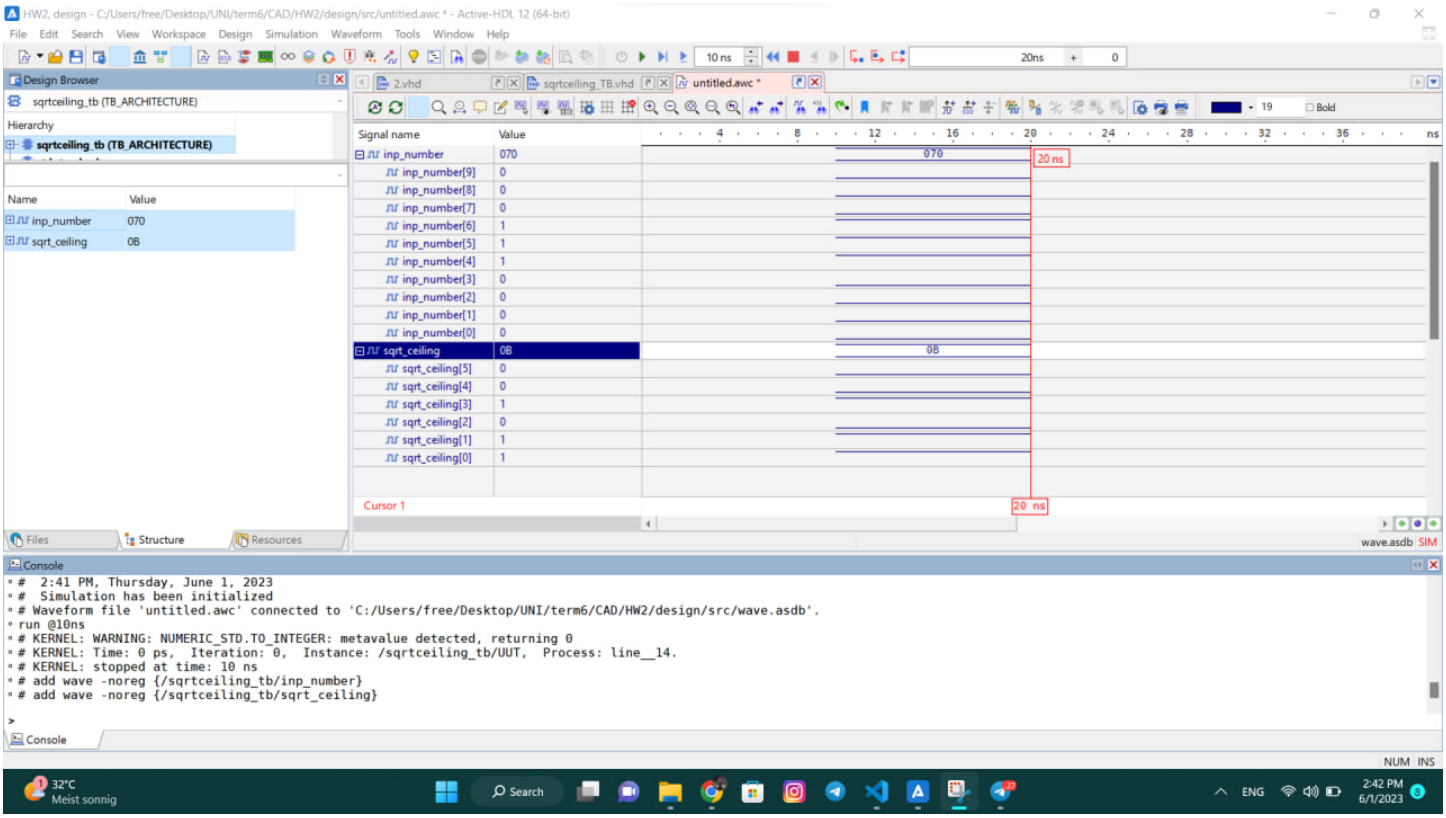
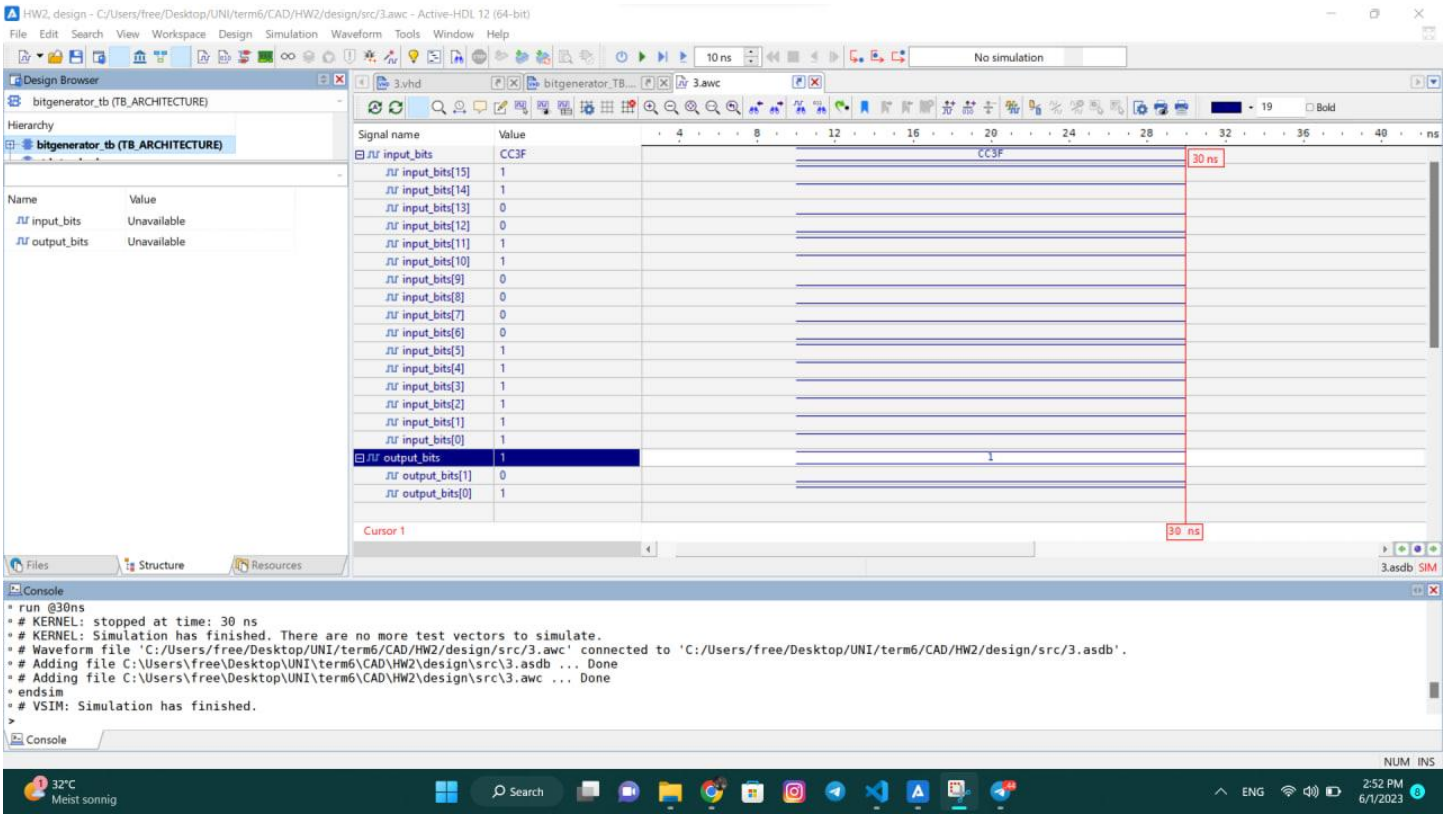
(Q1


HW2, design - C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/1.awc - Active-HDL 12 (64-bit)
Design Browser
sort10x8_tb (TB_ARCHITECTURE)
Hierarchy
sort10x8_tb (TB_ARCHITECTURE)
Name
Value
matrixIn
Unavailable
matrixOut
Unavailable
Signal name
Value
matrixIn
80, 40, 20, 30, 58, 7C, 6E, 47, 23, 01
matrixOut
01, 20, 23, 30, 40, 47, 58, 6E, 7C, 80
Cursor 1
20 ns
Console
# KERNEL: Simulation has finished. There are no more test vectors to simulate.
# add wave -noreg {/sort10x8_tb/matrixIn}
# add wave -noreg {/sort10x8_tb/matrixOut}
# 2 object(s) traced.
run @20ns
# KERNEL: stopped at time: 20 ns
# KERNEL: Simulation has finished. There are no more test vectors to simulate.
# Waveform file 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/1.awc' connected to 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/1.asdb'.
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\1.asdb ... Done
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\1.awc ... Done
endsim
# VSIM: Simulation has finished.

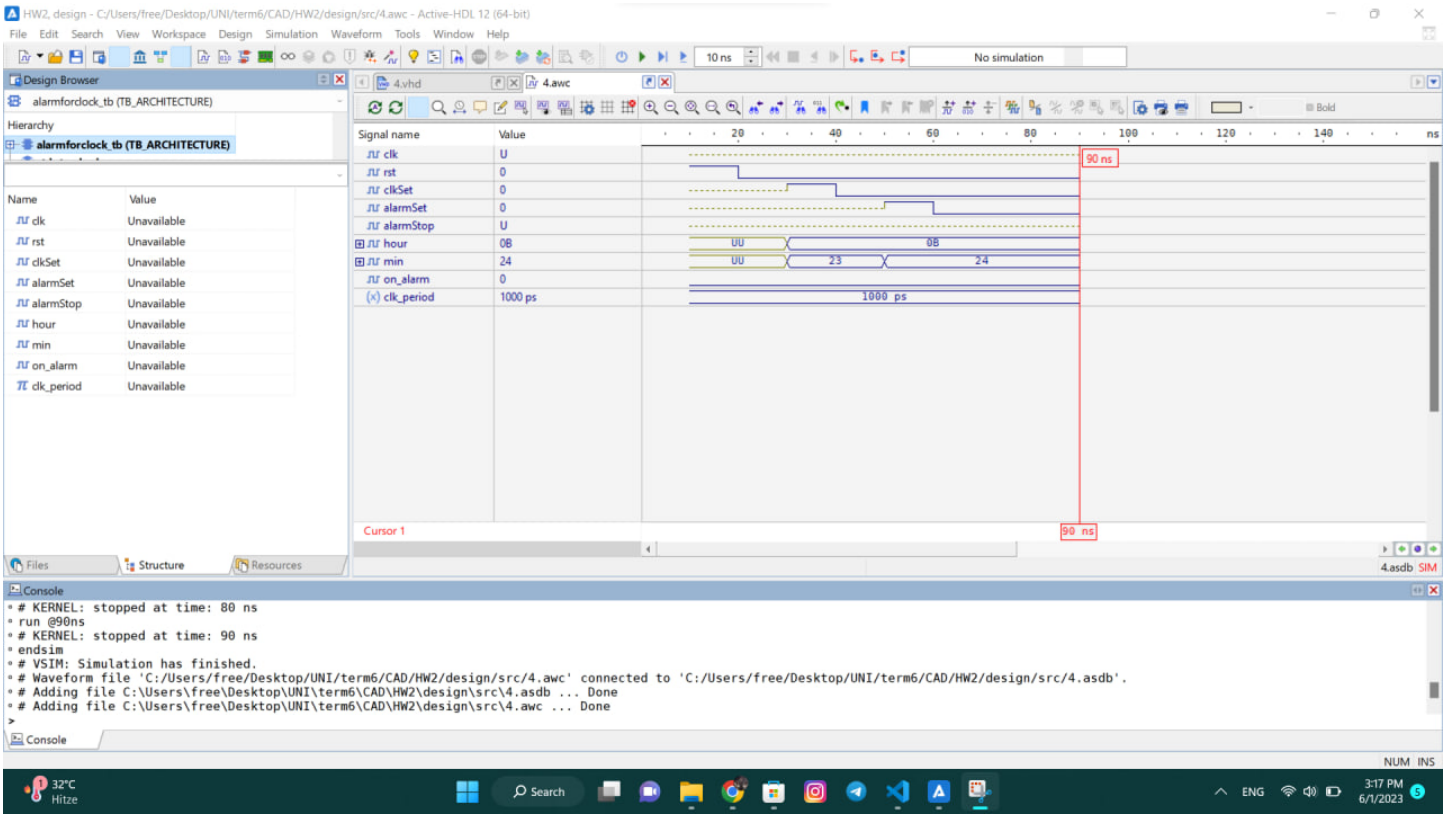
(Q2


HW2, design - C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/untitled.awc * - Active-HDL 12 (64-bit)
Design Browser
sqrtceiling_tb (TB_ARCHITECTURE)
Hierarchy
sqrtceiling_tb (TB_ARCHITECTURE)
Name
Value
inp_number
070
sqrt_ceiling
08
Signal name
Value
inp_number
070
inp_number[9]
0
inp_number[8]
0
inp_number[7]
0
inp_number[6]
1
inp_number[5]
1
inp_number[4]
1
inp_number[3]
0
inp_number[2]
0
inp_number[1]
0
inp_number[0]
0
sqrt_ceiling
08
sqrt_ceiling[5]
0
sqrt_ceiling[4]
0
sqrt_ceiling[3]
1
sqrt_ceiling[2]
0
sqrt_ceiling[1]
1
sqrt_ceiling[0]
1
Cursor 1
20 ns
Console
# 2:41 PM, Thursday, June 1, 2023
# Simulation has been initialized
# Waveform file 'untitled.awc' connected to 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/wave.asdb'.
run @10ns
# KERNEL: WARNING: NUMERIC_STD.TO_INTEGER: metavalue detected, returning 0
# KERNEL: Time: 0 ps, Iteration: 0, Instance: /sqrtceiling_tb/UUT, Process: line__14.
# KERNEL: stopped at time: 10 ns
# add wave -noreg {/sqrtceiling_tb/inp_number}
# add wave -noreg {/sqrtceiling_tb/sqrt_ceiling}

(Q3


HW2, design - C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/3.awc - Active-HDL 12 (64-bit)
Design Browser
bitgenerator_tb (TB_ARCHITECTURE)
Hierarchy
bitgenerator_tb (TB_ARCHITECTURE)
Name
Value
input_bits
Unavailable
output_bits
Unavailable
3.vhd
bitgenerator_TB....
3.awc
Signal name
Value
input_bits
CC3F
input_bits[15]
input_bits[14]
input_bits[13]
input_bits[12]
input_bits[11]
input_bits[10]
input_bits[9]
input_bits[8]
input_bits[7]
input_bits[6]
input_bits[5]
input_bits[4]
input_bits[3]
input_bits[2]
input_bits[1]
input_bits[0]
output_bits
output_bits[1]
output_bits[0]
30 ns
Cursor 1
Files
Structure
Resources
3.asdb SIM
Console
run @30ns
# KERNEL: stopped at time: 30 ns
# KERNEL: Simulation has finished. There are no more test vectors to simulate.
# Waveform file 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/3.awc' connected to 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/3.asdb'.
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\3.asdb ... Done
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\3.awc ... Done
endsim
# VSIM: Simulation has finished.
NUM INS
32°C
Meist sonnig
Search
ENG
2:52 PM
6/1/2023

(Q4


HW2, design - C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/4.awc - Active-HDL 12 (64-bit)
Design Browser
alarmforclock_tb (TB_ARCHITECTURE)
Hierarchy
alarmforclock_tb (TB_ARCHITECTURE)
Name
Value
clk
Unavailable
rst
Unavailable
clkSet
Unavailable
alarmSet
Unavailable
alarmStop
Unavailable
hour
Unavailable
min
Unavailable
on_alarm
Unavailable
clk_period
Unavailable
4.vhd
4.awc
Signal name
Value
clk
U
rst
0
clkSet
0
alarmSet
0
alarmStop
U
hour
0B
min
24
on_alarm
0
clk_period
1000 ps
90 ns
Cursor 1
Files
Structure
Resources
4.asdb SIM
Console
# KERNEL: stopped at time: 80 ns
run @90ns
# KERNEL: stopped at time: 90 ns
endsim
# VSIM: Simulation has finished.
# Waveform file 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/4.awc' connected to 'C:/Users/free/Desktop/UNI/term6/CAD/HW2/design/src/4.asdb'.
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\4.asdb ... Done
# Adding file C:\Users\free\Desktop\UNI\term6\CAD\HW2\design\src\4.awc ... Done
NUM INS
32°C
Hitze
Search
ENG
3:17 PM
6/1/2023