## Michael Hillendahl

michael.hillendahl@gmail.com • linkedin.com/in/mhillendahl • Los Angeles, CA

## Technical Consultant seeking full-time role in Southern California

Motivated to leverage technical experience in collaborative role.

Programmer

Engineer

**Solutions Architect** 

Solutions Architect	Engineer	Programmer	Human
Process Automation	Solutions Engineering	C/Java•Python/Perl•HTML/XML	Musician • Performer
Multi-server Systems	Pre-Sales Engineering	I2C•SPI•Serial•Parallel	Hiker • Climber
SQL & Repositories Tech Documentation	Release Engineering Application Engineering	UART•JTAG•GPIO•Drivers Data Structures•Algorithms	Enthusiast • Tinkerer Son • Brother
recii Documentation	Application Engineering	Data Structures Algorithms	3011 • Blottlei
Consulting	Solutions Engineer at Laserfiche (2018)  • Managed development, release, and support of Enterprise solutions		
Pre-Sales	<ul> <li>Conducted on-site interviews, demonstrations with high-value customers</li> <li>Drafted Statements of Work for \$1MM+ custom Enterprise solutions</li> </ul>		
Solutions	<ul> <li>Lead requirements gathering, analysis with subject matter experts</li> <li>Engineered custom solutions to complex workflow and process problems</li> </ul>		
Go-Live	<ul> <li>Coordinated personnel and technical efforts for system Go-Live</li> <li>Managed promotion across Dev, QA, Stage, Production environments</li> </ul>		
Architecture	<ul> <li>Designed, implemented, customized multi-server system architecture</li> <li>Configured databases, built repositories, managed service accounts</li> </ul>		
Development	Electronics Engineer at Marshall Electronics (2010-2017)		
	<ul> <li>Managed development, release, and support of flagship platforms</li> </ul>		
R & D	<ul> <li>Defined product spec, maximized cost/time efficiency of design reqs</li> <li>Adjusted dev plan or spec as necessary when technical issues arose</li> </ul>		
Testing	<ul> <li>Rewrote algorithm for project workflow between Engineering and QA</li> <li>Optimized artifact resolution and minimized development time</li> </ul>		
Release	<ul> <li>Managed technical organization of product and firmware releases</li> <li>Spearheaded related interdepartmental coordination</li> </ul>		
Production	<ul> <li>Conducted streamlining of R&amp;D procedures for Production team</li> <li>Managed technical implementation in production environment</li> </ul>		
Firmware	<ul> <li>Wrote bare metal embedded C for video and ARM processors</li> <li>Increased responsiveness &gt;100% rewriting key algorithms</li> </ul>		
Design	<ul> <li>Ensured market relevance via competitor and industry research</li> <li>Created feature-set and UI for next generation of devices</li> </ul>		
Efficiency	<ul> <li>Automated panel calibration, reduced overhead by over 95%</li> <li>Accelerated dev team's ability to launch new products</li> </ul>		
Education	<ul><li>Electrical Engineering Bachelor of Science at USC (2010)</li><li>University of Southern California Viterbi School of Engineering</li></ul>		