Michael Hillendahl

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Electronics Engineer seeking **full-time** role in **Southern California**

Driven to leverage communication and technical aptitude in collaborative role.

Engineer	Electronics Specialist	Solutions Architect	Scholar
Electronics Engineering	Hardware Testing	C.Java • Python.Perl • HTML.XML	USC Viterbi Alum
Firmware Engineering	Oscilloscopes	I2C • SPI • Serial • Parallel	BS Electrical Engr
Solutions Engineering	Power Supplies	UART • JTAG • GPIO • Drivers	Analog Circuit Dsn
Application Engineering	Function Generators	Data Structures • Algorithms	Digital Audio

Consulting

Solutions

Pre-Sales

Go-Live

Architecture

Solutions Engineer at Laserfiche (2018)

- Engineered custom solutions for complex workflow/process problems
- Managed development, release, and support of Enterprise solutions
- Lead requirements gathering, analysis with subject matter experts
- Conducted on-site interviews, demos with high-value customers
- Drafted Statements of Work for $1,000,000+ custom Enterprise solutions
- Coordinated, executed solution deployment into live production servers
- Lead solution promotion across Dev, QA, Stage, and Prod environments
- Designed, implemented, customized multi-server system architecture
- Configured databases, built repositories, managed service accounts

Development

Firmware

Testing

Release

Production

R & D

Design

Efficiency

Electronics Engineer at Marshall Electronics (2010-2017)

- Wrote bare metal embedded C for video and ARM processors
- Spearheaded development, release, and support of flagship platforms
- Increased responsiveness >100% by rewriting key algorithms
- Rectified procedures outlining Engineering and QA team coordination
- Optimized artifact resolution and minimized development time
- Managed technical organization of product and firmware releases
- Spearheaded related interdepartmental coordination
- Conducted streamlining of R&D procedures for Production team
- Performed technical implementation in production environment
- Defined product spec, maximized cost/time efficiency of design reqs
- Adjusted dev plan or spec as necessary when technical issues arose
- Ensured market relevance via competitor and industry research
- Created feature-set and UI for next generation of devices
- Automated panel calibration, reduced overhead by over 95%
- Accelerated dev team's ability to launch new products

About Me

Human - Son, Brother, Partner, Friend

- Musician, Performer, Hiker, Climber, Enthusiast, Tinkerer, Gamer