Michael Hillendahl

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QA Engineer seeking full-time role in Southern California

Driven to leverage communication and technical aptitude in collaborative role.

Engineer	Programmer	Solutions Architect	Scholar
Software Engineering	C • C++ • C# • Java	Process Automation	USC Viterbi Alum
Solutions Engineering	Python • Perl • HTML • XML	Multi-server Systems	BS Electrical Engr
Firmware Engineering	I2C • SPI • Serial • Parallel	SQL & Repositories	Acoustics & Dig Audio
Application Engineering	UART • JTAG • GPIO • Drivers	Tech Documentation	Analog Circuit Design

Consulting

Solutions Engineer at Laserfiche (2018)

• Engineered custom solutions for complex workflow/process problems

Solutions
• Managed development, release, and support of Enterprise solutions
• Lead requirements gathering, analysis with subject matter experts

Pre-Sales
• Conducted on-site interviews, demos with high-value customers
• Drafted Statements of Work for $1,000,000+ custom Enterprise solutions

Go-Live
• Coordinated, executed solution deployment into live production servers
• Lead solution promotion across Dev, QA, Stage, and Prod environments

Architecture
• Designed, implemented, customized multi-server system architecture
• Configured databases, built repositories, managed service accounts

Development

Electronics Engineer at Marshall Electronics (2010-2017)

• Wrote bare metal embedded C for video and ARM processors

Firmware
• Spearheaded development, release, and support of flagship platforms
• Increased responsiveness >100% by rewriting key algorithms

Testing
• Rectified procedures outlining Engineering and QA team coordination
• Optimized artifact resolution and minimized development time

Release
• Managed technical organization of product and firmware releases
• Spearheaded related interdepartmental coordination

Production
• Conducted streamlining of R&D procedures for Production team
• Performed technical implementation in production environment

R & D
• Defined product spec, maximized cost/time efficiency of design reqs
• Adjusted dev plan or spec as necessary when technical issues arose

Design
• Ensured market relevance via competitor and industry research
• Created feature-set and UI for next generation of devices

Efficiency
• Automated panel calibration, reduced overhead by over 95%
• Accelerated dev team's ability to launch new products

About Me

Human - Son, Brother, Partner, Friend

• Musician, Performer, Hiker, Climber, Enthusiast, Tinkerer, Gamer