



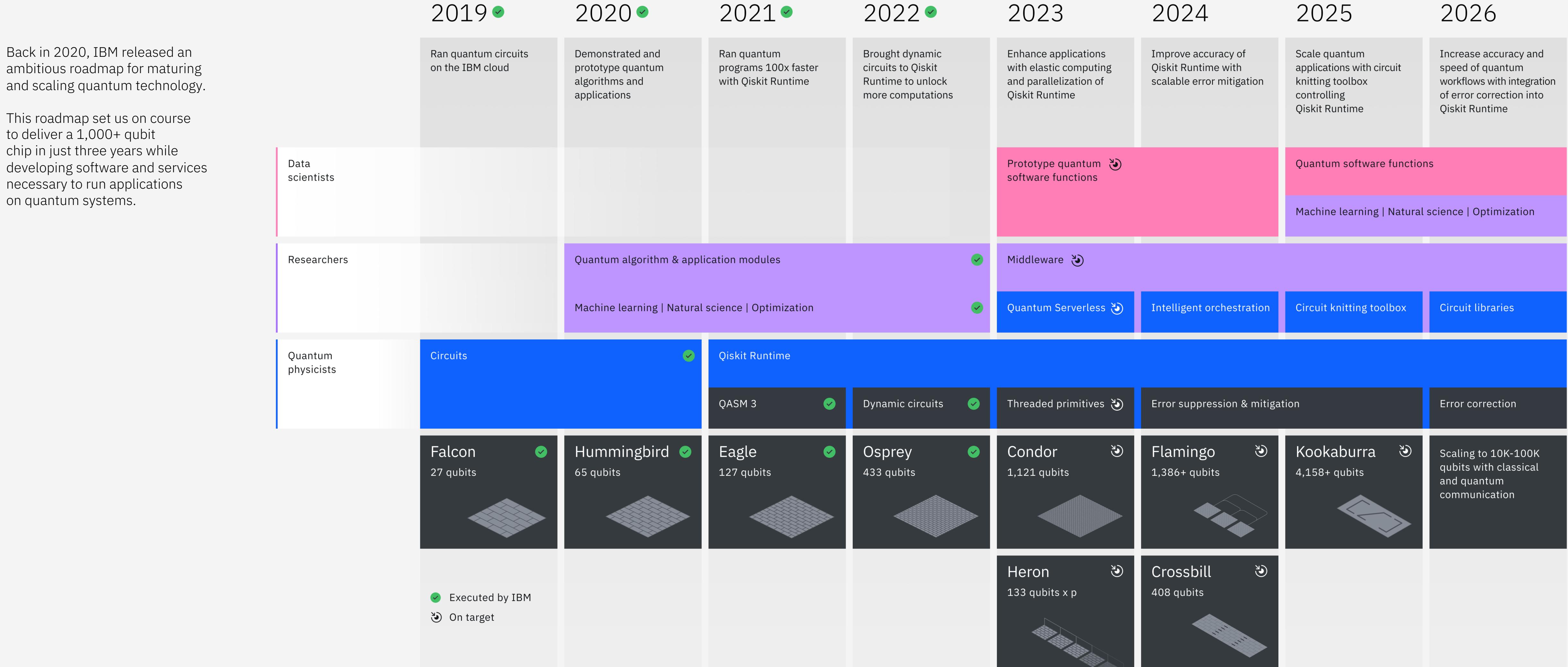
Development & Innovation Roadmap

IBM Quantum

IBM®

Development roadmap

IBM Quantum

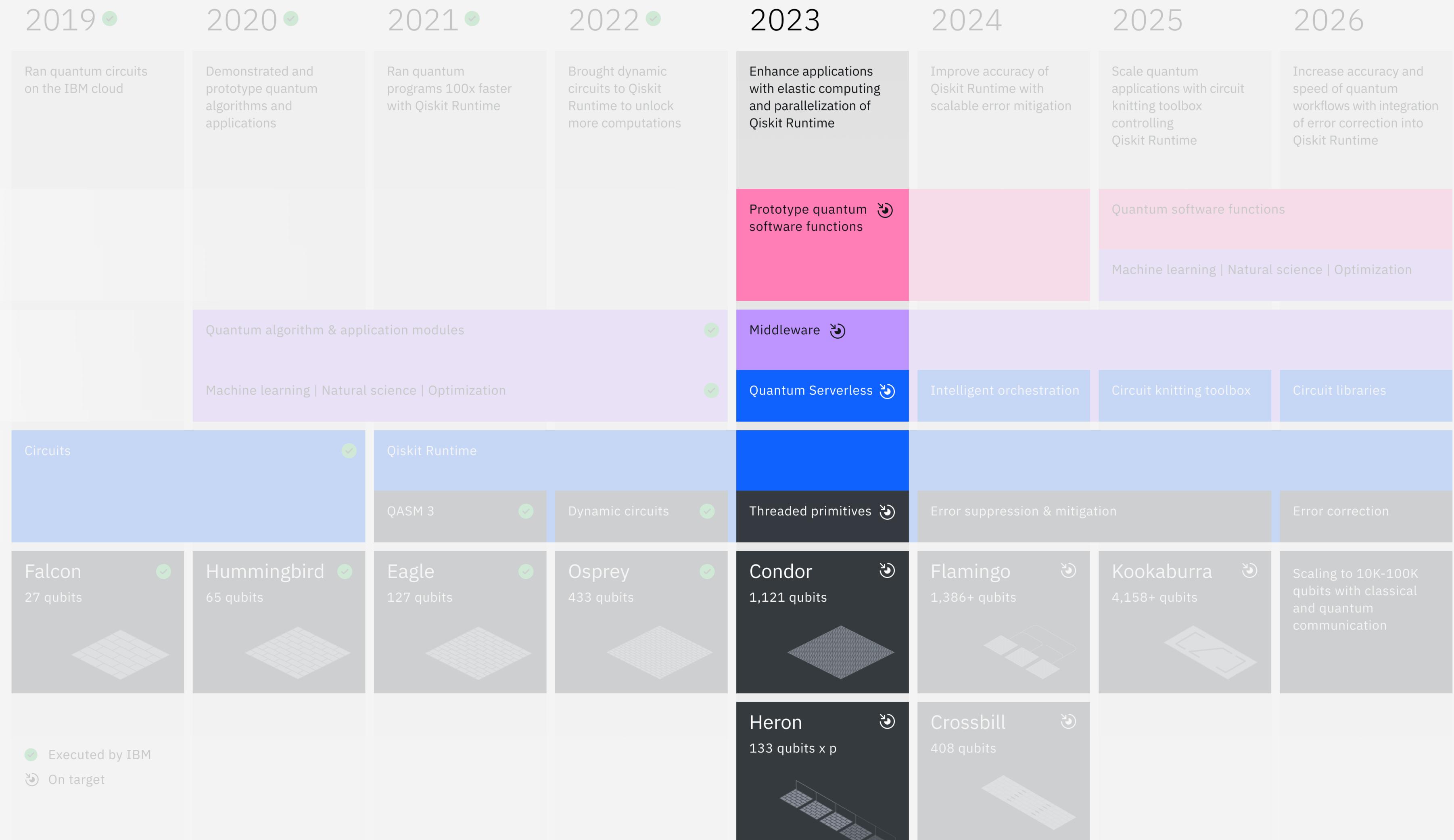


Development roadmap

IBM Quantum

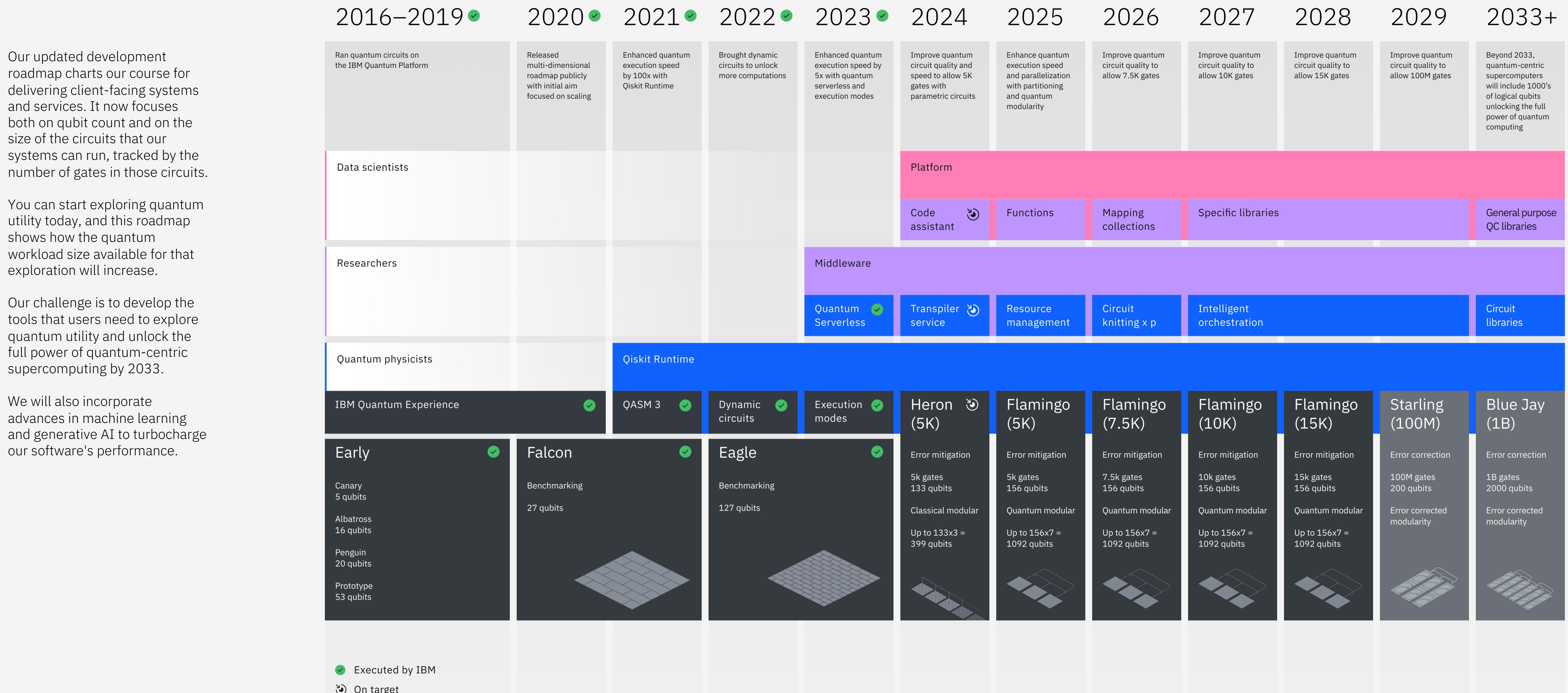
By 2023, our research and development work made it possible to use quantum computers as tools to run circuits beyond the reach of brute-force classical computation. We could also begin thinking about implementing error correction.

We realized: it was time for a bigger roadmap.

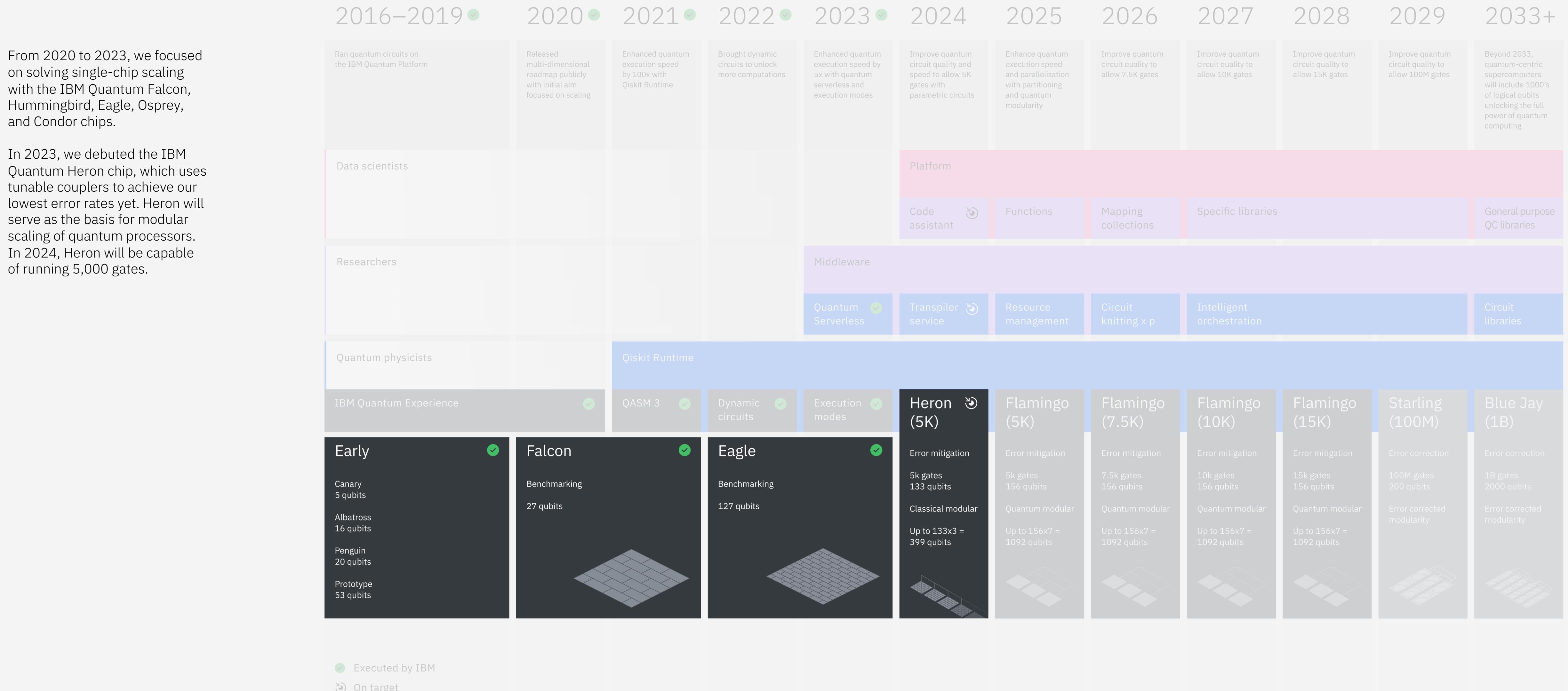


Development roadmap:

Updated



What we have accomplished: Hardware



Looking forward: Hardware

IBM Quantum

Now, we use error mitigation and interconnects to run larger circuits so users can look for quantum advantages in their domains. Through classical and quantum modularity, we plan to achieve an IBM Quantum Flamingo system capable of running 15,000 gates with the help of error mitigation by 2028.

We foresee advances in quantum error correction allowing us to debut IBM Quantum Starling, a system capable of running circuits with 100 million gates on 200 logical qubits, by 2029. In 2033, we will debut IBM Quantum Blue Jay, a system capable of running circuits with a billion gates on 2,000 logical qubits.

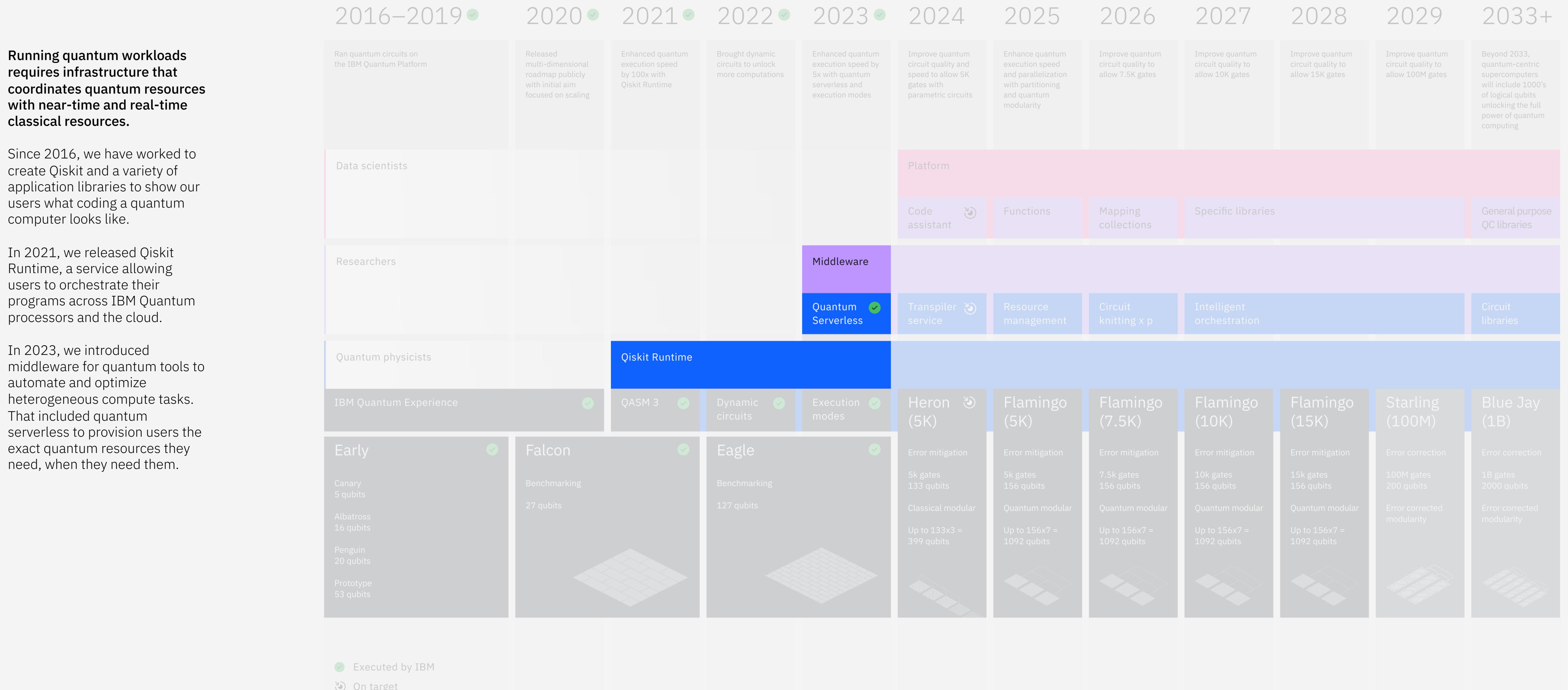
As we roll out error correction, developers need not change how they write quantum programs. They will simply notice that they can run longer workloads.



Executed by IBM

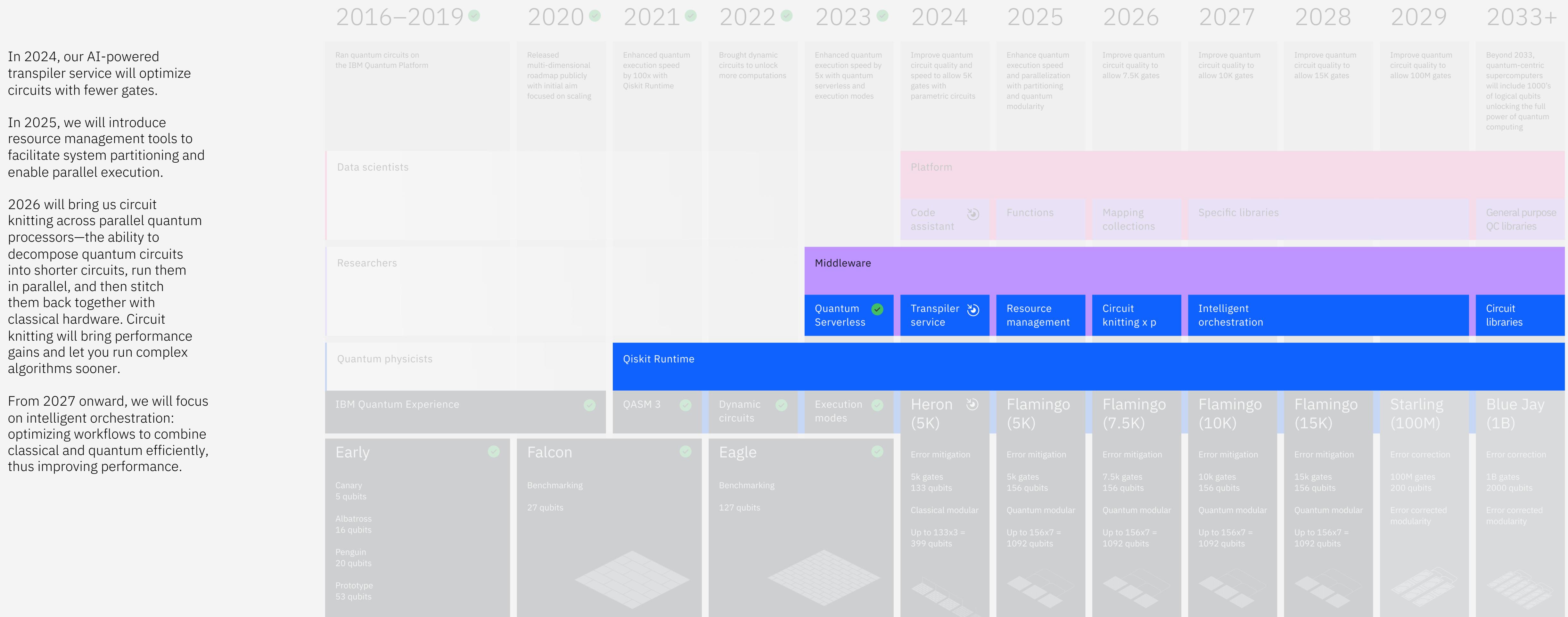
On target

What we have accomplished: Execution and orchestration



Looking forward:

Execution and orchestration

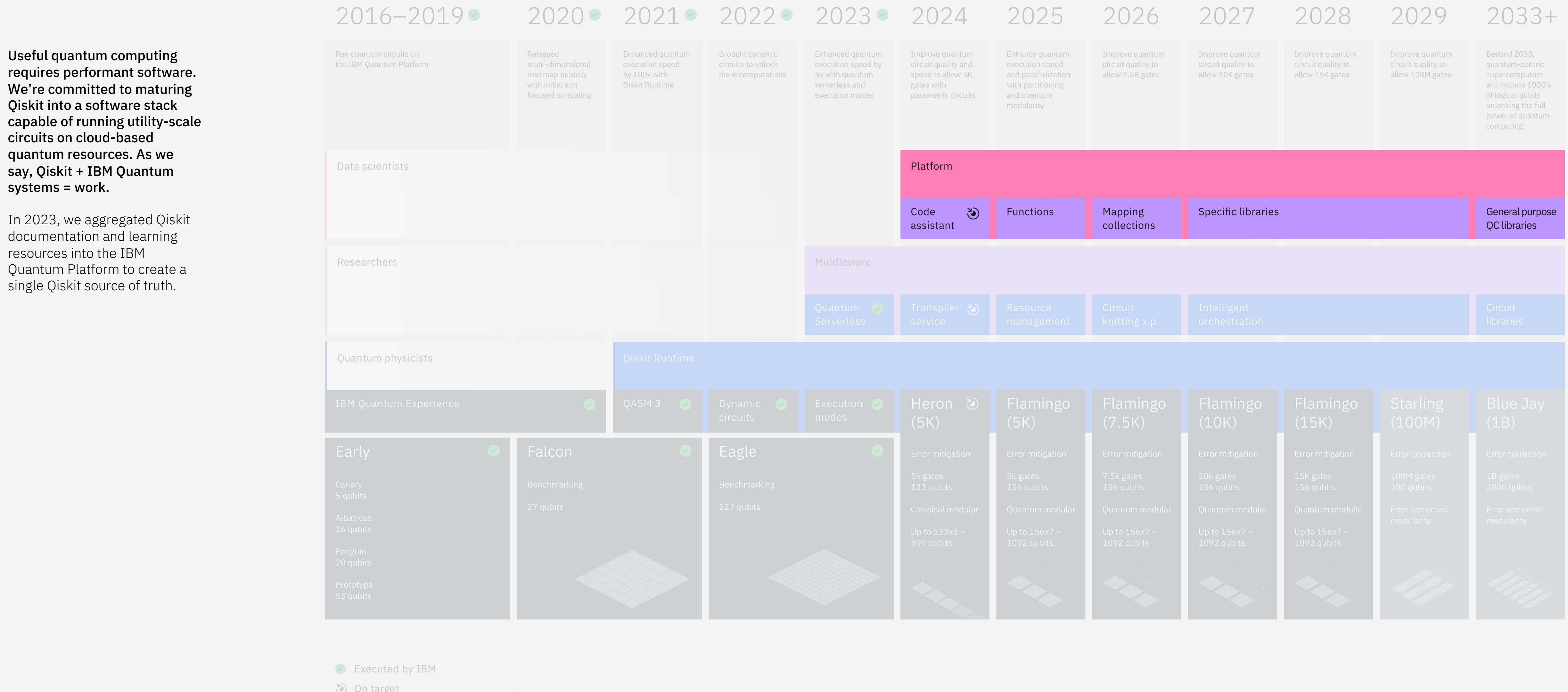


Executed by IBM

On target

What we have accomplished:

Software



Looking forward: Software

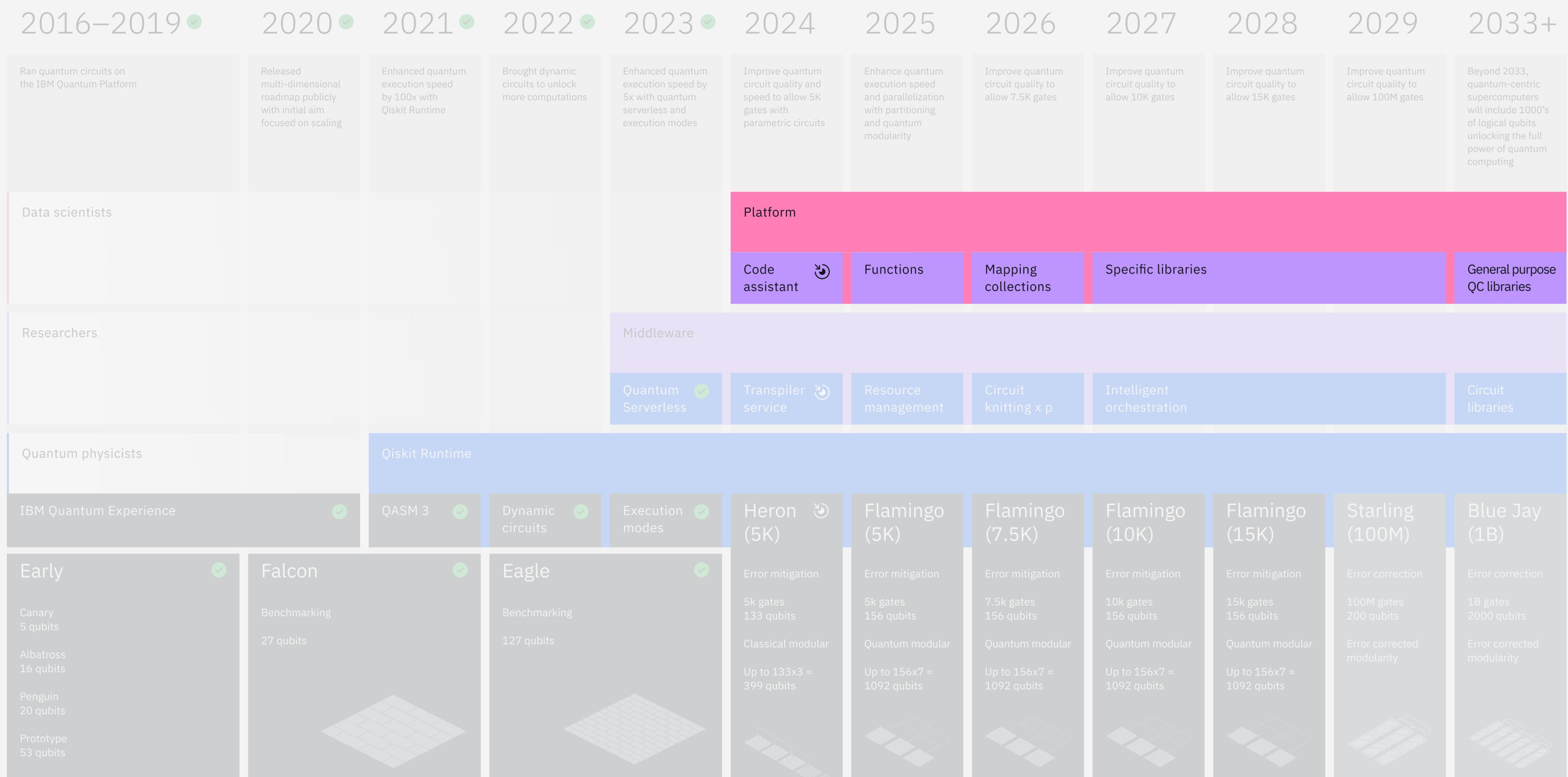
IBM Quantum

In 2025, we will introduce quantum functions so users can create and share reusable blocks of Qiskit code.

2026 will bring mapping collections so users can start automating the process of mapping their specific use cases to quantum circuits.

From 2027 onward, we will work alongside clients to build use-case-specific libraries as quantum advantages emerge for a variety of use cases.

By 2033, we expect to see general-purpose quantum computing libraries that users can incorporate into a wide variety of quantum applications.



Executed by IBM

On target

Innovation roadmap

IBM Quantum

2016–2019 ✓ 2020 ✓ 2021 ✓ 2022 ✓ 2023 ✓ 2024 2025 2026 2027 2028 2029

We remain committed to the transparent development of IBM quantum hardware and software. This includes showing off scientific discoveries required to clear roadblocks in the field.

Therefore, in 2023, we also announced our innovation roadmap. This roadmap features internal releases of hardware and software to enable the subsequent milestones on our development roadmap.

Some technologies on our innovation roadmap will be internal proofs-of-concept to inform future development. Others will be prototypes for eventual release.

	Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with quantum serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates
Software innovation	IBM Quantum Experience ✓	Qiskit ✓	Application modules ✓	Qiskit Runtime ✓	Quantum Serverless ✓	AI-enhanced quantum ✓	Resource management	Scalable circuit knitting	Error correction decoder		
Hardware innovation	Early ✓ Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	Falcon ✓ Demonstrate scaling with I/O routing with bump bonds	Hummingbird ✓ Demonstrate scaling with multiplexing readout	Eagle ✓ Demonstrate scaling with MLW and TSV	Osprey ✓ Enabling scaling with high density signal delivery	Condor ✓ Single system scaling and fridge capacity	Flamingo ✅ Demonstrate scaling with modular connectors	Kookaburra Demonstrate scaling with nonlocal c-coupler	Cockatoo Demonstrate path to improved quality with logical memory	Starling Demonstrate path to improved quality with logical gates	

✓ Executed by IBM

✅ On target

Innovation roadmap

IBM Quantum

2016–2019 ● 2020 ● 2021 ● 2022 ● 2023 ● 2024 2025 2026 2027 2028 2029

Our hardware innovations focus on building interconnects that allow us to scale processors and parallelize quantum workloads while laying a foundation for quantum error correction.

In 2024, we will demonstrate m-couplers to seam chips together and l-couplers to connect chips over longer distances with Crossbill and Flamingo, respectively.

In 2025 and 2026, we will develop c-couplers capable of linking distant qubits on the same chip as required by error correction schemes for a concept called Kookaburra.

2027 and 2028 further pave a path to error correction. Cockatoo will debut logical communication and Starling will be able to run logical gates on error-corrected logical qubits.

Software innovation	Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with quantum serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates
	IBM Quantum Experience	Qiskit	Application modules	Qiskit Runtime	Quantum Serverless	AI-enhanced quantum	Resource management	Scalable circuit knitting	Error correction decoder		
Hardware innovation	Circuit and operator API with compilation to multiple targets	Modules for domain specific application and algorithm workflows	Performance and abstraction through primitives	Demonstrate concepts of quantum-centric supercomputing	Prototype demonstrations of AI-enhanced circuit transpilation	System partitioning to enable parallel execution	Circuit partitioning with classical reconstruction at HPC scale				
	Early Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	Falcon Demonstrate scaling with I/O routing with bump bonds	Hummingbird Demonstrate scaling with multiplexing readout	Eagle Demonstrate scaling with MLW and TSV	Osprey Enabling scaling with high density signal delivery	Condor Single system scaling and fridge capacity	Flamingo Demonstrate scaling with modular connectors	Kookaburra Demonstrate scaling with nonlocal c-coupler		Cockatoo Demonstrate path to improved quality with logical communication	Starling Demonstrate path to improved quality with logical gates

● Executed by IBM

⌚ On target

2016–2019 ● 2020 ● 2021 ● 2022 ● 2023 ● 2024 2025 2026 2027 2028 2029

Our software innovations will support the execution of large circuits on modular quantum computers and build the tools for a frictionless developer experience, rising to the Development Roadmap in the following years.

In 2023, we showed our plan to incorporate AI into quantum computing workflows with AI-assisted circuit transpilation.

In 2024 and 2025, we will prototype new tools for resource management and scalable circuit knitting for parallel execution and classical reconstruction of circuits at the HPC scale.

In 2026, we will prototype a real-time error correction decoder for later error corrected systems.

Software innovation	Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with quantum serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates
	IBM Quantum Experience	Qiskit	Application modules	Qiskit Runtime	Quantum Serverless	AI-enhanced quantum	Resource management	Scalable circuit knitting	Error correction decoder		
	Circuit and operator API with compilation to multiple targets	Modules for domain specific application and algorithm workflows	Performance and abstraction through primitives	Demonstrate concepts of quantum-centric supercomputing	Prototype demonstrations of AI-enhanced circuit transpilation	System partitioning to enable parallel execution	Circuit partitioning with classical reconstruction at HPC scale	Demonstration of a quantum system with real-time error correction decoder			
	Early	Falcon	Hummingbird	Eagle	Osprey	Condor	Flamingo	Kookaburra	Cockatoo	Starling	
	Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	Demonstrate scaling with I/O routing with bump bonds	Demonstrate scaling with multiplexing readout	Demonstrate scaling with MLW and TSV	Enabling scaling with high density signal delivery	Single system scaling and fridge capacity	Demonstrate scaling with modular connectors	Demonstrate scaling with nonlocal c-coupler	Demonstrate path to improved quality with logical memory	Demonstrate path to improved quality with logical communication	
						Heron	Crossbill				
						Architecture based on tunable-couplers	Demonstrate m-couplers				

● Executed by IBM

⌚ On target

2016–2019 ✓ 2020 ✓ 2021 ✓ 2022 ✓ 2023 ✓ 2024 2025 2026 2027 2028 2029 2033+

Ran quantum circuits on the IBM Quantum Platform	Released multi-dimensional roadmap publicly with initial aim focused on scaling	Enhanced quantum execution speed by 100x with Qiskit Runtime	Brought dynamic circuits to unlock more computations	Enhanced quantum execution speed by 5x with Quantum Serverless and execution modes	Improve quantum circuit quality and speed to allow 5K gates with parametric circuits	Enhance quantum execution speed and parallelization with partitioning and quantum modularity	Improve quantum circuit quality to allow 7.5K gates	Improve quantum circuit quality to allow 10K gates	Improve quantum circuit quality to allow 15K gates	Improve quantum circuit quality to allow 100M gates	Beyond 2033, quantum-centric supercomputers will include 1000's of logical qubits unlocking the full power of quantum computing
Data scientists					Platform	Code assistant	Functions	Mapping collections	Specific libraries		General purpose QC libraries
Researchers				Middleware	Quantum Serverless	Transpiler service	Resource management	Circuit knitting x p	Intelligent orchestration		Circuit libraries
Quantum physicists	IBM Quantum Experience	QASM 3	Dynamic circuits	Execution modes	Heron (5K)	Flamingo (5K)	Flamingo (7.5K)	Flamingo (10K)	Flamingo (15K)	Starling (100M)	Blue Jay (1B)
	Early Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	Falcon Benchmarking 27 qubits	Eagle Benchmarking 127 qubits		Error mitigation 5k gates 133 qubits Classical modular 133x3 = 399 qubits	Error mitigation 5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 7.5k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 10k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error mitigation 15k gates 156 qubits Quantum modular 156x7 = 1092 qubits	Error correction 100M gates 200 qubits Error corrected modularity	Error correction 1B gates 2000 qubits Error corrected modularity

Innovation Roadmap

Software innovation	IBM Quantum Experience ✓	Qiskit ✓ Circuit and operator API with compilation to multiple targets	Application modules ✓ Modules for domain specific application and algorithm workflows	Qiskit Runtime ✓ Performance and abstraction through primitives	Quantum Serverless ✓ Demonstrate concepts of quantum-centric supercomputing	AI-enhanced quantum ✓ Prototype demonstrations of AI-enhanced circuit transpilation	Resource management System partitioning to enable parallel execution	Scalable circuit knitting Circuit partitioning with classical reconstruction at HPC scale	Error correction decoder Demonstration of a quantum system with real-time error correction decoder		
Hardware innovation	Early Canary 5 qubits Albatross 16 qubits Penguin 20 qubits Prototype 53 qubits	Falcon Demonstrate scaling with I/O routing with bump bonds	Hummingbird Demonstrate scaling with multiplexing readout	Eagle Demonstrate scaling with MLW and TSV	Osprey Enabling scaling with high density signal delivery	Condor Single system scaling and fridge capacity	Flamingo Demonstrate scaling with modular connectors	Kookaburra Demonstrate scaling with nonlocal c-coupler	Cockatoo Demonstrate path to improved quality with logical memory	Starling Demonstrate path to improved quality with logical gates	

