LAB₁

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Lab 1: create a BareMetal software

"learn-in-depth:mohamed" BY UART.

SECTION APP.O

```
MINGW64:/c/embedded c/embedded dip/lab 1
                                                                        ×
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
$ arm-none-eabi-objdump.exe -h app.o
           file format elf32-littlearm
app.o:
Sections:
                                                File off
Idx Name
                  Size
                            VMA
                                      LMA
                                                          Algn
                            00000000 00000000
                                                00000034
 0 .text
                  0000001c
                  CONTENTS, ALLOC, LOAD, RELOC,
                                                READONLY, CODE
 1 .data
                  00000064
                            00000000 00000000
                                                00000050 2**2
                  CONTENTS,
                            ALLOC, LOAD, DATA
 2 .bss
                  00000000
                            00000000 00000000
                                                000000b4
                                                          2**0
                  ALLOC
                  0000007f
 3 .comment
                            00000000
                                     00000000
                                                000000b4
                                                          2**0
                 CONTENTS, READONLY
 4 .ARM.attributes 00000032 00000000 00000000 00000133 2**0
                  CONTENTS, READONLY
```

SECTIONS UART.O

```
4HMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
 arm-none-eabi-objdump.exe -h uart.o
           file format elf32-littlearm
uart.o:
Sections:
Idx Name
                  Size
                                                File off
                                                          Algn
                            VMA
                                      LMA
 0 .text
                  00000054
                            00000000
                                     00000000
                                                00000034
                                                          2**2
                 CONTENTS, ALLOC, LOAD, READONLY, CODE
                 00000000
 1 .data
                           00000000 00000000 00000088
                                                          2**0
                 CONTENTS, ALLOC, LOAD, DATA
                 00000000 00000000 00000000
 2 .bss
                                                00000088
                  ALLOC
                 0000007f 00000000
                                      00000000 00000088
                                                          2**0
 3 .comment
                 CONTENTS, READONLY
 4 .ARM.attributes 00000032 00000000 00000000 00000107 2**0
                 CONTENTS, READONLY
```

SECTIONS STARTUP.O

```
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
$ arm-none-eabi-objdump.exe -h startup.o
startup.o:
              file format elf32-littlearm
Sections:
Idx Name
                  Size
                           VMA
                                      LMA
                                                File off
 0 .text
                  00000010
                           00000000 00000000
                                               00000034
                  CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
 1 .data
                  00000000 00000000 00000000 00000044
                  CONTENTS, ALLOC, LOAD, DATA
                  00000000 00000000 00000000 00000044
 2 .bss
                  ALLOC
  3 .ARM.attributes 00000022 00000000
                                       00000000
                                                 00000044
                                                            2**0
                  CONTENTS, READONLY
```

SECTIONS OF learn-in-depth.elf

```
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
 arm-none-eabi-objdump.exe -h learn-in-depth.elf
learn-in-depth.elf:
                       file format elf32-littlearm
Sections:
                                                File off
Idx Name
                 Size
                                     LMA
 0 .startup
                 00000010
                           00010000
                                     00010000
                                               00010000
                                                         2**2
                 CONTENTS, ALLOC, LOAD, READONLY, CODE
                 00000070 00010010 00010010
 1 .text
                                               00010010
                 CONTENTS, ALLOC, LOAD, READONLY, CODE
 2 .data
                 00000064 00010080 00010080 00010080 2**2
                 CONTENTS, ALLOC, LOAD, DATA
 3 .ARM.attributes 0000002e 00000000 00000000 000100e4 2**0
                 CONTENTS, READONLY
                 0000007e 00000000
                                     00000000 00010112 2**0
 4 .comment
                 CONTENTS, READONLY
```

Symbols of app.o

```
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
$ arm-none-eabi-nm.exe app.o
00000000 T main
00000000 D string_buffer
U uart_sendSTR
```

Symbols of uart.o

```
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1
$ arm-none-eabi-nm.exe uart.o
00000000 T uart_sendSTR
```

symbols of startup.o

symbols of learn-in-depth.elf

```
MHMD AMiin@DESKTOP-BDR7C1I MINGW64 /c/embedded c/embedded dip/lab 1

$ arm-none-eabi-nm.exe learn-in-depth.elf

00010010 T main

00010000 T reset

000110e4 D stack_top

00010008 t stop

00010080 D string_buffer

0001002c T uart_sendSTR
```

The final output of code by qemu