Mohammad Haris Minai

Doctoral candidate at IIM Lucknow

✆ +91-9044823169
✉ mhminai@iiml.ac.in
🖰 www.mhminai.com

Education

2012–present	**Fellow Program in Management**, *Indian Institute of Management*, Lucknow, India. The Fellow Program in Management (FPM) is a doctoral level program offered by the Indian Institutes of Management.

Expected completion: June, 2016
- Expected completion: June 2016
- CGPA for coursework: 8.4/10.0
- **Advisor**: Dr. Shailendra Singh

1998–2012	**Non academic work experience in semiconductor industry**, *Please see "Work Experience" section of vita for details*.
1994–1998	**B.Tech. Electronics Engineering**, *ZHCET, Aligarh Muslim University*, Aligarh, India. A four year bachelors program in engineering, established in 1988.

- Dissertation: Development of FIR filters in VHDL
- CGPA: 8.1/10.0

1992–1994	**Senior Secondary School Certificate**, *Aligarh Muslim University*, Aligarh, India. Qualifying exam for all university and college studies in India

- Aggregate percentage: 81%

1992	**GCE O' Levels**, *University of London*, School Examinations board. Equivalent to 11 years of education as per English system

- Grade A: Mathematics, Physics, Chemistry, and Biology
- Grade B: Pure Mathematics, and English

Manuscripts

Under review	Gupta M., and **Minai M. H.** *An Evaluation of Forecast Performance of GDP Growth in India*, Indian Economic Review.
Working	**Minai M. H.**, Jauhari H., and Singh S. *Exploring the boundary conditions of the relationship between LMX and Work Engagement.*, IIML WPS2014-15/11
Working	Jauhari H., **Minai M. H.**, and Singh S. *The role of Transformational Leadership in Psychologically Empowering frontline service employees.*, IIML WPS 2014-15/02
In progress	**Minai M. H.**, and Sahay Y. P. *Strength of Organizational Culture, Innovative Work Behaviors and Fit: A relational analysis.*

Conferences

2014	Gupta M., and **Minai M. H.** *Forecast Performance of GDP growth in India*, Poster presented at the 2nd Pan-IIM World Management (International) Conference, IIM Kozhikode, India.

2008   Kafeel M. A., Samad A., and **Minai M. H.** (2008, March, 29th – 30th) *Basics of FPGA design in context of the AES algorithm.* Paper presented at National Conference on Emerging Technologies (NCET-08), Lucknow, India.

2007   Kafeel M. A., and **Minai M. H.** (2007, September, 8th and 9th) *FPGA based AES Hardware implementation for secure management of systems.* Paper presented at All India Seminar on Communication Convergence (Institution of Engineers), Lucknow, India.

Other Activities

2015   Assisted in the creation of course outline for first course in Organizational Behavior for MBA students.

2015   Coordinator for interview of short listed doctoral candidates, IIM Lucknow, India.

2015   Reviewer for AOM Annual Meeting 2015.

2014   Reviewer for Pan-IIM World Management Conference held at IIM Kozhikode, India.

2014   Organized 5-day workshop for doctoral students on *Social Network Analysis* and *Structural Equation Modelling*, IIM Lucknow, India.

2014   Organized 3-day workshop for doctoral students on *Meta-analysis*, IIM Lucknow, India.

2013   Took a half hour session on "Leadership and Mentoring" for second year MBA students at IIM Lucknow, India.

2009   Invited lecture on *"Hardware accelerators for Network Security"* at workshop on "Network Security and Applications", Institute of Engineers, Women's Polytechnic, Aligarh, India.

2007–2008   Technical training on *Verilog Hardware Description Language*, for fresh graduates, Freescale Semiconductors, Noida, India.

Computer Skills

Office	Word, Excel, PowerPoint	Statistics	SPSS, AMOS, LISREL, R
Writing	LaTeX, JabRef(BibTex), Pandoc	Languages	Verilog, VHDL, R, C

Work Experience

2005–2012   **Applications Engineering Manager**, *Freescale Semiconductors*, Noida, India.
Started off as a *Senior IC Design Engineer* in 2005, was promoted to *Design Manager* in 2006. In 2009 I changed business group to **Applications** as *Applications Engineer* and was promoted to *Applications Engineering Manager* in 2010.

2004–2005   **Lead Engineer**, *Texas Instruments*, Bangalore, India.

2003–2004   **Senior Design Engineer**, *Interra Systems*, Noida, India.

2001–2003   **ASIC Design Engineer**, *Cogency Semiconductors*, Toronto, Canada.

2001–2001   **Emulation Engineer**, *Tundra Semiconductor*, Ottawa, Canada.

1998–2000   **Team Lead**, *STMicroelectronics*, Noida, India.
Was *Design Engineer* for 9 months and for the rest of the duration I worked as a *Team Lead*.

Accomplishments

What to put here	And here	then here	and here

Personal Details

What to put here And here   then here   and here

References

Dr. Shailendra Singh
Professor, HRM Area,
Indian Institute of Management, Lucknow,
shail@iiml.ac.in

Dr. Abhijit Bhattacharya
Professor, Decision Sciences Area,
Indian Institute of Management, Lucknow,
abhijit@iiml.ac.in