# 1. Description

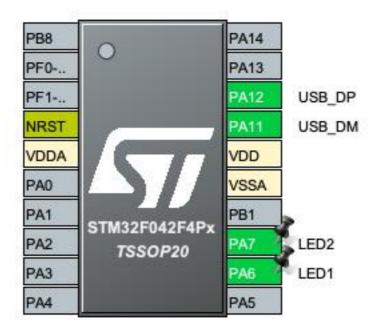
## 1.1. Project

| Project Name    | EOGee             |
|-----------------|-------------------|
| Board Name      | custom            |
| Generated with: | STM32CubeMX 5.5.0 |
| Date            | 02/16/2020        |

#### 1.2. MCU

| MCU Series     | STM32F0       |
|----------------|---------------|
| MCU Line       | STM32F0x2     |
| MCU name       | STM32F042F4Px |
| MCU Package    | TSSOP20       |
| MCU Pin number | 20            |

# 2. Pinout Configuration

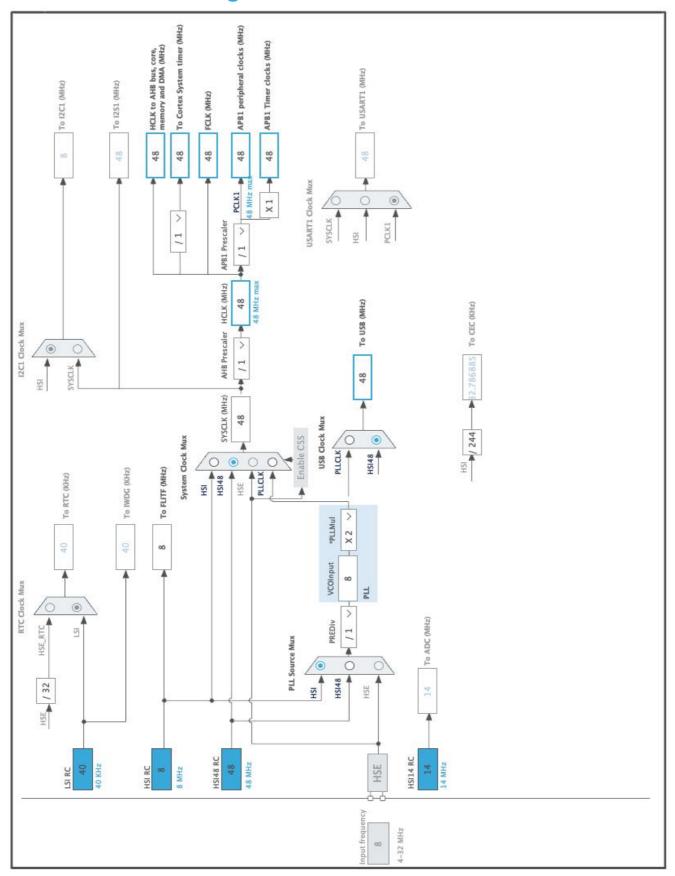


# 3. Pins Configuration

| Pin Number<br>TSSOP20 | Pin Name<br>(function after<br>reset) | Pin Type | Alternate<br>Function(s) | Label |
|-----------------------|---------------------------------------|----------|--------------------------|-------|
| 4                     | NRST                                  | Reset    |                          |       |
| 5                     | VDDA                                  | Power    |                          |       |
| 12                    | PA6 *                                 | I/O      | GPIO_Output              | LED1  |
| 13                    | PA7 *                                 | I/O      | GPIO_Output              | LED2  |
| 15                    | VSSA                                  | Power    |                          |       |
| 16                    | VDD                                   | Power    |                          |       |
| 17                    | PA11                                  | I/O      | USB_DM                   |       |
| 18                    | PA12                                  | I/O      | USB_DP                   |       |

<sup>\*</sup> The pin is affected with an I/O function

# 4. Clock Tree Configuration



# 5. Software Project

## 5.1. Project Settings

| Name                              | Value                              |  |
|-----------------------------------|------------------------------------|--|
| Project Name                      | EOGee                              |  |
| Project Folder                    | /Users/matthollands/Library/Mobile |  |
| Toolchain / IDE                   | STM32CubeIDE                       |  |
| Firmware Package Name and Version | STM32Cube FW_F0 V1.11.0            |  |

## 5.2. Code Generation Settings

| Name                                                          | Value                                 |
|---------------------------------------------------------------|---------------------------------------|
| STM32Cube MCU packages and embedded software                  | Copy only the necessary library files |
| Generate peripheral initialization as a pair of '.c/.h' files | No                                    |
| Backup previously generated files when re-generating          | No                                    |
| Delete previously generated files when not re-generated       | Yes                                   |
| Set all free pins as analog (to optimize the power            | No                                    |
| consumption)                                                  |                                       |

# 6. Power Consumption Calculator report

#### 6.1. Microcontroller Selection

| Series    | STM32F0       |
|-----------|---------------|
| Line      | STM32F0x2     |
| мси       | STM32F042F4Px |
| Datasheet | 025832_Rev5   |

#### 6.2. Parameter Selection

| Temperature | 25  |
|-------------|-----|
| Vdd         | 3.6 |

# 7. IPs and Middleware Configuration 7.1. GPIO

#### 7.2. RCC

#### 7.2.1. Parameter Settings:

**System Parameters:** 

VDD voltage (V) 3.3
Prefetch Buffer Enabled

Flash Latency(WS) 1 WS (2 CPU cycle)

**RCC Parameters:** 

HSE Startup Timout Value (ms) 100 LSE Startup Timout Value (ms) 5000

#### 7.3. SYS

mode: Pins PA11/12 instead of pins PA9/10

Timebase Source: SysTick

#### 7.4. USB

mode: Device (FS)

7.4.1. Parameter Settings:

**Basic Parameters:** 

Speed Full Speed 12MBit/s

Physical interface Internal Phy

**Power Parameters:** 

Low Power Disabled
Link Power Management Disabled

#### 7.5. USB DEVICE

Class For FS IP: Communication Device Class (Virtual Port Com)

7.5.1. Parameter Settings:

**Basic Parameters:** 

USBD\_MAX\_NUM\_INTERFACES (Maximum number of supported interfaces)

1
USBD\_MAX\_NUM\_CONFIGURATION (Maximum number of supported configuration)

1
USBD\_MAX\_STR\_DESC\_SIZ (Maximum size for the string descriptors)

512
USBD\_SELF\_POWERED (Enabled self power)

Enabled

USBD\_DEBUG\_LEVEL (USBD Debug Level) 0: No debug message

**Class Parameters:** 

USB CDC Rx Buffer Size 1000
USB CDC Tx Buffer Size 1000

#### 7.5.2. Device Descriptor:

#### **Device Descriptor:**

VID (Vendor IDentifier) 1155

LANGID\_STRING (Language Identifier) English(United States)

MANUFACTURER\_STRING (Manufacturer Identifier) STMicroelectronics

**Device Descriptor FS:** 

PID (Product IDentifier) 22336

PRODUCT\_STRING (Product Identifier) STM32 Virtual ComPort

CONFIGURATION\_STRING (Configuration Identifier) CDC Config
INTERFACE\_STRING (Interface Identifier) CDC Interface

#### \* User modified value

# 8. System Configuration

## 8.1. GPIO configuration

| IP   | Pin  | Signal      | GPIO mode        | GPIO pull/up pull           | Max   | User Label |
|------|------|-------------|------------------|-----------------------------|-------|------------|
|      |      |             |                  | down                        | Speed |            |
| USB  | PA11 | USB_DM      | n/a              | n/a                         | n/a   |            |
|      | PA12 | USB_DP      | n/a              | n/a                         | n/a   |            |
| GPIO | PA6  | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low   | LED1       |
|      | PA7  | GPIO_Output | Output Push Pull | No pull-up and no pull-down | Low   | LED2       |

### 8.2. DMA configuration

nothing configured in DMA service

## 8.3. NVIC configuration

| Interrupt Table                                                          | Enable | Preenmption Priority | SubPriority |  |
|--------------------------------------------------------------------------|--------|----------------------|-------------|--|
| Non maskable interrupt                                                   | true   | 0                    | 0           |  |
| Hard fault interrupt                                                     | true   | 0                    | 0           |  |
| System service call via SWI instruction                                  | true   | 0                    | 0           |  |
| Pendable request for system service                                      | true   | 0                    | 0           |  |
| System tick timer                                                        | true   | 0                    | 0           |  |
| USB global Interrupt / USB wake-up interrupt through EXTI line 18        | true   | 0                    | 0           |  |
| PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31 | unused |                      |             |  |
| Flash global interrupt                                                   | unused |                      |             |  |
| RCC and CRS global interrupts                                            | unused |                      |             |  |

<sup>\*</sup> User modified value

| 9. Software Pack Report | 9. | <b>Software</b> | <b>Pack</b> | Report |
|-------------------------|----|-----------------|-------------|--------|
|-------------------------|----|-----------------|-------------|--------|