

# Kobayashi Maru

## Heavy Duty 4WD Robot Platform

TRL	2 COMPLETE	LICENSE	GPL V3	PLATFORM	MCXN947	FRAMEWORK	QP/C++
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 **Technology Readiness Level:** TRL 2 Complete

 **Status:** Concept validated through simulation

 **Current State:** Architecture defined • Firmware operational in Renode • CI/CD established

 **Next Milestone:** TRL 3 component validation on physical hardware

[Documentation](#) • [Architecture](#) • [TRL Status](#)

## Project Overview

Heavy-duty autonomous 4WD robot platform with modular AI architecture:

-  **Modular AI processing unit** (Pixel 10 Pro, Raspberry Pi, or Jetson)
-  **GPS navigation** and sensor fusion
-  **Vision processing** with TensorFlow Lite
-  **Path planning** (A\*/RRT algorithms)
-  **Pan/tilt turret** for camera/sensor pointing
-  **Ethernet communication** for platform-independent control
-  **CAN-FD communication** between motor modules
-  **Renode simulation** for development and testing
-  **Quantum QP/C++ Framework** for real-time middleware

## Hybrid C/C++ Architecture

The firmware uses a recommended hybrid approach common in embedded systems:

Language	Usage
C++	MCU modules (QP framework), middleware integration, turret control, high-level motion control, vision + path planning on Pixel 10 Pro
C	Low-level drivers, ISRs, performance-critical routines

### Architecture Layers:

```

flowchart TB
    subgraph cpp["C++ LAYER (QP/C++ Active Objects)"]
        ao["Robot::MotorCtrlAO, TurretCtrlAO, PathPlannerAO"]
        wrappers["BSP::CanFD, BSP::Uart, BSP::Pwm (C++ wrappers)"]
    end

    subgraph clayer["C LAYER (Low-Level Drivers)"]

```

```
drivers["bsp_drivers.c - Direct hardware access"]
isrs["ISRs: SysTick_Handler, CANFD0_IRQHandler, etc."]
end

cpp --> clayer

style cpp fill:#e1f5ff,stroke:#01579b,stroke-width:2px
style clayer fill:#fff3e0,stroke:#e65100,stroke-width:2px
```

## 🔧 System Architecture

**💡 Modular Design:** Ethernet-based communication allows swapping AI processing units (Pixel 10 Pro, Raspberry Pi, Jetson Nano, etc.) without firmware changes. Standard TCP/IP protocol provides platform independence.

### System Block Diagram:

```

flowchart TB
    subgraph AI ["AI PROCESSING UNIT (Modular - Hot-swappable)"]
        direction TB
        subgraph platforms []
            pixel["Google Pixel 10 Pro  
(Current)"]
            rpi["Raspberry Pi  
Compute Module  
(Future)"]
            jetson["Jetson Nano  
Xavier NX  
(Future)"]
        end

        subgraph sensors ["Hardware Sensors"]
            gps["GPS  
Fusion"]
            imu["IMU  
Fusion"]
            cam["Camera  
+ Vision"]
            tf["TensorFlow Lite  
MediaPipe"]
        end

        subgraph aiapp ["AI Application"]
            fusion["Sensor Fusion"]
            detect["Object Detection"]
            plan["Path Planning"]
        end

        sensors --> aiapp
        end

        AI -->|Ethernet (100 Mbps)  
TCP/IP or UDP  
ControlMessage @ 50 Hz  
(~1.6 KB/s)| MCU
    end

    subgraph MCU ["FRDM-MCXN947 FREEDOM BOARD (Motor Brain)  
Quantum QP/C++ Framework"]
        direction TB
        subgraph aos ["Active Objects"]
    end

```

```

        supervisor["Supervisor AO
(State Machine)"]
        ethcomm["Ethernet Comm AO
(TCP/UDP)"]
        pathplan["Path Planner
(Local)"]
        motorctrl["Motor Ctrl AO
(CAN-FD)"]
        turretctrl["Turret Ctrl AO"]
    end
end

turretctrl -->|PWM| turret["TURRET
Pan/Tilt
0x200"]
motorctrl -->|CAN-FD| motors["MOTOR MODULES
FL FR RL RR
0x100-0x103"]

style AI fill:#e8f5e9,stroke:#2e7d32,stroke-width:3px
style MCU fill:#e3f2fd,stroke:#1565c0,stroke-width:3px
style turret fill:#fff3e0,stroke:#ef6c00,stroke-width:2px
style motors fill:#fff3e0,stroke:#ef6c00,stroke-width:2px
style aiapp fill:#f3e5f5,stroke:#6a1b9a,stroke-width:2px

```

For detailed architecture documentation, see [docs/ARCHITECTURE.md](#).

## 📁 Project Structure

```

kobayashi_maru/
├── firmware/                      # Embedded firmware for FRDM-MCXN947
│   ├── src/
│   │   ├── bsp/                   # Board Support Package
│   │   ├── drivers/              # Hardware drivers
│   │   ├── subsystems/           # Robot subsystem modules
│   │   └── qp_app/
│   │       └── main.c            # Application entry point
│   ├── include/                  # Header files
│   └── config/                  # Configuration files
├── simulation/                  # Renode simulation files
│   ├── renode/                  # Platform descriptions and scripts
│   └── models/                  # Python peripheral models
└── docs/                         # Documentation
    └── ARCHITECTURE.md          # System architecture details
└── tests/                        # Test files

```

Note: AI unit applications are developed separately and communicate via Ethernet TCP/IP (see [docs/ARCHITECTURE.md](#) for protocol details)

## 💡 Hardware Requirements

### FRDM-MCXN947 Freedom Board

**Processor:** Dual Arm Cortex-M33 @ 150 MHz

**Memory:** 2 MB Flash, 512 KB RAM

#### Key Features:

- Ethernet 10/100 (or external PHY module)
- 2x CAN-FD controllers
- Multiple FlexComm (UART, SPI, I2C)
- FlexPWM for servo control

AI Processing Unit (Modular - Choose One)

#### Option 1: Google Pixel 10 Pro (*Current*)

Feature	Specification
<b>Processor</b>	Tensor G4 chip with on-device AI acceleration
<b>AI Framework</b>	TensorFlow Lite / MediaPipe for object detection and tracking
<b>Sensors</b>	GPS + IMU with 9-axis sensor fusion
<b>Camera</b>	50 MP with vision processing
<b>Connectivity</b>	USB-C to Ethernet adapter

#### Option 2: Raspberry Pi Compute Module 4 (*Future*)

Feature	Specification
<b>Processor</b>	Quad-core ARM Cortex-A72 @ 1.5 GHz
<b>Networking</b>	Built-in Gigabit Ethernet
<b>Expansion</b>	GPIO for additional sensors
<b>Software</b>	Full Linux with ROS support
<b>Cost</b>	~\$35-75 (vs \$1000 phone)

#### Option 3: NVIDIA Jetson Nano / Xavier NX (*Future*)

Feature	Specification
<b>GPU</b>	128/384 CUDA cores for acceleration
<b>AI Framework</b>	TensorRT optimized inference

Feature	Specification
<b>Networking</b>	Built-in Gigabit Ethernet
<b>Best For</b>	Advanced vision AI, multiple cameras

**Note:** All options communicate via standard Ethernet TCP/IP. No firmware changes needed to swap platforms.

## Motor Modules (x4)

- Brushless DC motors with encoders
- CAN-FD enabled ESC
- Node IDs: 0x100 (FL), 0x101 (FR), 0x102 (RL), 0x103 (RR)

## Pan/Tilt Turret

- High-torque digital servos
- Pan:  $\pm 180^\circ$ , Tilt:  $-45^\circ$  to  $+90^\circ$
- CAN-FD Node ID: 0x200

# 🚀 Getting Started

## Prerequisites

- [Renode](#) - For simulation
- ARM GCC Toolchain - For firmware compilation
- AI Unit Application - Android/Python app for chosen platform
- QP/C Framework - Real-time embedded framework
- Ethernet network (100 Mbps recommended)

## Running the Simulation

```
# Start Renode simulation
cd simulation/renode
renode robot_simulation.resc

# Connect to AI unit terminal (in another terminal)
telnet localhost 3456
```

## Building the Firmware

```
cd firmware
# Build with ARM GCC (configure toolchain first)
make
```

## 📡 Communication Protocol

**Ethernet-Based Protocol:** TCP for reliable control commands, UDP for high-frequency sensor data. Platform-independent (works with any device supporting TCP/IP).

### AI Unit → MCXN947 (Control Messages - TCP)

```
struct ControlMessage { // 32 bytes total
    uint8_t msg_type;           // 1=GPS, 2=TARGET, 3=CMD, 4=IMU
    uint8_t reserved[3];        // Alignment
    float target_x, target_y;   // Vision: Target position (meters)
    float target_distance;     // Vision: Range to target (meters)
    float heading;             // IMU: Robot orientation (degrees)
    float gps_lat, gps_lon;    // GPS: Current position
    uint8_t command;           // STOP=0, GO=1, FIRE=2, AUTO=3
    uint8_t target_class;      // Object class ID (0-255)
    uint16_t confidence;       // Detection confidence (0-1000)
}; // Sent at 50 Hz = 1.6 KB/s
```

### MCXN947 → AI Unit (Status Messages - UDP)

```
struct StatusMessage { // 24 bytes total
    uint8_t msg_type;          // STATUS=1, ACK=2, ERROR=3
    uint8_t robot_state;        // IDLE=0, MANUAL=1, AUTO=2, EMERGENCY=3
    uint16_t battery_mv;       // Battery voltage (millivolts)
    float position_x, position_y; // Odometry position (meters)
    float velocity;            // Current speed (m/s)
    uint32_t error_flags;      // Error bitfield
    uint32_t timestamp_ms;     // Milliseconds since boot
}; // Sent at 20 Hz = 480 bytes/s
```

### Network Configuration:

Device	IP Address	Protocol	Purpose
AI Unit	192.168.1.100:5000	TCP Server	Control commands
MCXN947	192.168.1.10:5001	UDP	Status broadcasts

## ⚙️ QP Framework Active Objects

### Hardware Interrupts (Highest Priority)

**Emergency Stop GPIO:** 1-2 µs response (interrupt latency: ~60-100 cycles @ 150 MHz)

- Immediately disables motors
- Hardware-level safety mechanism

### Active Objects (Software Priorities)

Active Object	Priority	Function
<b>Supervisor</b>	7	System state machine, safety coordination, heartbeat monitoring
<b>MotorCtrl</b>	6	4WD motor control via CAN-FD @ 1 kHz (time-critical)
<b>TurretCtrl</b>	5	Pan/tilt servo control, target tracking
<b>EthernetComm</b>	4	TCP/UDP communication with AI unit (platform-agnostic)
<b>SensorFusion</b>	3	Local sensor processing, position estimation
<b>PathPlanner</b>	2	Local obstacle avoidance, waypoint tracking

**Priority Rationale:** Motor control and turret positioning require real-time guarantees, while network I/O runs at lower priority to prevent jitter in control loops.

**Note:** High-level sensor fusion and AI processing handled on external AI unit.

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