## ADSP\_HW5

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(1)

## 1. matlab code:

```
A=ones(N,N);
% find A
product=1;
for k=1:N-1
     product=product*alpha;
     for n=1:N-1
     A(k+1,n+1)=mod(product*A(k+1,n),M);
end
% find N^-1
N_1=0;
bool=0;
while bool==0
      N_1=N_1+1;
      if mod(N_1*N,M)==1
      break;
end
% find α^-1
alpha_1=0;
bool=0;
while bool==0
      alpha_1=alpha_1+1;
      if mod(alpha_1*alpha,M)==1
      break;
end
B=ones(N,N)*N_1;
% find B product=1;
for k=1:N-1
      product=product*alpha_1;
      \begin{array}{ll} & \text{B(k+1,n+1)=mod(product*B(k+1,n),M);} \\ & \text{end} \end{array}
```

## 2. sample result

```
>> run
Α:
      1
             1
                     1
                            1
      1
             2
                     4
                            3
      1
             4
                     1
                            4
      1
             3
                            2
В:
      4
             4
                     4
                            4
      4
             2
                     1
                            3
      4
             1
                     4
                            1
      4
             3
                     1
                            2
(A*B) \mod M:
      1
                     0
                            0
      0
                     0
                            0
             1
      0
             0
                     1
                            0
      0
                            1
      = I
```

## (2)

- 1. The complexity of computation is in linear time.
- 2. The hardware architecture is fixed for different input signal length.

(a) 
$$W_2 = 2$$
  
 $W_4 = 2^*W_2 + 4 = 8$   
 $W_8 = 2^*W_4 + 8 = 24$   
 $W_{16} = 2^*W_8 + 16 = \underline{64}$   
 $W_{32} = 2^*W_{16} + 32 = \underline{160}$ 

(b) 
$$h[6] = f[0]g[6\oplus 0] + f[1]g[6\oplus 1] + f[2]g[6\oplus 2] + f[3]g[6\oplus 3] + f[4]g[6\oplus 4] + f[5]g[6\oplus 5] + f[6]g[6\oplus 6] + f[7]g[6\oplus 7]$$
  
=  $f[0]g[6] + f[1]g[7] + f[2]g[4] + f[3]g[5] + f[4]g[2] + f[5]g[3] + f[6]g[0] + f[7]g[1]$ 

- **(4)**
- (a) CDMA (code division multiple access)
- (b) Adaboost face detection (extract local feature)
- (5)
- (c) Integer LTI system analysis:Since NTT is appropriate for convolution, and it is in integer field.
- (d) Encryption:

Since it is difficult to predict the mapping of NTT and the computation for modern cryptosystem can be speeded up by NTT.

- (6)
- (1) OFDM is orthogonal. There is no interference between different channels.  $A^T = A^{-1} \rightarrow AA^T = A^TA = I$ .
- (2) OFDM has fast algorithm, which is similar to DFT.

  Since it involves IFFT-FFT operations, it is simple for us to implement it.
- (7)
  (a)

(b) No, it is not better.

Since the computation of NTT is much more complicated.

-1, 1, -1, -3, -1, -3, -1, 1, -1, -3, -1, 1, -1, 1, -1, -3]