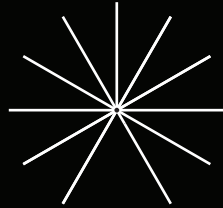
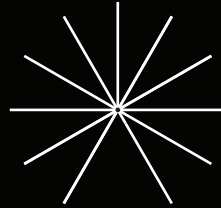


# Sample And Hold

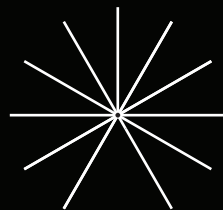
Shape



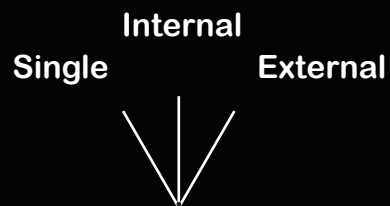
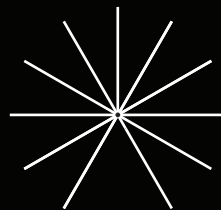
DC Trim



Internal Sample Rate



External Threshold



Single Sample



External Clock In



Input



Output

