

# Operating System hw4 part 1 B103040009 尹信亨

1. NRU replaces page 0. ( $R=0, M=0$ )  
FIFO replaces page 2. (最早 loaded)  
LRU replaces page 1. (最近 reference)  
Second Chance replaces page 0. (loaded at 260,  $R=0$ )
2. Page D. 來的順序:  $N \rightarrow G \rightarrow H \rightarrow D \rightarrow A \rightarrow L \rightarrow C \rightarrow B$   
reference: 1 1 1 0 1 1 0 0  
bit
3. Real memory uses physical addresses. Physical addresses are the numbers that the memory chip react to on the bus.  
Virtual addresses are the logical addresses that refer to a process' address space.
4. No. Only in the data structure used to keep track of the pages that they differ.
5. Page 0: 01101110  
1: 01001001  
2: 00110111  
3: 10001011