# MICAH ZENON UMPA GALOS

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## EDUCATION

**University of California, Riverside**  — Riverside, CA
*Bachelor of Science in Computer Engineering* — *Jan. 2019 – Dec 2021*

**Riverside City College** — Riverside, CA
*Associate of Science in Computer Science and Mathematics* — *Aug. 2014 – Dec 2018*

## RELEVANT COURSEWORK

- **Undergraduate**: Logic Design, Design & Architecture of Computer Systems, and Intro to VLSI Design, Design of Operating Systems, Concurrent Programming & Parallel Systems, Intro to Embedded Systems, and Sensing & Actuation for Embedded Systems, Intro to Machine Learning & Data Mining, Electronic Circuits, Solid-State Electronics, and Compiler Design

## JOB EXPERIENCE

**Micro Center** — Tustin, CA
*Customer Service Representative* — *October 2022 – Present*

- Cooperated with fellow CSRs and Front-End Leads in maintaining a fast-paced environment–performing accurate sales transactions among across forms of payment.
- Optimized transaction routines in operating the POS register to decreasing wait times between each customer's item returns and purchases.
- Consulted customers by offering protection plans for products being purchased to ensure product replacement and customer satisfaction.
- Maintained clean work station for each POS register in accordance to the area of responsibility policy.

## ACADEMIC PROJECTS

**University of California, Riverside** — Riverside, CA

**4-Way Traffic Light** | *Xilinx ISE, Verilog*

- Developed the behavioral module code for the FPGA board to simulating a four-way traffic light.
- Designed the state diagram to simulate the behavior when a button is pressed during moving traffic.
- Created the circuit schematic to show the connection between controller and datapaths into the FPGA
- Implemented test benches to verify button presses at different timings of traffic.

**Linux Shell Emulator** | *Vim, Linux, Git, C++*

- Developed a Linux based terminal emulator designed to run various Linux commands.
- Implemented unit tests to check working functions using the Google Mock Test.
- Implemented integration tests to check the entire program on Linux via bash scripts of linux commands contained in text files.

**Compiler: Java to MIPs** | *VSCode, Linux, Java*

- Composed of tasks in type-checking, code generation, register allocation, and instruction selection
- Expanded code on existing visitor classes to parse and generate the compiler's syntax tree.
- Implemented integration tests of various Java programs in stressing the program's implementation.

## TECHNICAL SKILLS

**Programming Languages**: C/C++, Java, Verilog, Python
**Tools**: Git, VSCode, Vim, Xilinx ISE, Synopsys Design Tool, Cadence
**Technologies**: Arduino, Raspberry Pi