Last Updated April 17, 2022

MICAH GALOS

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Links

LinkedIn **GitHub**

EDUCATION

Riverside, CA **University of California** **January 2019 – December 2021**
B.S. in Computer Engineering

- **Undergraduate Coursework**: Operating Systems; Comp Sys. Architecture; Embedded Systems; VLSI Design; Logic Design; Software Construction; Compiler Design Senior Project; Machine Learning and Data Mining;

Riverside, CA **Riverside City College** **August 2014 – December 2019**

- A.S. in Computer Science and Mathematics

WORK EXPERIENCE

Volunteer **Kaiser Permanente** **June 2013 – June 2014**
Post Anesthesia Care Unit

- In-person informational computer front desk assistance.
- Cooperated with nurses and secretaries managing schedules and moving medical equipment.

ACADEMIC PROJECTS

Riverside, CA **University of California** **January 2019 – December 2021**

Four-Way Traffic Light - Verilog

- Two-Person Team
- Implemented and co-designed utilizing an FPGA programmable board to simulate a traffic light in a four-way stop.
- After a few seconds, the direction of traffic changes when an LED signal is shown.

Remote Shell for Linux – C++

- Two-Person Team
- Co-designed a Linux based terminal emulator designed to run programs given Linux command inputs.
- Each input is a potential Linux command which is checked to be syntactically correct and perform its instruction.

Solar Tracker – C

- Two-Person Team
- Implemented and co-designed by utilizing a FRDM-K64F development board to program a sensor to face where the sun emits the highest amounts of light via photoresistors and servomotors.

Java to MIPs Compiler - Java

- Two-Person Team
- Implemented and co-designed a Java to MIPs assembly-language compiler in four phases comprised of type-checking, code generation, register allocation, and instruction selection.
- Compiler takes in Java output files as an input and checks the file is syntactically correct to be runnable code.

TECHNICAL SKILLS

Programming Languages: C++; C; Java; Python; Verilog;
Technologies: Vim; Visual Studio Code; Kinetis Design Studio; Xilinx Vivado; Synopsys Custom Design;
Machine Learning Technologies: Pandas; NumPy; Sklearn; Keras