Micah Zenon Umpa Galos

951-525-9665 | micahzugalos@gmail.com | linkedin.com/in/micahgalos/ | github.com/micahgalos

Education

University of California, Riverside — Riverside, CA

Bachelor of Science in Computer Engineering — *Jan. 2019 – Dec 2021*

- Logic Design, Design & Architecture of Computer Systems, and Intro to VLSI Design, Design of Operating Systems, Concurrent Programming & Parallel Systems, Intro to Embedded Systems, and Sensing & Actuation for Embedded Systems, Electronic Circuits, Solid-State Electronics, and Compiler Design

Riverside City College — Riverside, CA

Associate of Science in Computer Science and Mathematics — *Aug. 2014 – Dec 2018*

- Program Logic Using C++, C++ Programming: Objects, Java Programming: Objects, Discrete Structures

Technical Skills

Coding Languages: C, C++, Java, Verilog, Python

Tools: Git, VSCode, Vim, Xilinx ISE, Synopsys Design Tool, Cadence

Technologies: Arduino, Raspberry Pi

Job Experience

Micro Center — Tustin, CA

Customer Service Representative/Cashier (CSR) — *October 2022 – Present (40 Hours/Week)*

- Assisted shadowing new hires to become efficient utilizing the cash register's POS system.
- Provided leadership support to supervisors in assisting cashiers to ensure efficient customer checkout times.
- Oversaw online tickets for return policies, transaction inquiries and concerns through the store's zendesk portal.
- Administered excellent customer service at customer checkout by providing assistance on inquiries and concerns.
- Maintained a positive working environment at the front end department for the safety of fellow CSRs and supervisors.
- Performed daily routines such as merchandise restocking, cleaning POS stations, inventory cycle counting, and sales counting amongst CSRs.

Course Projects

University of California, Riverside — Riverside, CA

4-Way Traffic Light | *Verilog, Windows*

- Developed the behavioral modules to the FPGA board to ensure four-way traffic light simulation.
- Designed state diagrams to describe behaviors for when a signal is interrupted to the system.
- Created circuit schematics visualizing the connection between controller and data paths from the FPGA.
- Implemented test benches to verify signal presses at different timings of traffic to ensure proper traffic flow.

Terminal Emulator | *C++, Linux*

- Developed an assignment board of tasks to ensure a productive workflow without conflicts during program development.
- Developed a Linux based terminal emulator designed to run various Linux commands.
- Implemented unit tests to verify working functions via the Google Test framework.
- Implemented integration tests to verify the program functions through a variety of known Linux commands.

Compiler: Java to MIPs | *Java, Windows*

- Remotely communicated with team in creating structure and program's development plan.
- Assign tasks ranging from small and big priorities to maintain productivity in developing each feature of the program.
- Composed of tasks in type-checking, code generation, register allocation, and instruction selection
- Expanded code on existing visitor classes to parse and generate the compiler's syntax tree.
- Implemented integration tests of various Java programs in stressing the program's implementation.