

MICAH GALOS

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EDUCATION

University of California, Riverside

Bachelor of Science in Computer Engineering

Riverside, CA

Jan. 2019 – Dec 2021

Riverside City College

Associate's in Computer Science and Mathematics

Riverside, CA

Aug. 2014 – Dec 2018

EXPERIENCE

Cashier

Micro Center

Tustin, CA

October 2022 – Present

- Cooperating with fellow cashiers and front end leads in maintaining a fast-paced environment in performing accurate sales transactions.
- Optimizing transaction routines with POS system to decrease times between item returns and purchases for every customer.
- Consulting with customers by offering protection plans for products being purchased to ensure product replacement and customer satisfaction.

Volunteer

Kaiser Permanente

Riverside, CA

June 2013 – June 2014

- Front desk information assisting visitors with questions and directions on mornings and afternoon.
- Shows positive attitude towards co-workers and hospital visitors for six hours on the day-to-day.
- Cooperated with recovery room nurses and secretaries with checking-in patients for surgery.
- Answered to phone calls from nurses and secretaries to bring in patients into the recovery room for surgery.

PROJECTS

University of California, Riverside

Riverside, CA

Jan. 2019 – Dec 2021

4-Way Traffic Light | Verilog

- Co-developed a program to communicate with an FPGA board in simulating a traffic light.
- Co-designed state machine diagrams to simulate incoming traffic via button input signal.
- Created signal edge test benches to observe button inputs at different timings.

Linux Shell Emulator | C++

- Co-developed a Linux based terminal emulator designed to run various Linux commands.
- Created unit tests to test various instances of functions using the Google Test library for object errors.
- Created integration tests on to test entire system—automated using bash scripts with text files of Linux commands as inputs.

Solar Panel Tracker | C

- Co-developed in programming a panel sensor to face where the highest amount of light is emitted from the sun via servomotors.
- Utilizes a FDRM-K64F board for the embedded software communication and input readings.

Compiler: Java to MIPS | Java

- Lead group and co-developed a compiler which reads Java programs and interprets into MIPS assembly code..
- Split into four phases: type-checking, code generation, register allocation, and instruction selection
- Created integration tests of various java programs to test for any implementation errors.

PROGRAMMING LANGUAGES & TOOLS

Languages: C/C++, Java, Verilog, Python

Developer Tools: Git, VS Code, Visual Studio, Vim, Xilinx Vivado, Kinetis Design Studio

Productivity: Notion, Trello

Communication: Slack, Zoom, Microsoft Teams, Discord

Libraries: pandas, NumPy, Matplotlib, sklearn, keras