MICAH GALOS

951-525-9665 | Email | Linkedin | Github | Website

EDUCATION

University of California, Riverside — Riverside, CA
Bachelor of Science in Computer Engineering — *Jan. 2019 – Dec 2021*

- Operating Systems, Comp Sys. Architecture, Embedded Systems, VLSI Design, Logic Design, Software Construction, Compiler Design Senior Project, Concurrent Programming and Parallel System, Machine Learning and Data Mining

Riverside City College — Riverside, CA
Associate's in Computer Science and Mathematics — *Aug. 2014 – Dec 2018*

EXPERIENCE

Volunteer — June 2013 – June 2014
Kaiser Permanente — *Riverside, CA*

- Front desk information assisting visitors with questions and directions to their desired location.
- Cooperated with recovery room nurses and secretaries when checking-in family members of the patient post-surgery.
- Bringing abandoned wheelchairs and medical equipment back to their designated location.

PROJECTS

University of California, Riverside — Riverside, CA
Jan. 2019 – Dec 2021

4-Way Traffic Light | *Verilog*

- Co-developed a program to communicate with an FPGA board in simulating a traffic light.
- Co-designed a high-level state machine diagram to simulate incoming traffic via button input signal.
- Created signal edge test benches on Xilinx Vivado to observe button inputs at different timings.

Linux Shell Emulator | *C++*

- Co-developed a Linux based terminal emulator designed to run various Linux commands.
- Created unit tests to test various instances of functions using the Google Test library for object errors.
- Created integration tests on to test entire system–automated using bash scripts with text files of Linux commands as inputs.

Solar Tracker | *C*

- Co-developed in programming a panel sensor to face where the highest amount of light is emitted from the sun via servomotors.
- Utilizes a FDRM-K64F board for the embedded software communication and input readings.
- Tested system at different times of the day to ensure panel sensors were facing at the correct position of the sun.

Java to MIPs Compiler | *Java*

- Lead group and co-developed a compiler which reads Java programs and interprets into MIPs assembly code..
- Split into four phases: type-checking, code generation, register allocation, and instruction selection
- Created integration tests of various java programs to test for any implementation errors.
- Increased productivity by assigning "TO-DO" tasks to implement certain functions of the program based on team's strengths and weaknesses.
- Created project reports to detail the scope of each phase and show which functions were implemented or changed.

TECHNICAL SKILLS

Languages: C/C++, Java, Verilog, Python
Developer Tools: Git, VS Code, Visual Studio, Vim, Xilinx Vivado, Kinetis Design Studio
Productivity: Notion, Trello
Communication: Slack, Zoom, Microsoft Teams, Discord
Libraries: pandas, NumPy, Matplotlib, sklearn, keras