MICAH ZENON UMPA GALOS

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EDUCATION

Bachelor's of Science in Computer Engineering Riverside, CA
University of California, Riverside *Jan. 2019 – Dec 2021*

Relevant Coursework:

  Computer Architecture: Logic Design, Design & Architecture of Computer Systems, and Intro to VLSI Design
  Operating Systems: Design of Operating Systems, Concurrent Programming & Parallel Systems
  Embedded Systems: Intro to Embedded Systems, and Sensing & Actuation for Embedded Systems
  Electives: Intro to Machine Learning & Data Mining, Electronic Circuits, Solid-State Electronics, and Compiler Design

Associate's in Computer Science and Mathematics Riverside, CA
Riverside City College *Aug. 2014 – Dec 2018*

JOB EXPERIENCE

Customer Service Representative Tustin, CA
Micro Electronics Inc *October 2022 – Present*

- Cooperated with fellow CSRs and Front-End Leads in maintaining a fast-paced environment–performing accurate sales transactions among across forms of payment.
- Optimized transaction routines in operating the POS register to decreasing wait times between each customer's item returns and purchases.
- Consulted customers by offering protection plans for products being purchased to ensure product replacement and customer satisfaction.
- Maintained clean work station for each POS register in accordance to the area of responsibility policy.

ACADEMIC PROJECTS

University of California, Riverside Riverside, CA
Jan. 2019 – Dec 2021

4-Way Traffic Light | *Verilog*

- Co-developed a program to communicate with an FPGA board in simulating a traffic light.
- Co-designed state machine diagrams to simulate incoming traffic via button input signal.
- Created signal edge test benches to observe button inputs at different timings.

Linux Shell Emulator | *C++*

- Co-developed a Linux based terminal emulator designed to run various Linux commands.
- Created unit tests to test various instances of functions using the Google Test library for errors in OOP design.
- Created integration tests on to test entire system–automated using bash scripts with text files of Linux commands as inputs.

Compiler: Java to MIPs | *Java*

- Composed of tasks in type-checking, code generation, register allocation, and instruction selection
- Expanded code on existing visitor classes to parse and generate the compiler's syntax tree.
- Created integration tests of various Java programs to test for any implementation errors.

PROGRAMMING LANGUAGES & TOOLS

Languages: C/C++, Java, Verilog, Python
Developer Tools: Git, VSCode, Xilinx Vivado, Synopsys Design Tool
Productivity: Notion
Communication: Slack, Zoom, Microsoft Teams, Discord